

## IPMI Firmware / BIOS Release Notes Form

*Supermicro disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade. All products, computer systems, dates, and figures specified are preliminary based on current expectations, and are subject to change without notice. Supermicro and the Supermicro logo are trademarks of Super Micro Computer, Inc. in the U.S. and/or other countries. Copyright © 2018 Super Micro Computer, Inc. All rights reserved.*

<b>Product Name</b>	<b>H12SSFR-AN6</b>
<b>Release Version</b>	<b>2.5</b>
<b>Release Date</b>	<b>10/12/2022</b>
<b>Previous Version</b>	<b>2.4</b>
<b>Update Category</b>	<b>Recommended</b>
<b>Dependencies</b>	<b>N/A</b>
<b>Important Notes</b>	BIOS Image: BIOS_H12SSFR-1C14_20221012_2.5_STDsp.bin BIOS Update Package: BIOS_H12SSFR-1C14_20221012_2.5_STDsp.zip
<b>Enhancements</b>	1. Update MilanPI to 1.0.0.9 based on 5.22_MilanCrb_0ACOU020. 2. Remove "Vendor Keys" in security page. 3. Modify String naming from SMCI to Supermicro. 4. Update DBX file to fix Secure Boot Bypass issue. 5. Update AGESA RomePI to 1.0.0.E based on 5.14_RomeCrb_0ACMK025.
<b>New features</b>	<b>N/A</b>
<b>Fixes</b>	<b>N/A</b>

## Release Notes from Previous Release(s)

### **2.4 (04/22/2022)**

1. Configuration backward compatible issue in 01.02.40
2. Wrong Inlet Temp reading on NM command.
3. AMD BH12SSi platform unexpected throttling when enable Smart Power in CMM
4. Update network hostname format
5. The readings style of "Power Consumption" graph should be use radio button.
6. Cannot set the SNMP IP as 0.0.0.0 on X12DGQ-R even though the BMC WEBUI's SNMP destination address setting has already been changed to 0.0.0.0s

### **2.3 (10/21/2021)**

1. [Rome/Milan BIOS] Change BIOS revision to 2.3.
2. [Rome BIOS] Update AGESA RomePI to 1.0.0.C.
3. [Rome BIOS] Exposed Setup item "Enhanced Preferred IO Mode".
4. [Milan BIOS] Update AGESA MilanPI to 1.0.0.6.
5. [Rome/Milan BIOS] Patch BMC Redfish Host Interface was named as ethX when CDN was disabled case under Linux OS.
6. [Rome/Milan BIOS] Disable EFI iSCSI support.
7. [Milan BIOS] Exposed Setup items "BankGroupSwapAlt", "SEV-SNP Support", "Enhanced Preferred IO Mode" and "Root Complex 0x00~0xE0 LCLK Frequency".
8. [Rome/Milan BIOS] According to PM's request, change default of "Wait For "F1" If Error" to Disable.
9. Update Milan B0/B1 stepping CPU microcode and Milan-X B2 stepping CPU microcode to patch Erratum #1381 Processor May Hang When Coherency Probe Hits Instruction Cache Line While Evicted.  
B0 Milan microcode 0x0A00104C, B1 Milan microcode 0x0A001143, B2 Milan-X microcode 0x0A001223.

### **2.2 (9/3/2021)**

1. [Rome/Milan BIOS] Change BIOS revision to 2.2.
2. [Rome/Milan BIOS] Add Redfish/SUM Secure Boot feature, update OOB for secure boot and reserve Key.
3. [Rome/Milan BIOS] Support SUM upload/delete HTTPS TLS certificate. (Default Enabled by TOKEN "Sum\_UploadTlsKey\_SUPPORT")
4. [Rome/Milan BIOS] Per AMD's suggestion, set Relaxed Ordering default to Enabled.
5. [Milan BIOS] Update AGESA MilanPI to 1.0.0.4.
6. [Milan BIOS] Update A010 GN B0 microcode 0A001046.
7. [Rome BIOS] Update AGESA RomePI to 1.0.0.C.
8. [Milan BIOS] Fix SUM cannot modify AMD CBS settings.
9. [Milan BIOS] Fix IPMI Web GUI No Sensor info

### **2.0 (03/12/2021)**

1. [Rome/Milan BIOS] Change BIOS revision to 2.0.
2. [Milan BIOS] Update AGESA MilanPI to 1.0.0.1.

3. [Milan BIOS] Update A011 GN B1 microcode 0A001119.
4. [Rome BIOS] Update AGESA RomePI to 1.0.0.A.
5. [Rome/Milan BIOS] Fix IPV6 disable in the IPMI GUI but BIOS initialize will appear IPV6 address.
6. [Rome/Milan BIOS] Per Platform Management FRU Information Storage Definition v1.1 to enhance SMBIOS Type39 System Power Supply information.
7. According to PM request, disabled NVDIMM support.

### **1.3 (02/08/2021)**

1. Implemented AH25G-m2S2TM Intel X550 Lan Drop recovery scheme
2. Issue 120294, AOC-AH25G-m2S2TM system information incorrect MAC address for 25G

### **1.0 (12/28/2020)**

First release.