

BIOS Release Notes Form

Supermicro disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade. All products, computer systems, dates, and figures specified are preliminary based on current expectations, and are subject to change without notice. Supermicro and the Supermicro logo are trademarks of Super Micro Computer, Inc. in the U.S. and/or other countries. Copyright © 2018 Super Micro Computer, Inc. All rights reserved.

Product Name	X11DAI-N
Release Version	3.8b SPS: 04.01.04.901
Build Date	01/12/2023
Previous Version	3.8a
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	1. Updated AMI label 5.14_PurleyCrb_0ACLA058 for RC0626.P01 2023.1 IPU. 2. Updated Skylake-SP/Cascade Lake-SP CPU PC microcode for IPU 2023.1. 3. Updated token RC_VERSION_VALUE setting to 626.P01. Updated token PRICESSO_2_UCODE_VERSION setting to 04003303. Updated token PRICESSO_3_UCODE_VERSION setting to 05003303. Updated token FW_SPS_VERSION setting to 4.1.4.901. 4. Updated RSTe VMD/SATA/sSATA driver to v7.8.0.1012.
New features	None
Fixes	None

Release Notes from Previous Release(s)

3.8a (10/27/2022)

1. Changed BIOS Revision to 3.6.
2. Updated AMI label 5.14_PurleyCrb_OACLA057 for RC0623.D09 2022.3 IPU to address INTEL-SA-00688.
3. Updated AEP uEFI driver to 01.00.00.3536 for IPU2022.3.
4. Updated Skylake-SP/Cascade Lake-SP CPU PC microcode for IPU 2022.2.
5. Modified String naming from SMCI to Supermicro.
6. Updated BIOS ACM Firmware v1.7.54 and SINIT ACM Firmware v1.7.55 PW for IPU2022.1 to address Intel-TA-00161: CVE-2021-0159 (7.8 High) CVE-2021-33123 (8.2 High) and CVE-2021-33124 (7.5 High).

3.6 (01/24/2022)

1. Changed BIOS Revision to 3.6.
2. Updated 5.14_PurleyCrb_OACLA054 for RC update and 2021.2 IPU PV.
3. Updated Skylake-SP/Cascade Lake-SP CPU PC microcode from IPU2021.2.
4. Updated SPS 04.01.04.601.
5. Update Intel BIOS ACM Firmware to v1.7.51 and SINIT ACM Firmware to v1.7.51.

3.4 (11/10/2020)

1. Changed BIOS Revision to 3.4.
2. Updated 5.14_PurleyCrb_OACLA052 for RC update and 2020.2 IPU PV to address Intel-TA-00358: CVE-2020-0587 (6.7, Medium), CVE-2020-0591 (6.7, Medium), and CVE-2020-0592 (3, Low) security issues, and Intel-TA-00390: CVE-2020-0593 (4.7, Medium), CVE-2020-8738 (7.5, High), CVE-2020-8739 (4.6, Medium), CVE-2020-8740 (6.7, Medium), and CVE-2020-8764 (8.7, High) security issues.
3. Updated Skylake-SP/Cascade Lake-SP CPU PV microcode from IPU 2020.2 PV address Intel-TA-00381: CVE-2020-8696 (2.5, Low) security issue and MD_Clear Errata, MOB Speedpath, and IRR Restore with RS Throttle (ITR #2).
4. Updated SPS 4.1.4.423 to address Intel-TA-00391: CVE-2020-8755 (4.6, Medium) and CVE-2020-8705 (7.1, High) security issues.
5. Updated Intel BIOS ACM Firmware to v1.7.41 (20200406) and SINIT ACM Firmware to v1.7.49 (20200406).

3.3 (2/26/2020)

1. Changed BIOS Revision to 3.3.
2. Updated Intel Server Platform Services 4.1.4.381 for Purley-Refresh Platforms.
3. Updated Intel Reference Code to IPU2020.1 Rev: RC0602.D02.
4. Updated Intel BIOS ACM Firmware to v1.7.40(20190909) and SINIT ACM Firmware to v1.7.48(20191029).
5. Changed patrol scrub from uncorrectable to correctable errors.
6. Added support for SMC HDD Security password erase and reset.

7. Updated Skylake-SP/Cascade Lake-SP CPU microcode from Intel-Restricted-2020-1-IPU for Intel-SA-00329 (CVE2020-0548 2.8 Low, CVE2020-0549 6.5 Medium).

8. Added Driver Health support.

3.2 (11/13/2019)

1. Updated BIOS version to 3.2.

2. Updated Intel Skylake-SP H0 Microcode to 02000065.

3. Updated Intel Cascadelake-SP B0 Microcode to 0400002C.

4. Updated Intel Cascadelake-SP B1 Microcode to 0500002C.

5. Updated Intel Server Platform Services 04.01.04.339.0 for Purley-Refresh Platforms.

6. Displayed third revision number for IPMI Firmware.

7. Updated Intel BIOS ACM Firmware to v1.7.3 (20190712) and SINIT ACM Firmware to v1.7.45 (20190710).

8. Updated Intel Reference Code to BKC WW36 Rev: RC0595.D04.

9. Disabled ADDDC/SDDC and set PPR as hPPR.

10. Added Enhanced PPR function and set disabled as default.

11. Fixed problem of F12 PXE boot causing DMI data to return to default value.

3.1a (6/11/2019)

1. Changed BIOS Revision to 3.1a.

2. Fixed problem of DMI table Type 2 AssetTag field containing with SerialNumber.

3.1 (5/3/2019)

1. Changed BIOS Revision to 3.1.

2. Updated Intel Reference Code to BKC WW16 Rev: RC0584.D01.

3. Updated Intel Xeon microcode for Skylake-EP to Rev. 5E for Intel Xeon Skylake-EP H0/H0-QS.

4. Added Cascade Lake Server B0 Stepping CPU Support with Rev. 24 for Intel Xeon Scalable Cascade Lake Server B0-QS.

5. Added Cascade Lake Server B1 Stepping CPU Support with Rev. 24 for Intel Xeon Scalable Cascade Lake Server B1-QS.

6. Updated Intel Server Platform Services 04.01.04.296.0 for Purley-Refresh Platforms.

7. Updated Intel BIOS ACM firmware to v1.7.1 and SINIT ACM firmware to v1.7.2.

8. Updated RSTe VMD/SATA/sSATA driver to v6.1.0.1017.

9. Fixed problem of SKX/CLX 4xxx SKU CPU causing memory training error with Micron RDIMM (18ASF2G72PDZ-2G6E1).

10. Fixed malfunction of hotkey F12 when switching onboard LAN Option ROM to EFI.

3.0a (3/06/2019)

1. Added support for Purley Refresh platform.

2. Changed BIOS revision to 3.0a.

3. Updated Intel Reference Code 2019WW08 Rev: RC0576.D20.

4. Updated Intel Xeon Micro-code for Skylake-EP.

5. Updated Intel BIOS ACM Firmware to v1.7.1 and SINIT ACM Firmware to v1.7.2.

6. Updated RSTe VMD/SATA/sSATA drivers to v6.0.0.1024.

7. Enabled RFC4122 UUID support.

8. Added support for Tesla T4 GPU.

9. Updated BIOS Setup Template to v0.7.
10. Fixed malfunction of Intel SST-BF feature.

2.1 (7/3/2018)

1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.
2. Changed BIOS revision to 2.1.
3. Enabled the Jumperless BIOS Flash feature.
4. Set Descriptor Region of BIOS Region Write Access to "No".

2.0b (2/28/2018)

1. Implemented enhancement to address 'Spectre' variant 2 (CVE 2017-5715) security patch issue.
2. Changed BIOS revision to 2.0b.
3. Added maximum performance option under Energy Performance on BIOS setup page.
4. Removed BIOS time stamp from main BIOS setup page.
5. Prevented certain corner cases that cause ME update to fail when using AFUEFI tool.
6. Fixed inability of certain SKX CPUs to display the microcode version correctly on BIOS setup page.
7. Fixed inability to use OOB SUM to get current BIOS configuration file.
8. Fixed problem of system hanging at POST code "91" after BIOS update with EFI shell command "reset".

Product Manager

Date