# **IPMI Firmware Release Notes Form**

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<b>Product Name</b>	H13DSH
<b>Release Version</b>	01.00.14
<b>Build Date</b>	02/10/2023
<b>Previous Version</b>	01.00.12
<b>Update Category</b>	Critical
Dependencies	BIOS:1.2 (BIOS_H13DSH-1C84_20230209_1.2_STDsp)
Important Notes	BMC image:
	BMC_H13AST2600-ROT-1401MS_20230210_01.00.14_STDsp.bin
	BMC image and flash utility package:
	BMC_H13AST2600-ROT-1401MS_20230210_01.00.14_STDsp.zip
	Please update with BIOS:1.2 with CPLD:F5.0F.19 and contact our technical support for any inquiries about CPLD.
Enhancements	1. Improved the MB CPLD Jtag port to be locked.
New features	N/A
Fixes	1. Fixed the problem of failing to read the correct power status when
	an H13 system was not installed with an AMD CPU.
	2. Fixed the issue in which MELL Log msg was incorrect when only the
	Dynamic option was enabled.

## Release Notes from Previous Release(s)

# 01.00.12 (2023/01/17)

[Enhancement]

- 1. Improved the problem of the SMBUS reading and writing the NIC card.
- 2. Added short-term solution to improve NVMe eject/insert glitches.

[Fixes]

## Fixed the following issues:

- 1. The PSU failure LED is ON when CPU temp reaching sensor threshold. The PSU failure LED is blinking when removing PSU module. The PSU failure LED is blinking when removing FAN module.
- 2. Missing SEL logs "[PWR-0000] PS Status, Power Supply Installed Assertion" after system power on with IPMI 01.00.08
- 3. IPMI Web help page link confirm and Web help page link confirm.
- 4. Deploy OS test fail (error message:Failed to mount iso on 10.184.21.36) on the H13SSL-NT and executed the diagnose system under SSM v5.4.
- 5. MCX75510AAS-HEAT ands MCX653105A-ECAT not detected NAOC info on BMC Web.
- 6. Call home trigger item (temp:Local Administrator Setting) no match SSM design on the H13SSL-NT.
- 7. There are duplicate BMC events when triggering CPU temp sensor.
- 8. Enabled the "Task Message" when BIOS firmware verify and updated completed.
- 9. Failed in IPMIECO TC0002, TC0019, TC2015 and error message "Cannot establish SSL connection."
- 10. When DIMM is inserted into position G1~F1, the incorrect DIMM name is displayed.

#### **01.00.06 (**2022/11/15**)**

Initial Release