BIOS Release Notes Form

Product Name	**X13SET-G**
Release Version	**1.1a**
Release Date	**03/03/2023**
Build Date	**03/03/2023**
Previous Version	**1.0**
Update Category	**Recommended**
Dependencies	None
Important Notes	None
Enhancements	1. **Updated Intel BKC 2023_WW07, Intel RC Version 9409.P19.** 2. **Exposed and adjusted the MMRS item to fine tune Gen5 performance.** 3. **Fixed the FWTS and WHQL fail issue.** 4. **Used Supermicro's defined GUID for compatibility between SuperBIOS and AMI.** 5. **Fixed Type 17, which did not follow SPD Spec MemoryChannelBusWidth.** 6. **Exposed the CPU feature, "Optimized Power Mode."** 7. **Updated the SATA SGPIO Mode option strings for all SATA configuration menus.** 8. **Followed BIOS Setup Template v1.1 to hide the "UPI3" feature in UP projects.** 9. **Hid the "Homeless Prefetch" feature.**

		10. Updated SPS 6.0.4.25 2S PV Hotfix Release for EagleStream platforms.
		11. BIOS POST Progress Support requirement in Redfish.
		12. Followed BMC_Network_AOC_Monitoring_Spec_v2.2 to avoid sending the AIOM/AOC IO Module LAN MAC to BMC.
		13. Removed the "Supermicro Security erase" page from HII format.
		14. Followed the setup template to expose the Consistent Device Name Support" feature when SMC_CONSISTENT_DEVICE_NAME_SUPPORT is enabled.
		15. Updated the AST2600 VBIOS and VGA UEFI driver to 1.13.01.
		16. Created UEFI OS name on the boot option for removable devices.
		17. Converted "Supermicro Security Erase Configuration" page to JSON format.
		18. Added SecureBoot Database to meet the Redfish specifications.
		19. Created SmcKMSOOB to prepare the VFR/UNI and to create the ConfigAccess Protocol which will be used when transferring files to JSON format. Commented out the extra "Supermicro KMIP Form" when using the SUM utility to generate BIOS config file.
		20. Transferred Supermicro HTTP BOOT MAIN" page (HTTPS TLS certificate upload/delete via SUM/BMC) from HII to JSON format.
		21. Made Enable the default TOKEN "SMC_HECI_HIDE_DELAY"(HECI-x hide delay) for all projects to fix BSOD issue.
		22. Exposed the "Equalization Bypass To Highest Rate" feature.
		23. Automatically hid the ADDDC Sparing feature with x8 width DIMMs.

	24. Exposed "CXL Type 3 Legacy", "CXL Security Level" and "CXL Header Bypass" features for CXL.
	25. Revised the code for NVMe PCC init.
	26. Updated PCIe riser card support to include CBL-ASMC-1321DM5YP.
	27. Enhanced the BIOS setup string and SMBIOS for different system configurations.
	28. Fixed the issue where M.2 devices did not appear when setting up the VMD mode from PCH.
	29. Enhanced front IO naming on BIOS setup.
	30. Enhanced SMBIOS data when installing AIOM card.
	31. Swapped the Slot ID for front IO AIOM slot.
	32. Enhanced reporting SMBIOS type40 data.
New features	N/A
Fixes	1. Fixed the WHQL HSTI Rollback firmware error.
	2. Fixed HTTP/HTTPS boot failed.
	3. Fixed system hang at 0xEE if the COM1/COM2 console redirection is disabled.
	4. Updated binaries for SMCOOBV2.00.17.
	5. Fixed the memory serial number of SMBIOS type 17, which did not match the memory DIMM bar code.
	6. Fixed the NVMe hotplug malfunction with BPN-NVMe5-F418-B6S6.
	7. Masked ASPEED onboard VGA PCIe correctable error.
	8. Corrected SLOT BDF for CPU port PE5.

Release Notes from Previous Release(s)

1.0 (09/21/2022)

1. First release.