

BIOS Release Notes Form

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Product Name	X11DGQ
Release Version	3.8b SPS: 04.01.04.901
Build Date	1/17/2023
Previous Version	3.6
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	1. Changed BIOS revision to 3.8b. 2. Updated AMI label 5.14_PurleyCrb_0ACLA058 for RC0626.P01 IPU 2023.1. a. For INTEL-SA-00717 Security Advisory to address CVE-2022-32231 (7.5, High) / CVE-2022-26343 (8.2, High) security issues. 3. Updated token RC_VERSION_VALUE setting to 0626.P01. 4. Updated Cascade Lake-SP CPU PV microcode for IPU 2023.1. 5. Updated VROC SATA/sSATA EFI driver to VROC PreOS v7.8.0.1012.
New features	None
Fixes	None

Release Notes from Previous Release(s)

3.6 (5/17/2022)

1. Change BIOS Revision to 3.6.
2. Updated 5.14_PurleyCrb_OACLA054 for RC update and 2021.2 IPU PV.
3. Updated Skylake-SP/Cascade Lake-SP CPU PC microcode from IPU2021.2.
4. Updated SPS 04.01.04.601.
5. Updated Intel BIOS ACM Firmware to v1.7.51 and SINIT ACM Firmware to v1.7.51.
6. Fixed the Nvidia A100 GPU P2P bandwidth score low issue.
7. Fixed cburn black screen issue with fully loaded DIMMs.

3.5 (7/19/2021)

1. Changed BIOS revision to 3.5.
2. Updated 5.14_PurleyCrb_OACLA053 for RC update and 2021.1 IPU PV.
3. Updated Skylake-SP/Cascade Lake-SP CPU PC microcode from IPU2021.1.
4. Updated SPS 04.01.04.505.
5. Updated Intel BIOS ACM Firmware to v1.7.43 (20201216) and SINIT ACM Firmware to v1.7.4A (20201216).

3.4 (11/9/2020)

1. Changed BIOS revision to 3.4.
2. Updated 5.14_PurleyCrb_OACLA052 for RC update and 2020.2 IPU PV to address Intel-TA-00358: CVE-2020-0587 (6.7, Medium), CVE-2020-0591 (6.7, Medium), and CVE-2020-0592 (3, Low), and Intel-TA-00390: CVE-2020-0593 (4.7, Medium), CVE-2020-8738 (7.5, High), CVE-2020-8739 (4.6, Medium), CVE-2020-8740 (6.7, Medium), and CVE-2020-8764 (8.7, High) security issues.
3. Updated Skylake-SP/Cascade Lake-SP CPU PV microcode from 20200918_NDA Release to address Intel-TA-00381: CVE-2020-8696 (2.5, Low) security issue, MD_Clear Errata, MOB Speedpath, and IRR Restore with RS Throttle (ITR #2).
4. Updated SPS 4.1.4.423 to address Intel-TA-00391: CVE-2020-8755 (4.6, Medium) and CVE-2020-8705 (7.1, High) security issues.
5. Updated Intel BIOS ACM Firmware to v1.7.41 (20200406) and SINIT ACM Firmware to v1.7.49 (20200406).
6. Added support for A100 GPU by setting MMIO high size to 1024G by default.
7. Fixed inability of BIOS to load default when plugging in M.2.
8. Fixed problem of DVM-LITE-DVD18-HBT causing system to keep rebooting.
9. Fixed problem of Micron NVMe U.2 displaying as ???? in BIOS.

3.3 (2/25/2020)

1. Changed BIOS revision to 3.3.
2. Implemented hot fix from Intel for Purley Refresh BIOS Reference Code and Sample Code WW36_595D04 - IPU 2019.2.
3. Updated Intel Server Platform Services 4.1.4.381 for Purley-Refresh Platforms.
4. Updated Intel Reference Code to IPU2020.1 Rev: RC0602.D02.

5. Updated Intel BIOS ACM firmware to v1.7.40 (20190909) and SINIT ACM firmware to v1.7.48 (20191029).
6. Downgraded memory Patrol Scrubbing UC errors to correctable errors.
7. Updated Skylake-SP/Cascade Lake-SP CPU microcode from Intel-Restricted-2020-1-IPU for Intel-SA-00329 (CVE2020-0548 2.8 Low and CVE2020-0549 6.5 Medium).
8. Enhanced PPR log function.
9. Added SMC HDD Security feature.
10. Fixed problem of passive RTX6000/8000 causing system to halt.
11. Fixed occurrence of SUM internal error when using duplicated NVMe drives.

3.2 (11/7/2019)

1. Updated BIOS version to 3.2.
2. Updated Intel Skylake-SP H0 Microcode to 02000065.
3. Updated Intel Cascadelake-SP B0 Microcode to 0400002C.
4. Updated Intel Cascadelake-SP B1 Microcode to 0500002C.
5. Updated Intel Server Platform Services 04.01.04.339.0 for Purley-Refresh Platforms.
6. Displayed third revision number for IPMI Firmware.
7. Updated Intel BIOS ACM Firmware to v1.7.3 (20190712) and SINIT ACM Firmware to v1.7.45 (20190710).
8. Updated Intel Reference Code to BKC WW36 Rev: RC0595.D04.
9. Disabled ADDDC/SDDC and set PPR as hPPR.
10. Added Enhanced PPR function and set disabled as default.
11. Added support for mapping PCIe slots below 4G.
12. Fixed problem of F12 PXE boot causing DMI data to return to default value.
13. Fixed problem of IPv6 settings missing from BMC network configuration page.

3.1 (5/9/2019)

1. Changed BIOS Revision to 3.1.
2. Updated Intel Reference Code to BKC WW16 Rev: RC0584.D01.
3. Updated Intel Xeon microcode for Skylake-EP to Rev. 5E for Intel Xeon Skylake-EP H0/H0-QS.
4. Added Cascade Lake Server B0 Stepping CPU Support with Rev. 24 for Intel Xeon Scalable Cascade Lake Server B0-QS.
5. Added Cascade Lake Server B1 Stepping CPU Support with Rev. 24 for Intel Xeon Scalable Cascade Lake Server B1-QS.
6. Updated Intel Server Platform Services 04.01.04.296.0 for Purley-Refresh Platforms.
7. Updated RSTe VMD/SATA/sATA driver to v6.1.0.1017.
8. Updated SMBIOS Type 20 Information.
9. Set SDDC Plus One or SDDC to disabled by default.
10. Set ADDDC to enabled by default.
11. Fixed issue with NVIDIA P2P Low Performance.

3.0c (3/28/2019)

1. Changed BIOS revision to 3.0c.
2. Updated Intel Reference Code to BKC 2019_WW12 Rev: RC0580.D04.
3. Updated Intel Xeon microcode for Skylake-EP to Rev. 5A for Intel Xeon Skylake-EP H0/H0-QS.
4. Added Cascade Lake Server B0 Stepping CPU Support with Rev. 21 for Intel Xeon Scalable Cascade Lake Server B0-QS.

5. Added Cascade Lake Server B0 Stepping CPU Support with Rev. 21 for Intel Xeon Scalable Cascade Lake Server B1-QS.
6. Updated Intel Server Platform Services 04.01.04.256.0 for Purley-Refresh Platforms.
7. Updated Intel BIOS ACM firmware to v1.7.1 and SINIT ACM firmware to v1.7.2.
8. Changed BIOS setup item SR-IOV to disabled as default.
9. Added MAC address to the end of each network device boot option to eliminate duplication of the network boot option name when onboard LAN OPROM is set to EFI.
10. Fixed malfunction of the temperature sensor of riser AOC-SLG3-2M2 on some slots.
11. Fixed problem of memory training error occurring on SKX/CLX 4xxx CPU SKU with Micron (18ASF2G72PDZ-2G6E1) RDIMM.
12. Fixed malfunction of hotkey F12 when switching onboard LAN Option ROM to EFI.

3.0a (1/14/2019)

1. Added support for Purley Refresh platform.
2. Changed BIOS revision to 3.0a.
3. Updated Intel Reference Code to BKC WW52 Rev: RC0566.D03.
4. Updated Intel Xeon microcode for Skylake-EP to Rev. 57 for Intel Xeon Skylake-EP H0/H0-QS.
5. Updated Intel BIOS ACM firmware to v1.7.0 and SINIT ACM firmware to v1.7.1.
6. Enabled RFC4122 UUID support.
7. Added support for Tesla T4 GPU.

2.1 (8/10/2018)

1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.
2. Changed BIOS revision to 2.1.
3. Enabled the Jumperless BIOS Flash feature.
4. Added support for AOC-SLG3-2M2 rev. 1.01 auto-bifurcation.
5. Added Pre-Go-S1 feature.
6. Added Intel VMD Technology page to BIOS Setup page.
7. Added support for NVIDIA P2P.
8. Set Descriptor Region of BIOS Region Write Access to "No".
9. Fixed malfunctioning of "Wait for F1 If Error" function.
10. Fixed failure to detect hot plugged NVMe in Legacy Mode if OS is up first without any NVMe disk installed.

2.0b (2/28/2018)

1. Implemented enhancement to address 'Spectre' variant 2 (CVE 2017-5715) security patch issue.
2. Changed BIOS revision to 2.0b.
3. Removed BIOS time stamp from main BIOS setup page.
4. Added new FRU name for AOM-SXMV board.
5. Displayed the CPU T State Control Page on BIOS Setup Page.
6. Updated SMBus address for AOM-SXMV board after hardware rework.
7. Fixed inability to use OOB SUM to get current BIOS configuration file.
8. Fixed error when using AFUEFI tool to downgrade to previous ECO BIOS.
9. Fixed problem of system hanging at POST code "91" after BIOS update with EFI shell command "reset".

2.0 (11/29/2017)

1. *Changed BIOS revision to 2.0.*
2. *Updated Intel Xeon microcode for Skylake-EP.*
3. *Updated Intel Server Platform Services 04.00.04.288.0 for Purley Platforms.*
4. *Updated Intel BIOS ACM Firmware to v1.3.4 and SINIT ACM Firmware to v1.3.2.*
5. *Updated RSTe VMD/SATA/sSATA driver to v5.3.0.1052.*
6. *Added support for 16GB x 4 density DRAM Chip (64GB in total for 2rank x 4 RDIMM).*
7. *Updated Setup item "Cores Enabled" in BIOS CPU Configuration setup page.*
8. *Updated SMBIOS revision from 3.0 to 3.1.*
9. *Removed the Unsupported Memory Frequency setting in BIOS page.*
10. *Fixed inability of DMI Customized Information to preserve after user implements BIOSLoadDefault or CMOS Clear Action.*

Product Manager

Date