BIOS Release Notes Form

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Product Name	X11DPS-RE
Release Version	3.8c SPS: 4.1.4.901
Build Date	3/23/2023
Previous Version	3.8b
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	None
New features	None
Fixes	1. Fix for some system stops at POST 15

Release Notes from Previous Release(s)

3.8b (1/17/2023)

- 1. Updated AMI label 5.14_PurleyCrb_0ACLA058 for RC0626.P01 IPU 2023.1.
- 2. Updated token RC_VERSION_VALUE setting to 0626.P01.
- 3. Updated Cascade Lake-SP CPU PV microcode for IPU 2023.1.
- 4. Updated VROC SATA/sSATA EFI driver to VROC PreOS v7.8.0.1012.

3.6 (5/17/2022)

1. Changed to BIOS revision 3.6.

2. Fixed bizarre characters on XML file output by SUM --GetCurrentBiosCfg command with more than 9 pcs of NVME.

- 3. Updateed 5.14_PurleyCrb_0ACLA055 with RC update and 2021.2 IPU PV.
- 4. Updated Skylake-SP/Cascade Lake-SP CPU PC microcode.
- 5. Updated to SPS 4.1.4.700.
- 6. Updated Intel BIOS AC FW to v1.7.54 and SINIT ACM FW to v1.7.55

3.5 (10/19/2021)

1. Updated Skylake H0/M0/U0 CPU microcode 50654 to version 02006B05, Cascade Lake B0 microcode 50656 to version 04003101, and Cascade Lake B1/L1 microcode 50657 to version 05003101.

- 2. Updated for security vulnerability SA50090.
- 3. Updated ME firmware to SPS_E5_4.1.4.505.

3.3 (2/26/2020)

1. Changed BIOS revision to 3.3.

2. Updated Skylake H0/M0/U0 CPU microcode to version 69. Updated Cascade Lake B0/B1 to version 2F00.

3. Changed patrol scrub from uncorrectable to correctable error.

- 4. Updated Intel Reference Code to IPU2020.1 Rev: RC0602.D02.
- 5. Added Driver Health support.

6. Updated Intel BIOS ACM Firmware to v1.7.40 (20190909) and SINIT ACM Firmware to v1.7.48 (20191029).

7. Updated Intel Server Platform Services 4.1.4.381 for Purley-Refresh Platforms.

3.2 (12/6/2019)

1. Changed BIOS revision to 3.2.

2. Updated Skylake H0/M0/U0 CPU μ -code to version 65, Cascade Lake A0 to version 12, and Cascade Lake B0/B1 to version 2C.

- 3. Updated Intel[®] VROC version to 6.2.0.1034.
- 4. Updated Intel Reference Code to BKC WW36 Rev: RC0595.D04.
- 5. Enabled Intel PPR type option and Disabled ADDDC/SDDC option.
- 6. Displayed third revision number for IPMI Firmware.
- 7. Updated ME firmware revision to SPS E5 04.01.04.339.0.
- 8. Fixed problem of F12 PXE boot causing DMI data to return to default value.

3.1a (8/13/2019)

- 1. Updated Skylake H0/M0/U0 CPU μ -code to version 60 and Cascade Lake B0/B1 to version 29.
- 2. Updated Intel[®] VROC version to 6.1.1.1004.
- 3. Updated RC0584.D01.
- 4. Re-routed RP2 from MC1 to MC2 to balance the NUMA node.
- 5. Implemented workaround for problem of IPMI resetting with AC on/off test.
- 6. Fixed loss of DMI when running the DMICHECK script.
- 7. Fixed failure of the ruler system to automatically boot into PXE, USB, or the next boot priority when
- 32 pieces of ruler driver are installed

3.0a (4/2/2019)

- 1. Added support for Purley Refresh platform.
- 2. Updated Skylake H0/M0/U0 CPU microcode to version 57.
- 3. Updated Intel[®] RSTe package to version 6.0.0.1024.
- 4. Set NVDIMM ADR timeout to 600µs.
- 1. Added ME Jumpless Recovery Feature.

2.1a (10/3/2018)

- 1. Updated the RSTe revision to 5.5.0.1028.
- 2. Enhanced the BIOS secure flash upgrade feature (firmware signature).
- 3. Fixed the 50-second delay when booting into UEFI shell, DOS, and various Operating Systems (OS).
- 4. Fixed DMI writing error with full memory DIMM and ruler NVMe SSD plugged in.

2.1 (7/18/2018)

1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.

2. Enabled BIOS secure flash upgrade feature (firmware signature).

3. Added security signature feature for IPMI BIOS Upgrade.

4. Implemented RSD feature.

5. Fixed inability of system to detect SATA device when the PCH is very hot.