BIOS Release Notes Form

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Product Name	H13SSW			
Release Version	1.5			
Release Date	07/25/2023			
Build Date	07/25/2023			
Previous Version	1.4			
Update Category	Recommended			
Dependencies	IPMI 01.00.20			
·	BIOS_H13SSW-1C85_20230725_1.5_STDsp.bin - BIOS image			
Important Notes	BIOS_H13SSW-1C85_20230725_1.5_STDsp.zip - BIOS image and flash			
	utility package			
	"To support AMD EPYC Bergamo/Genoa-X processor, please update			
	to BIOS v1.4 or higher."			
	1. Upgraded BIOS version to 1.5 for AGESA1007.			
Enhancements	2. Added support for redfish API			
	Oem/Supermicro/FixedBootOrder.			
	3. Added an option to hide the Local APIC Mode.			
	4. Corrected the "xGMI Force Link Width Control" & "xGMI Force			
	Link Width" settings.			
	5. Fixed the problem no event logs generated in IPMI Web after			
	"DRAM UECC Retry Error Injection" was run.			
	6. Changed the ASPM setting to not sync to the PCIe port.			
	7. Changed the "Power Off" string to "Stay off."			
	8. Synced up Core control option items			
	9. Disabled the ASPM root port to fix the problem of single disk IO			
	in which SAS card was installed in Slot 2.			
New Features	N/A			
Fixes	1. Fixed the issue in which the AMD cbs_config item did not align			
	with APCB and caused systems hang in the early video phase.			
	2. Fixed the issue with AIOM2 detection as P3 BDF reversal.			
	3. Fixed the issue with BBS item modification the SUM tool.			
	Fixed the issue with uncorrectable non-fatal reporting SERR.			
	5. Fixed the issue with setting up BIOS without using ACS control.			
	6. Fixed CPU related event logs.			

1.4 (2023/04/17)

[Enhancements]

- 1. Changed BIOS version to 1.4 for AGESA1006.
- 2. Followed the spec to update from MHz to MT/s in POST messages.
- 3. Changed PSP Logo to add a warning message at POST time.
- 4. Added support for the Xilnix U30 FPGA card.

[Fixes]

- 1. Fixed the error of MCA not being triggering when MCEON was disabled.
- 2. Fixed non-fatal triggering the SERR report.
- 3. Fixed issue with UUID having no MAC address in SMBIOS type 1 when AIOM was installed.
- 4. Fixed issue with DMI data not being restored after the default settings were loaded.
- 5. Fixed the OPROM FW display issue on the Setup Advanced page when AIOM was installed with two controllers.
- 6. Corrected CCD/ core count control setting on the BIOS CPU information page.

1.1 (2023/01/17)

[Enhancements]

- 1. Changed BIOS version to 1.1.
- 2. Updated AMD patch amd-dxio-floyd-1.13.04-20230111212355 for the NVMe hot plug issue.
- 3. Updated AMD patch -GenoaPI-SP5 1.0.0.3 Patch 1-for GPU (NVqual Test).
- 4. Upgraded code base to CRB16 Beta/AGESA 1.0.0.3.
- 5. [OOB] Updated SmcSecureBoot module to support the KEK, DB, DBX, DBT, and DBR key delete functions.
- 6. Fixed USB KB no function under Legacy mode.
- 7. [OOB] Upgraded Platform_ID to 0x0D for SUM Feature Flag.
- 8. [OOB] Added "Supermicro Security Erase Configuration item to the SUM Tool.
- 9. [OOB] Fixed the issue with the "User Password" item not displaying when SUM was used. 10. Added the Enhanced PPR item.

[Fixes]

- 1. Fixed the issue by automatically disabling the Above 4G Decode option when Intel LAN chip PCIe/AIOM card was installed under Legacy mode.
- 2. Fixed the issue with Slot 1 A2 GPU being shown lost on BMC Web when six A2 GPUs were installed.
- 3. Fixed the issue with AIOM1 failing to load Oprom when AIOM1, AIOM2 and RSC-D2-666G5&RSC-D2R-666G5 were installed.
- 4. Fixed the issue with [SuperDiag_1.7.0b220810] that SuperDiag could not be run on H13SSW remotely or via USB.
- 5. Fixed the issue in which the riser card failed to auto-detect.
- 6. Fixed the issue in which the "AOC-AG-i2M AIOM2 OPROM" option in PCIe/PCI/Pnp Configuration was not available after "Boot Mode" was set to "Legacy.
- 7. Fixed the issue with AOC-AG-i2M: The legacy PXE banner failed to show up during system boot up.
- 8. Fixed the issue of system hanging (0xb0) after the SUM Inband update BIOS command was executed and system was reboot.
- 9. Fixed the incorrect display of Memory SN on BMC Web.

1.0 (2022/11/15)

First release.