

BIOS Release Notes Form

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Product Name	H13DSH
Release Version	1.5
Release Date	07/24/2023
Build Date	07/24/2023
Previous Version	1.4
Update Category	Enhancement
Dependencies	BMC: 01.01.01 (BMC_H13AST2600-ROT-1401MS_20230721_01.01.01_STDsp)
Important Notes	BIOS image: BIOS_H13DSH-1C84_20230724_1.5_STDsp.bin BIOS image and flash utility package: BIOS_H13DSH-1C84_20230724_1.5_STDsp.zip 1. Please also update with CPLD: F5.0F.1C and get further assistance from our technical support about CPLD inquiry. 2. "To support AMD EPYC Bergamo, Genoa X processor, please update to BIOS v1.4 or higher."
Enhancements	1. GenoaPI-SP5_1007_Patch_Update 2. Genoa_Bergamo_Siena_SN7005_BIOS_Patch 3. Fixed issue of the SUM tool failing to retrieve BBS Priorities via the GetCurrentBiosCfg command. 4. Fixed issue where the L0s message showing in the Help string in ASPM setting. 5. Added support for redfish API Oem/Supermicro/FixedBootOrder. 6. Improved issue of the ADDC feature being enabled. 7. Fixed issue of WA for system hang up at 0xA2 when NVMe namespace total create over 100

New Features	N/A
Fixes	<ol style="list-style-type: none"> 1. Fixed issue of the configured ASPM not syncing with the PCIe port that the "ASPM Support" option in BIOS Setup didn't work. 2. Added PCIe lanes drop "Patch" for 155978 - [ECO_AS-2125HS-TNR_Bergamo] AOC-CX766003N-SQ0 PCIe link status downgrade the width from x16 to x8 during AC/DC cycle 3. Fixed issue of reducing HP mmio preserved from 16M to 8M in order to prevent GenoaX from hanging post code 91 during resource access. 4. Fixed issue of failing to syncing up the setup items with AMD H13-Genoa BIOS Setup Template v0.8_20230112.xlsx. 5. Fixed issue of not reporting the uncorrectable non-fatal SERR issues. 6. Fixed issue of the BBS item failing to be changed via the SUM tool. 7. Fixed issue of the AMD cbs_config item aligning with APCB because the system hung in the early video phase.

Release Notes from Previous Release(s)

1.4(2023/04/19)

[Enhancement]

1. Added the FSRM and ERSM items.
2. Added the Periodic Training, EQ Bypass To Highest Rate, CXL SPM, and Sync Header Bypass items.
3. Updated Agesa code to Genoa PI 1.0.0.6.
4. Updated the OFID table to match Agesa module code information.
5. Added an "xGMI Configuration" group to match the AMD H13-Genoa BIOS Setup Template.
6. Improved to prevent sync setting from failing in SmcAmdCbsDxeDriver.c with the direct use of CBS_CONFIG variable in SmcSetup.sd.
7. Improved the issue in which a device's upper bridge was not recognized due to its bridges by adding a patch for NvidiaGPUGA107GL.
8. Changed the unit of memory from MHz to MT/s in POST messages.
9. Changed the PSP Logo to include a warning message about post time.
10. Modified the string length of OEM_STRING_1/OEM_STRING_2 in SMBIOS type11 to at least 50 characters to meet the SMBIOS spec.
10. Modified the GetVariable() service for buffer overflow in certain cases.

[Fixes]

1. Fixed the issues in which Intel AOC failed to access legacy PXE, system failed to boot into legacy mode Cburn, and legacy PXE boot failed.
2. Fixed the automation failure.
3. Fixed the problem of failing to set the EQ Bypass To Highest Rate.
4. Fixed the problem in which the bus number reported to SMBIOS type 9 was incorrect when NVIDIA A16 was connected to the slot from JMCIO3/JMCIO4.
5. Corrected "xGMI Force Link Width Control" and "xGMI Force Link Width" settings.
6. Fixed the problem of Intel AOC failing to access entry legacy PXE when two different vendor AOC/AIOM LAN cards were installed.
7. Added support for RNDIS-ECM.
8. Fixed the problem of BPN_S24 NVMe0's failure of building connection when AC power was on.
9. Fixed the problem of suppressing hotplug when BPN_24 151514 was used and the NVMe eject and insert functions were invalid.

1.2(2023/02/09)

[Fixes]

1. Fixed the issue in which NVMe lane dropped during ACOFF test.
2. Fixed the problem of no boot videos generated with the Console Capture Video function enabled.

1.1(2023/01/16)

[Enhancement]

1. Enabled reporting of all SATA or all NVMe hard drive to CPLD offset 08h bit7.
2. Added "Supermicro Security Erase Configuration" item for SUM Tool.
3. Upgraded Platform_ID to 0x0D for SUM FeatureFlag.
4. Updated AMD patch "GenoaPI-SP5_1.0.0.3_Patch_1" for GPU.
5. Updated MPIO FW 1.0.13.1 for NVMe Hotplug and roll back to NVMe module A according to AMD's suggestion.
6. Changed default JMCIO5/6 as RSC connector to avoid PCIe-AOC FRU misjudgment.
7. Changed the type of BMC_LAN to USB from Network on SMBIOS Type 8.
8. Added E810 Intel Network controller AIOM card in Legacy PXE patch list.
9. Enabled "Preserve Boot Options Configuration" function.
10. Updated AMD MPIO 1.13.04

[Fixes]

1. Fixed TC2016 issue.
2. System cannot boot into Cburn via AOC-ATG-I2TM_Intel X550 (legacy PXE boot).
3. Run ATT Failed with few TC on H13DSH.

(TC 2008/2010/3002/3004/4006/4008/5004/7004 only)

4. NAOC order detection for system slot 1 and 2 reversed on BMC Web.

1.0 (2022/11/15)

Initial release.