

IPMI Firmware / BIOS Release Notes Form

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| Product Name | H13SSL-N/NT |
| Release Version | 1.5 |
| Release Date | 07/25/2023 |
| Build Date | 07/25/2023 |
| Previous Version | 1.4 |
| Update Category | Recommended |
| Dependencies | IPMI 01.01.03 |
| Important Notes | ECO #28611 BIOS Image: BIOS_H13SSL-1C7D_20230725_1.5_STDsp.bin BIOS Update Package: BIOS_H13SSL-1C7D_20230725_1.5_STDsp.zip Please update BIOS with attached Flash Utility in package. "To support AMD EPYC Bergamo/Genoa-X processor, please update to BIOS v1.4 or higher." |
| Enhancements | <ol style="list-style-type: none">1. Change BIOS version to 1.5 for AGESA1007.2. AMD ucode patch 2 to fix for erratum #1483/14853. Support redfish API Oem/Supermicro/FixedBootOrder.4. Hidden Local APIC Mode due to PM's decision, will update H13 setup template later.5. Setup ASPM not sync to PCIe port.6. Change "Power Off" string to "Stay off" to match AMD H13-Genoa BIOS Setup Template.7. Sync up Core control option items with AMD H13-Genoa BIOS Setup Template v0.8_20230112.xlsx |
| New features | N/A |
| Fixes | <ol style="list-style-type: none">1. Fix AMD cbs_config item does not align APCB cause system hang at early video phase.2. Fix BBS item cannot be changed problem via SUM tool.3. Fix uncorrectable non-fatal will report SERR issue.4. Fix BIOS setup doesn't have ACS control item.5. Force BMC (AST2600) link speed to Gen1 to avoid link speed drop issue on DC on-off test |

Release Notes from Previous Release(s)

Revision 1.4(04/17/2023)

1. Change BIOS version to 1.4 for AGESA1006.
2. Upgrade code base to CRB19/AGESA 1.0.0.6
3. Using CBS_CONFIG variable directly in SmcSetup.sd to prevent sync setting fail in SmcAmdCbsDxeDriver.c
4. Follow the spec to change unit of memory translate from MHz to MT/s in POST message.
5. Change PSP Logo to include add warming message for post time.
6. Enable display of Day in Date Format.
7. Add SuperGuardians support.
8. Add "AmdSetup" variable group support for OOB used.
9. Increase OEM_STRING_1/OEM_STRING_2 in SMBIOS type11 string length to at least 50 characters to meet SMBIOS spec.
10. Fixed MCA correctable error cannot trigger when MCEON disabled.
11. Fixed uncorrectable non-fatal will report SERR issue.

Revision 1.1a(02/14/2023)

Update AMD patch amd-dxio-floyd-1.13.04-20230111212355 to fix NVMe hotplug issue.

Revision 1.1(12/23/2022)

1. Upgrade code base to AGESA 1.0.0.3
2. Onboard M.2 Support PCIe speed for M.2-H1/H2.

Revision 1.0 (2022/11/22)

First release.