

BIOS Release Notes Form

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Product Name	X13SEDW-F
Release Version	1.4
Release Date	08/10/2023
Build Date	08/10/2023
Previous Version	1.1
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	<ol style="list-style-type: none">1. Updated Intel BKC SPR 2023_WW21 PLR2/3, Intel RC Version 103.D70.2. Update PCIe Leaky Bucket Expected BER and Error Threshold items default value.3. Corrected wrong report of SMBIOS Type16.4. Resolved some boards resetting repeatedly after use IPMI tool to reset NVRAM.5. According to motherboard UP/DP/MP, the corresponding HBM SMBIOS type 17 device locator format is now used (UP : HBMIOx, DP/MP : Px_HBMIOx).6. According to motherboard UP/DP/MP, the corresponding HBM memory format on BIOS setup is now used (UP : HBMIOx, DP/MP : Px_HBMIOx).7. Fixed some VGA cards having no display under legacy mode.

	8. Memory speed is now displayed as MT/s instead of MHz.
New features	None
Fixes	<ol style="list-style-type: none"> 1. When RTC power loss happens, BIOS build time is configured to system time. 2. "Available Bitmap:" is now present in the BIOS configuration file. 3. Fix missing end tag (0x78) of VPD data when the VPD length is a multiple of 4 (0 base).

Release Notes from Previous Release(s)

1.1 (01/19/2023)

- 1. Updated Intel BKC 2022 WW51 (5.29_EagleStreamCrb_OACOR_071_BETA).*
- 2. Followed latest BIOS setup template, to hide the UPI3 feature in setup for UP projects.*
- 3. Ensured there is no FWTS error relative to SRIO.*
- 4. Followed latest BIOS setup template to expose Consistent Device Name Support.*
- 5. Updated SmcOOB to add support for the Redfish update.*
- 6. Exposed "CXL Security Level" and "CXL Header Bypass" for CXL.*
- 7. Removed CPU2, CPU3, and CPU4, which were displayed under IIO Configuration in setup after restoring optimized defaults.*
- 8. Resolved the issue that caused "Crow pass Numa node info is missing" to appear.*

1.0a (10/20/2022)

- 1. Updated Intel BKC WW41 from AMI label 5.29_EagleStreamCrb_OACOR_065.*
- 2. Disabled Pre-Go S1.*
- 3. Default enable TOKEN "SMC_HECI_HIDE_DELAY" (HECI-x hide delay) for all projects to fix BSOD issue.*
- 4. Exposed "Equalization Bypass To Highest Rate."*
- 5. Automatically hide ADDDC Sparing item with x8 width DIMM.*
- 6. Updated DBX file to fix Secure Boot Bypass issue.*
- 7. Enabled the BOOT Guard relate setting.*
- 8. Followed the SMBIOS template to sync the chassis type from FRU0 to SMBIOS Type 03.*
- 9. Fixed Memory serial number of SMBIOS type 17 that does not match the memory DIMM bar code issue.*
- 10. Fixed the problem that the password could not be saved after the NVRAM module updated label 26. (Updated SmcOOB module to 2.00.15)*

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