

BIOS Release Notes Form

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Product Name	X13SEW-(T)F
Release Version	1.4
Release Date	09/08/2023
Build Date	09/08/2023
Previous Version	1.3a
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	<ol style="list-style-type: none">1. Updated kernel to AMI label 87 (BKC WW26).2. After using SUM to load BIOS default, BBS can now be restored to default.3. Modify Me version strings, removed the "Manufacturer ID" string.4. According to motherboard UP/DP/MP, motherboard now uses corresponding HBM memory format on BIOS setup (UP : HBMIOx, DP/MP : Px_HBMIOx).5. According to motherboard UP/DP/MP, motherboard now uses corresponding HBM SMBIOS type 17 device locator format (UP : HBMIOx, DP/MP : Px_HBMIOx).6. Followed SMBIOS structure template 1.4 to modify device locator format and bank locator format in HBM SMBIOS type 17.

New features	None
Fixes	<ol style="list-style-type: none"> 1. Resolved secure boot keys not being preserved after BIOS update. 2. Resolved X13SEW-F resetting repeatedly after using IPMI tool to set CMOS clear flag. 3. After RTC power loss, BIOS build time is configured to system time. 4. "Available Bitmap:" is now present in BIOS configuration file.

Release Notes from Previous Release(s)

1.3a (04/28/2023)

- 1. Updated kernel to AMI label 5.31_EagleStreamCrb_EMR_OACOR_083 (SPR : BKC WW13, EMR : BKC WW19).*
- 2. Updated default configuration of PCIe Leaky Bucket Expected BER and Error Threshold.*
- 3. Kept HBM SMBIOS type 17 structures.*
- 4. Added SEL (Sensor Type: 0xC3, Data1 : 0x06, Data2 : CPU Index, Data3 : UPI Index) for UPI (topology change) link degraded.*
- 5. Updated the PPIN default configuration to Unlock.*
- 6. Added support for inserting a secure boot key via SUM.*
- 7. Modified the IOU bifurcation according to the AOC card when the IOU bifurcation was set to auto on setup.*
- 8. Modified the IOU bifurcation to take setup configuration if it is not set to auto.*

1.3 (04/28/2023)

- 9. Updated Intel BKC 2023 WW13 (PLR1).*
- 10. Used try-catch mechanism to recover from CPU exception during driver health check.*
- 11. Improved variable interface for buffer overflow case in security erase module.*
- 12. Added PCIe leaky bucket degradation SEL (Sensor Type 0x13 Data1 0xAB).*
- 13. Made RC revision so it can be decoded correctly.*

1.1 (1/19/2023)

- 14. Updated Intel BKC 2022 WW51 (5.29_EagleStreamCrb_OACOR_071_BETA).*
- 15. Updated SPS to 6.0.4.25 2S PV Hotfix Release.*
- 16. Enhanced the memory map out.*
- 17. Exposed "CXL Security Level" and "CXL Header Bypass" in setup for CXL.*
- 18. Updated the SMCOOB module to 2.00.19.*
- 19. Followed the latest BIOS setup template, and hid the UPI3 feature in setup for UP projects.*
- 20. Fixed FWTS fail relative to SRIO.*
- 21. Followed the latest BIOS setup template and exposed "Consistent Device Name Support" in setup.*

22. Updated and enabled the PRM package to resolve the issue with the PRM test device, which could not be recognized under Windows device manager.
23. Resolved the CXL memory which could not be reconfigured from DAX device to memory.
24. Removed CPU2, CPU3, and CPU4, which were displayed under IIO Configuration in setup after restoring optimized defaults.
25. Removed CrowPass numa node information.

1.0a (10/19/2022)

1. Updated Intel BKC WW41 from AMI label 5.29_EagleStreamCrb_0ACOR_065_BETA.
2. Enabled the "SMC_HECI_HIDE_DELAY" token (HECI-x hide delay) for all projects to fix the BSOD issue.
3. Added the token ENABLE_SRIO_ASL_PORTING to control ASL porting for ASL device SRIO.
4. Updated the DBX file to fix the Secure Boot Bypass issue.
5. Added enhancements to run the SUM TC: 220/271/356/457 Windows OS and fix the boot order compare fail after flash BIOS.
6. Enabled the password to be cleaned after flashing the BIOS and then resetting the system.
7. Fixed COM1/COM2 console redirection causing a system to hang when disabled.
8. Fixed HTTP/HTTPS boot failure.
9. Fixed the SUM updating the DMI data causing every DMI value to be set.
10. Removed the BMC and the TPM smbios type 45 to fix WHQL smbios failure.
11. Enabled saving the password after the NVRAM module is updated.