

# IPMI Firmware / BIOS Release Notes Form

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<b>Product Name</b>	<b>H12SSW-NTR</b>
<b>Release Version</b>	<b>2.7</b>
<b>Release Date</b>	<b>10/25/2023</b>
<b>Build Date</b>	<b>10/25/2023</b>
<b>Previous Version</b>	<b>2.6a</b>
<b>Update Category</b>	<b>Recommended</b>
<b>Dependencies</b>	<b>N/A</b>
<b>Important Notes</b>	<b>ECO #29067</b> <b>BIOS Image: BIOS_H12SSWR-1B74_20231025_2.7_STDsp.bin - BIOS image</b> <b>BIOS Update Package: BIOS_H12SSWR-1B74_20231025_2.7_STDsp.zip</b> <b>Please update BIOS with attached Flash Utility in package.</b>
<b>Enhancements</b>	<b>1. [Milan][Rome] Restore SMCI secure boot keys and Update Secure Boot DB variable. (Unknown certificate "Addtrust External CA Root" is expired on May 30th 2020).</b> <b>2. [Milan] Update MilanPI to 1.0.0.B based on 5.22_MilanCrb_0ACOU022.</b> <b>3. [Rome] Update AGESA RomePI to 1.0.0.G based on 5.14_RomeCrb_0ACMK027.</b> <b>4. [Milan][Rome][SmcOptIpmiBoot][Enhancements] Support that changing Pxe from Uefi(U)/Legacy(L) to L/U through Redfish.It can adjust setup option and priority of Pxe boot order according to Boot Flag Command.</b>
<b>New features</b>	<b>N/A</b>
<b>Fixes</b>	<b>1. [Milan][Rome] Set RDIMM\LRDIMM\3DSDIMM memory throttling trip-point @85C part II, According to DDR4 Spec and our thermal design guide.</b> <b>2. [Milan][Rome] Modify the reading BPNID method from BMC.fixes during the DC/AC long run test, PCIe device will not match.</b> <b>3. [Milan][Rome] Fixed the issue that SUT will hang at post code "92" when AsMedia SATA Controller is Disabled.</b> <b>4. [Milan]Fix some dmivar unfit with ami sbmbios settings, AMI using UnicodeSPrint instead of Swprintf. The hex number case was different.</b>

*Release Notes from Previous Release(s)*

## **Revision 2.6a(2022/10/06)**

1. Update Milan B0/B1/B2 stepping CPU microcode and Milan-X B2 stepping CPU microcode for AMD-SB-7005 Security Bulletin to address CVE-2023-20569 issue.
2. Support the Supermicro System LockDown feature.
3. Update Rome B0 stepping CPU microcode 0x0830107A for AMD-SB-7008 Security Bulletin to address CVE-2023-20593 issue.
4. Add Rocky Linux Boot Option Name.
5. Enable token ASM1061\_Workaround to disable ASM1061 64-bit memory address capability.
6. Update MilanPI to 1.0.0.A based on 5.22\_MilanCrb\_0ACOU021 for AMD-SB-1032.
7. Update Milan B0/B1/B2 stepping CPU microcode and Milan-X B2 stepping CPU microcode to patch Patch for Errata 1304, 1310, 1311, 1313, 1329, 1330, 1336, 1343, 1350, 1352, 1361, 1378,1379, 1381, 1386, 1407, 1433, 1450.
8. Follow the SMBIOS template sync the chassis type from FRU0 to SMBIOS Type 03, only for H12 projects.
9. Update AGESA RomePI to 1.0.0.F based on 5.14\_RomeCrb\_0ACMK026 for AMD-SB-1032.
10. Modified GetVariable() service for buffer overflow in certain cases.
11. Fixes keep the PCIe original bifurcation when the required link width for the AOC-SLG card is greater than x4.
12. Fixes When AsMedia SATA Controller Disabled SUT will hang at post code "92"

## **Revision 2.5(2022/10/06)**

1. Update Milan AGESA to 1.0.0.9
2. Update Rome AGESA to 1.0.0.E
3. Follow the SMBIOS template and sync the chassis type from FRU0 to SMBIOS Type 03.
4. Fixed the system can't boot to UEFI USB KEY with command 'ipmi power bootoption 11'.
5. Fixed Update SMBIOS type 41 data. Change "NEC USB" string to "Renesas USB"

## **Revision 2.4(2022/02/23)**

1. Update Milan AGESA to 1.0.0.8
2. Update Rome AGESA to 1.0.0.D
3. Fixed the system randomly hanged postcode "0x00"/"0xCd"/"0x4b" when the system runs AC On/Off burning test.
4. Fixed the system hanged postcode "AC" when TPM 2.0 module is installed.
5. Fixed the MAC address of AOC-A25G-i2SM that can't show in BMC Web UI.
6. Fixed Milan and Milan-X CPU can't detect AMD GPU MI210 populated on slot-1 but Rome can.
7. Change each root bridge MMIO size from 16M to 64M to fix out of resource issue.

## **Revision 2.3 (2021/10/01)**

1. Update Milan AGESA to 1.0.0.6
2. Update Rome AGESA to 1.0.0.C

## **Revision 2.2 (2021/08/09)**

1. Followed the new cable plan to change the G1 & G2 PCIe config.
2. New AIOM card (AOC-A100G-m2CM) is Support; BIOS supports this AIOM card for BMC sensor reading.
3. When the AIOM1 changed device, the BMC gets a reset. However, it causes the BIOS cannot update the OOB file successfully.
4. Update BIOS version from 2.1 to 2.2
5. USB controller PCIe gen. speed fix from PCIe Gen. 1 to PCIe Gen. 2

## **Revision 2.1 (2021/07/12)**

fixes the BIOS version shows incorrectly

## **Revision 2.0 (2021/05/25)**

First release.