

## BIOS Release Notes Form

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<b>Product Name</b>	<b>X13DET-B</b>
<b>Release Version</b>	<b>1.5a SPS: 6.1.2.48</b>
<b>Build Date</b>	<b>10/31/2023</b>
<b>Previous Version</b>	<b>1.5 SPS: 6.1.2.48</b>
<b>Update Category</b>	<b>Recommended</b>
<b>Dependencies</b>	<b>None</b>
<b>Important Notes</b>	<b>None</b>
<b>Enhancements</b>	<b>None</b>
<b>New features</b>	<b>None</b>
<b>Fixes</b>	<ol style="list-style-type: none"><li><b>1. Resolved the system would encounter ASSERT while executing GetHIDevicePath function.</b></li><li><b>2. Rollback NVMe port 0/1 back to NS1 from NS3 for SCC-P6NG5-B1.</b></li></ol>

#### **Release Notes from Previous Release(s)**

##### **1.5 SPS: 6.1.2.48**

1. Enhanced the no bootable device message on the POST screen feature.
2. Enhanced the solution for the error/warning message from dmidecode after a modification by AMIDE.
3. Supported 3'rd LAN card VPD (Mellanox).
4. Changed the strings from "Lock mode" to "Lockdown mode".
5. Kept HBM SMBIOS type 17 structures.
6. Set PPIN default value to Unlock.
7. Added SmcPciHookLib for Broadcom LAN 57414 and 57416.
8. Enabled PCIe device drop check for AIOM, PCIe slot1, slot2, and SCC-P6NG5-B1.
9. Updated Intel BKC SPR 2023\_WW21 PLR2/3, EMR 2023\_WW25, Intel RC Version 103.D42.
10. Followed template to remove AMI's BMC VLAN items.
11. Updated Seamless MCU capsule revision to 1.0b for PLR2/3.
12. Updated AMI label to 5.31\_EagleStreamCrb\_EMR\_OACOR\_087.
13. Updated Intel Server Platform Services SPS\_E5\_6.0.5.046 PLR3 HF for Eagle Stream Server Update 782193 Intel Server Platform Services SPS\_E5\_6.0.5.046 PLR3 HF for Eagle Stream Server.
14. Fixed SUM RoT power fail malfunction issue.
15. Modify Me version strings, removed the "Manufacturer ID" string.
16. Corrected string issue in "Preferred DNS server IP" and "Alternative DNS server IP" when BMC sending an invalid IP.
17. Updated AMI label to 5.32\_EagleStreamCrb\_EMR\_OACOR\_093.
18. Adjusted the position of FwCapHdr to the front of FV\_MAIN to avoid secure flash and Boot guard sign fail.
19. Exposed "Page Policy".
20. After using SUM to load BIOS default, BBS can be restored to default as well.
21. Updated Seamless MCU capsule revision to 1.0c for PLR4.
22. [SmcRecBootStat] Made sure that data can be gotten from BMC after adopting the newer AMI IPMI module.
23. Rollback GPNV module to fix type 15 disappear.
24. Fixed missing end tag (0x78) of VPD data when the VPD length is multiple of 4 (0 base).
25. Removed UPI3 item and expose PE3 Link Speed Control.
26. As RTC power loss ever happens, configure BIOS build time to system time.
27. Memory speed should be MT/s, not MHz.
28. Resolved IPV6 "Gateway IP" is not displayed correctly on the BIOS setup.
29. [SmcGetSetSBkeys] Fixed the issue of Preserving Secure Boot Keys.
30. Resolved an issue where the system would hang when restoring the Secure Boot keys.
31. Fixed system hangs on ACPI enable if CPU core number is more than 256 and set max performance.
32. Fixed the problem that httpboot boot option cannot be created.
33. Resolved the "BMC LAN Selection" setup item differs from the "LAN selection interface" under BMC WEB GUI.
34. Resolved even if the 'Ipv6' is disabled in the BMC WEB GUI, the IPv6 address still displays during the EarlyVideo stage.

##### **1.2 SPS: 6.0.4.33 (3/28/2023)**

1. Updated Intel BKC 2023\_WW09, Intel RC Version 9409.P31 MR2 and HBM PV.
2. Updated options of Enhanced PPR to Disable(Original Disabled)/Enable(Original Once)/Persistent(Original Enabled).

3. *Removed first capital letter S from Micron type 17 serial number.*
4. *Exported PCIe leaky bucket related items and added X-AMI ID.*
1. *Only reported LAN's VPD to BMC to avoid BMC fail parsing SMBIOS.*

### **1.1 (1/20/2023)**

1. *Updated AMI label to 5.29\_EagleStreamCrb\_0ACOR\_071\_BETA.*
2. *Exposed and adjusted the MRRS item*
3. *[SmcUefiBootsOptionRedfish] In order to be compatible with the code base of SuperBIOS and AMI, use SMC's defined GUID for passing variables to BMC.*
4. *Fixed Type 17 that does not follow SPD Spec MemoryChannelBusWidth.*
5. *Exposed CPU "Optimized Power Mode".*
6. *Updated SATA SGPIO Mode option strings for all SATA configuration menus.*
7. *Hid "Homeless Prefetch".*
8. *Updated SPS 6.0.4.25 2S PV Hotfix Release for Eaglestream Platforms.*
9. *Revised code for NVMe PCC init.*
10. *Exposed "CXL Security Level" and "CXL Header Bypass" for CXL.*
11. *Enabled PRM module and updated PrmPKG from EIP724445*
12. *Referred*  
*751004\_Intel\_VROC\_Comp\_Issue\_Microsoft\_Windows\_OS\_Ver\_TA\_WW47\_2022.pdf*  
*Temporary W/A#3 to set SATA RAID controller SSVID/SSID to 8086/7270 for tSATA RAID*  
*Windows 2022 issue.*
13. *Applied CPLD E2 mux w/a for PCH SMBUS can't access issue.*
14. *Fixed BIOS IIO Configuration menu will display four CPU sockets menus when loading BIOS*  
*Optimized Defaults issue.*
15. *Fixed error where CPU2 TDP disappears in BMC Web.*
16. *Corrected BankLocator for DP 1SPC projects.*
17. *Resolved CXL memory cannot be reconfigured from DAX device to memory.*

### **1.0a (11/03/2022)**

18. *Changed BIOS Revision to 1.0a.*
19. *Updated AMI label to 5.29\_EagleStreamCrb\_0ACOR\_066.*
20. *Enhanced BiosCfg to show Sum GetBiosCfg when booted to setup menu.*
21. *Enhanced tool to support user disable for "CPU C1 auto demotion".*
22. *For chassis types "unknown" or "other", synchronization is performed from FRU0 to SMBIOS*  
*Type 03.*
23. *Added and enabled TOKEN "SMC\_HECI\_HIDE\_DELAY" to control the HECI-x hide delay to patch*  
*some unexpected behavior (system hang, system reboot etc.).*
24. *Updated Supermicro KMIP form.*
25. *Updated AST2600 VBIOS and VGA uEFI driver to 1.13.01.*
26. *Enhanced tool to force PE3 port A to gen4 when UPI3 is enabled.*
27. *Enhanced tool to automatically create UEFI OS name for removable device (USB key).*
28. *Removed "Supermicro Security erase" page from HII format.*
29. *Added SecureBoot Database to meet Redfish specification and single key Delete function.*
30. *Discarded duplicate MAC address from Redfish interface.*
31. *Updated SPS 6.0.3.271 2S PC Hotfix Release for Eaglestream Platforms.*

- 32. Added support for BIOS POST Progress in Redfish.
- 33. Added new feature to set BIOS\_EXIT\_UBOOT/BIOS\_BOOT\_OK at end of POST
- 34. Fixed the problem that the password could not be saved after the NVRAM module updated label 26.
- 35. Fixed issue with Memory serial number of SMBIOS type 17 not matching with memory DIMM bar code.
- 36. Fixed issue with SUM update DMI data setting every DMI value.
- 37. Corrected VMD mapping in backplane CPLD register.

#### **1.0 (06/28/2022)**

- 1. First release based on BKCWW25 2022.

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*Product Manager*

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*Date*