

IPMI Firmware / BIOS Release Notes Form

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Product Name	H11DSU-iN
Release Version	2.8
Release Date	12/14/2023
Previous Version	2.7
Update Category	Recommended
Dependencies	MB Rev 2.10
Important Notes	BIOS R2.8 only support MB Rev2.10 with 32MB SPI flash ROM to support both AMD 7001/7002 series processors.
Enhancements	<ol style="list-style-type: none">1. [Rome][Enhancements] Update AGESA RomePI to 1.0.0.H based on 5.14_RomeCrb_OACMK028.2. [Naples][Enhancements] Update AGESA NaplesPI to 1.0.0.L based on 5.013_NaplesCrb_OACIJ031.3. [Naples][Rome][Enhancements] Update SA50216_Supplement(LogoFAIL Vulnerability).4. [Rome][Enhancements] Fixed MP1(SMU) and CPU WDT uncorrectable error no event log issue.5. [Rome][Enhancements] Disable "Correctable/Non-Fatal error reporting enable" bits when BIOS item "PCI AER support" set Disabled.
New features	N/A
Fixes	<ol style="list-style-type: none">1. [Rome][Fixes] Fixed the problem that the AMD Radon PRO W7500 & W7600 VGA card cannot be displayed in the shell or BIOS setup menu.

Release Notes from Previous Release(s)

2.7 (10/20/2023)

1. [Rome][Enhancements] Update AGESA RomePI to 1.0.0.G based on 5.14_RomeCrb_0ACMK027.
2. [Rome][SmcOptlpmiBoot][Enhancements] Support that changing Pxe from Uefi(U)/Legacy(L) to L/U through Redfish.
3. [Naples][Enhancements] Enable token ASM1061_Workaround to disable ASM1061 64-bit memory address capability.
4. [Naples][Rome][Fixes] Set RDIMM\LRDIMM\3DSDIMM memory throttling trip-point @85C.

2.6a (09/27/2023)

1. [Enhancements][Rome BIOS] Update AGESA RomePI to 1.0.0.F based on 5.14_RomeCrb_0ACMK026 for AMD-SB-1032.
2. [Enhancements][Naples/Rome BIOS] Add Rocky Linux Boot Option Name.
3. [Enhancements][Rome BIOS] Update Rome B0 stepping CPU microcode 0x0830107A for AMD-SB-7008 Security Bulletin to address CVE-2023-20593 issue.
4. [Enhancements][Rome BIOS] Restore SMCI secure boot keys and Update Secure Boot DB variable. (Unknown certificate "Addtrust External CA Root" is expired on May 30th 2020).
5. [Features][Naples BIOS] Add InBand OemFID support.
6. [Features][Naples BIOS] Add OutBand OemFID support.
7. [Fixes][Naples BIOS] Update OEM FID information for PRICESO_0_DESC and PRICESO_1_DESC.

2.5 (10/25/2022)

1. [Naples/Rome BIOS] Update DBX file to fix Secure Boot Bypass issue
2. [Rome BIOS] Update AGESA RomePI to 1.0.0.E based on 5.14_RomeCrb_0ACMK025.
3. [Rome BIOS] Update Rome B0 stepping CPU microcode 0x08301055 to patch Errata 1161, 1176, 1179, 1183, 1214, 1268, 1386, 1417.
4. [Rome BIOS] Modify Setup string naming from SMCI to Supermicro.
5. [Rome BIOS] Remove "Vendor Keys" in security page.

2.4 (12/28/2021)

1. [Enhancements][Naples/Rome BIOS] Change BIOS revision to 2.4.
2. [Enhancements][Naples BIOS] Update AGESA NaplesPI to 1.0.0.H.
3. [Enhancements][Naples BIOS] Update 8012 ZP-B2 microcode 0800126E.
4. [Enhancements][Rome BIOS] Update AGESA RomePI to 1.0.0.D.
5. [Enhancements][Rome BIOS] Update 8310 SSP-B0 microcode 8301052.
6. [Enhancements][Rome BIOS] Per AMD's suggestion, set Relaxed Ordering default to Enabled.
7. [Enhancements][Rome BIOS] Exposed Setup item "Enhanced Preferred IO Mode".

2.1c (8/28/2020)

1. Changed BIOS revision to 2.1c.
2. Fixed system with 8TB memory support.
3. Fixed CPU speed information is not correct in BIOS SETUP.

2.1b (6/9/2020)

1. Changed BIOS revision to 2.1b.
2. Disabled NVDIMM function to patch HW redundant power failure designed limitation

2.1a (05/14/2020)

1. Changed BIOS revision to 2.1a.
2. Updated AGESA RomePI to 1.0.0.7.
3. Updated 8310 SSP-B2 microcode 8301038.
4. Fixed problem of system hanging on POSTCODE 0xB2 when JPG1 is set to disabled and the VGA card is plugged in.
5. Added SMCI HDD Security feature.
6. Fixed failure of FRU0 - Manufacturer Name(PM) to sync to SMBIOS Type 3 - Manufacturer(CM).
7. Removed "\$SMCUNHIDE\$" string from "PCI AER support" setup item help string.
8. Added AMD IOMMU patch code for fixing problem of NVMe devices dropping and hardware error in RHEL 7.x.
9. Fixed TCG admin password reverse bug.

2.1 (02/21/2020)

1. Changed BIOS revision to 2.1.
2. Updated AGESA RomePI to 1.0.0.5 based on 5.14_RomeCrb_OAC MK013.
3. Displayed "PCI AER Support" setup item on ACPI page.
4. Added SMC HDD Security feature.
5. Removed requirement to use Admin password for erasing TCG device.
6. Fixed issue of system hanging at post code A7h.
7. Fixed inability of SUM to change the function of NUMA Node Per Socket.
8. Fixed problem of system sometimes rebooting during legacy Windows 2019 OS installation when using Rome CPU 7502.

2.0b (11/15/2019)

1. Changed BIOS revision to 2.0b.
2. Added "DRAM Scrub Time" to Memory Configuration
3. Updated AGESA RomePI to 1.0.0.4 based on 5.14_RomeCrb_OACMK012.
4. Set AMD CBS "PCIe ARI Support" item to be used instead of "ARI Forwarding".
5. Updated item string "Input the description" and "HTTP Boot One Time" to adhere to Rome BIOS Setup Template v0.7_20190705.
6. Displayed 3rd IPMI version in BIOS setup.
7. Updated SSID of AMD Host Bridge according to each project's board ID.
8. Forced all PCIe to Gen3 only for H11 drop-in projects.
9. Displayed the "4-link xGMI max speed" setup item and set to 10.667Gbps by default on H11 DP drop-in projects.
10. Set IOMMU default to Auto (Enabled).
11. Displayed "Preferred IO" item.
12. Prevented display of any AMD memory error messages during the POST phase.
13. Fixed malfunction of recovery.

- 14. Added support for OOB SATA HDD information and asset information of 2 SATA controllers.
- 15. Fixed missing screen output when Boot Mode is changed to EFI.
- 16. Fixed problem of system hanging when installing NVidia RTX 2080/5000/6000.
- 17. Fixed the issue of "SMCI POST Screen Message" appearing on BIOS setup menu.
- 18. Fixed the issue of "SMCI POST Screen Message" appearing on POST screen when executing EFI Shell application.
- 19. Fixed problem of the xGMI speed reaching 16G after pressing "load default."
- 20. Fixed problem of the default xGMI speed not being 10.6G

2.0 (8/16/2019)

- 1. First Release.
- 2. Fixed the PXE error message "No enough memory to load an image" when load FTU7.
- 3. Fixed the issue of AOC sensor reading.