

BIOS Release Notes Form

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Product Name	H11DST-B
Release Version	2.8
Release Date	12/14/2023
Build Date	12/14/2023
Previous Version	2.7
Update Category	Recommended
Dependencies	MB Rev 2.00
Important Notes	BIOS Image: BIOS_H11DSTB-098A_20231214_2.8_STDsp.bin BIOS Update Package: BIOS_H11DSTB-098A_20231214_2.8_STDsp.ZIP Note: BIOS R 2.x only supports MB Rev. 2.00, with 32MB SPI flash ROM to support both AMD 7001/7002 series processors.
Enhancements	<ol style="list-style-type: none">1. Updated AGESA RomePI to 1.0.0.H based on 5.14_RomeCrb_0ACMK028.2. Updated AGESA NaplesPI to 1.0.0.L based on 5.013_NaplesCrb_0ACIJ031.3. Updated SA50216_Supplement(LogoFAIL Vulnerability).4. Disabled "Correctable/Non-Fatal error reporting enable" bits when BIOS item "PCI AER support".
New features	NA
Fixes	<ol style="list-style-type: none">1. Fixed MP1(SMU) and CPU WDT uncorrectable error, a "no event log" issue.

Release Notes from Previous Release(s)

2.7 (10/23/2023)

1. Updated AGESA RomePI to 1.0.0.F based on 5.14_RomeCrb_OACMK026 for AMD-SB-1032.
2. Added Rocky Linux boot option name.
3. Updated Rome B0 stepping CPU microcode 0x0830107A from AMD-SB-7008 Security Bulletin to address CVE-2023-20593 issue.
4. Restored SMCI secure boot keys and updated Secure Boot DB variable. (Unknown certificate "Addtrust External CA Root" expired on May 30, 2020).
5. Updated AGESA RomePI to 1.0.0.G based on 5.14_RomeCrb_OACMK027.
6. Changed Pxe from Uefi(U)/Legacy(L) to L/U through Redfish.
7. Added InBand OemFID support.
8. Added OutBand OemFID support.
9. Updated OEM FID information for PRICESSO_0_DESC and PRICESSO_1_DESC.
10. Set RDIMM\LRDIMM\3DSDIMM memory throttling trip-point @85C.

2.5 (10/27/2022)

1. Changed BIOS revision to 2.5.
2. Updated AGESA NaplesPI to 1.0.0.H.
3. Updated 8012 ZP-B2 microcode 0800126E.
4. Updated 8310 SSP-B0 microcode 8301052.
5. Set Relaxed Ordering default to Enabled.
6. Added Setup item "Enhanced Preferred IO Mode".
7. Updated DBX file to fix Secure Boot Bypass issue.
8. Updated AGESA RomePI to 1.0.0.E based on 5.14_RomeCrb_OACMK025.
9. Updated Rome B0 stepping CPU microcode 0x08301055 to patch Errata 1161, 1176, 1179, 1183, 1214, 1268, 1386, 1417.
10. Modified Setup string naming from SMCI to Supermicro.
11. Removed "Vendor Keys" in security page.

2.3 (11/25/2022)

1. Changed BIOS revision to 2.3.
2. Updated AGESA RomePI to 1.0.0.9.
3. Updated 8310 SSP-B0 microcode 830104D.
4. Exposed the items below for GPU performance tuning
 - TSME
 - DDR Power Down Enable
 - PCIe Ten Bit Support

2.2 (8/28/2020)

Changed BIOS revision to 2.2.

Updated AGESA RomePI to 1.0.0.8.

Updated 8310 SSP-B0 microcode 8301038.

Updated AGESA NaplesPI to 1.0.0.D.

Updated 8012 ZP-B2 microcode 0800126C.

Fixed problem of system hanging on POSTCODE 0xB2 when JPG1 is set to disabled and the VGA card is plugged in.

Added SMCI HDD Security feature.

Updated help string of item "Input the description".

Fixed failures of FRU0 - Manufacturer Name (PM) to sync to SMBIOS Type 3 - Manufacturer (CM) and FRU0 - Product Part/Model Number (PPM) to sync to SMBIOS Type 1 - ProductName (PN).

Removed "\$SMCUNHIDE\$" string from "PCI AER support" setup item help string.

Added AMD IOMMU patch code to fix NVMe Devices drop and Hardware error in RH 7.x.
Fixed problem of TCG admin password reverting.
Corrected CPU speed information in BIOS setup.

2.1 (2/21/2020)

1. Changed BIOS revision to 2.1.
2. Updated AGESA RomePI to 1.0.0.5 based on 5.14_RomeCrb_OACMK013.
3. Displayed "PCI AER Support" setup item on ACPI page.
4. Added SMC HDD Security feature.
5. Fixed issue of system hanging at post code A7h.
6. Fixed inability of SUM to change the function of NUMA Node Per Socket.
7. Fixed problem of system sometimes rebooting during legacy Windows 2019 OS installation when using Rome CPU 7502.
8. Removed requirement to use Admin password for erasing TCG device.

2.0b (11/15/2019)

1. Changed BIOS revision to 2.0b.
2. Added "DRAM Scrub Time" to Memory Configuration.
3. Updated AGESA RomePI to 1.0.0.4 based on 5.14_RomeCrb_OACMK012.
4. Set AMD CBS "PCIe ARI Support" item to be used instead of "ARI Forwarding".
5. Updated item string "Input the description" and "HTTP Boot One Time" to adhere to Rome BIOS Setup Template v0.7_20190705.
6. Displayed 3rd IPMI version in BIOS setup.
7. Updated SSID of AMD Host Bridge according to each project's board ID.
8. Set IOMMU default to Auto (Enabled)
9. Displayed "Preferred IO" item.
10. Prevented display of any AMD memory error messages during the POST phase.
11. Fixed malfunction of recovery.
12. Added support for OOB SATA HDD information and asset information of 2 SATA controllers.
13. Fixed missing screen output when Boot Mode is changed to EFI.
14. Fixed the issue of "SMCI POST Screen Message" appearing on BIOS setup menu.
15. Fixed the issue of "SMCI POST Screen Message" appearing on POST screen when executing EFI Shell application.

2.0 (9/12/2019)

Initial Release