

BIOS Release Notes Form

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Product Name	H12DGO-6
Release Version	2.7a
Release Date	03/08/2024
Build Date	03/08/2024
Previous Version	2.6a
Update Category	Recommended
Dependencies	IPMI firmware 01.01.16
Important Notes	<p>ECO #29655</p> <p>BIOS image: BIOS_H12DGO6-1C1E_20240308_2.7a_STDsp.bin</p> <p>BIOS Update package: BIOS_H12DGO6-1C1E_20240308_2.7a_STDsp.zip</p> <p>Notes:</p> <ol style="list-style-type: none">1) BIOS R2.x supports 7002 and 7003 processor.2) Flash Utility in package only supports BIOS to update from R1.x to R2.x, and rolling back is not allowed.3) The default R2.x BIOS boot mode has been changed to EFI. If your OS is legacy, please press the key during POST to enter into the BIOS setup to change boot mode after upgrade to R2.x.
Enhancements	<ol style="list-style-type: none">1. Changed BIOS revision to 2.7a.2. Updated Milan AGESA to 1.0.0.B.3. Applied AMD security patch for SN3002.4. Applied AMD security patch for SN3005.5. Supported XGMI setup option in OOB setup configuration.
New Features	N/A
Fixes	N/A

Release Notes from Previous Release(s)

2.6a (09/27/2023)

1. Changed BIOS revision to 2.6a.
2. Updated CPU UCODE for security update.
3. Updated Milan AGESA to 1.0.0.A.
4. Updated Rome AGESA to 1.0.0.F.
5. Fixed issue where SUM could not get the BIOS configuration file without an HDD installed.
6. Fixed the problem that AMI secure boot key was mistakenly used.

2.5a (04/18/2023)

7. Changed BIOS revision to 2.5a.
8. Updated Rome AGESA to 1.0.0.E.
9. Treated GPU socket as PCIe slot.
10. Fixed the problem that NIC temperatures are missing on BMC WEBGUI.

2.5 (02/14/2023)

11. Updated Milan AGESA to 1.0.0.9.
12. Updated Rome AGESA to 1.0.0.D.
13. Changed BIOS revision to 2.5.
14. Treated GPU socket as PCIe slot.
15. Removed VRM override for security concern.
16. Synced SMBIOS system and chassis info from FRU.
17. Updated BIOS OOB module.
18. Fixed issue that NUMA NPS4 caused system hang up during POS with full load configuration.
19. Fixed issue that system hangs at POST 94 with full configuration system.

2.3a (06/21/2022)

1. Changed BIOS revision to 2.3a
2. Fixed issue of VRM checking failed.
3. Fixed issue of BMC static IP cannot be set.

2.3 (05/09/2022)

1. Changed BIOS revision to 2.3.
2. Updated Milan AGESA to 1.0.0.6 to support Milan-X processor.
3. Used BMC NIC MAC address to generate UUID.
4. Exposed EDC setup options in BIOS setup.
5. Changed PCIe AER setup default to disabled.
6. Remove some KCS command to patch hang 92 issue.
7. Add new AIOM support with NIC device VID 0x8086 DID 0x15ff.
8. Fixed issue of OS generated USB overcurrent warning.
9. Fixed issue of incorrectly display correctable memory error in SEL.
10. Fixed BMC missing problem during CBURN on/off testing.

2.1a (10/22/2021)

1. Changed BIOS revision to 2.1a.
2. Updated AGESA code.
3. Revised workaround for Nvidia-smi GPU lost issue.
4. Changed PCIE S0P2&S1P2 bifurcation to x16.
5. Changed PCI/PCIE Slot2 OPR0M setup string from x16 to x8.
6. Added workaround on PCI/PCIE OPR0M execution.
7. Fixed issue of build date is correct.

8. Fixed issue of BIOS hangs on POST code 92 with full system configuration.

2.1 (07/27/2021)

1. Changed BIOS revision to 2.1.
2. Updated AGESA code for 7002 processor.
3. Updated SMC modules.
4. Set UEFI shell boot order ahead of AIOM PXE.
5. Changed bifurcation of riser slot1 from 2x8 to 1x16.
6. Fixed failure of 4TB memory to boot.
7. Fixed missing Network Configuration in SUM BIOS configuration file.
8. Updated SMBIOS Type 2 board name to H12DGO-6.
9. Fixed failure to eject NVMe4 and 5 under Windows.
10. Fixed support for SMBIOS Type 9 AIOM card.
11. Fixed insufficient resources of some GPUs under Linux.

2.0c (06/03/2021)

1. Changed BIOS revision to 2.0c.
2. Modified UEFI shell boot order ahead of AIOM PXE with Milan CPU.
3. Fixed PCIE Gen drop issue on PLX switch downstream ports.

2.0b (05/24/2021)

1. Changed BIOS revision to 2.0b.
2. Added CPU and NB menus to SUM setup template.
3. Updated Rome/Milan AGESA code to 1.0.0.A/1.0.0.1.
4. Added delay in reading PSU FRU through BMC.
5. Changed system onboard slot number to 11.
6. Added a patch to solve NVidia-smi GPU loss issue with Intel NIC installed on switch board.
7. Added a workaround to assign 32bit resource for AIOM NICs to support legacy PXE.
8. Applied BIOS setup settings pushed by SUM without entering BIOS setup.
9. Changed PCIe port PMEM size.
10. Added support for unique filename.
11. Improved UOL data transfer between BIOS to BMC.
12. Corrected help strings in PCIe/PCI/PNP configuration page.

2.0a (03/25/2021)

1. Changed BIOS revision to 2.0a.
2. Removed unused SATA ports in BIOS setup.
3. Updated SMBIOS TYPE 9 and 40 table.
4. Treated AIOM as onboard NIC.

2.0 (03/04/2021)

1. Changed BIOS revision to 2.0.
2. Added support for both AMD EPYC 7002 and next generation processors.
3. Added support for Nvidia A100 80G GPU.
4. Improved SMBIOS Type 39 PSU reading procedure.
5. Added xGMI setup options.
6. Fixed problem of NVMe hot-swap resetting the system.
7. Fixed inability to enable ECC reporting by Cburn.

1.0a (12/10/2020)

1. Changed BIOS revision to 1.0a.
2. Provided all of AIOM NIC MAC addresses to BMC.
3. Fixed issue of CPU2 dropping.

1.0 (11/13/2020)

First release.