# **BIOS Release Notes Form**

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Product Name	X12DSC-6
Release Version	2.1 SPS: 4.4.4.603
<b>Build Date</b>	07/04/2024
<b>Previous Version</b>	1.6
<b>Update Category</b>	Recommended
Dependencies	None
Important Notes	None
Enhancements	<ol> <li>Updated kernel to 5.22_WhitleyCrb_OACMS_ICX_079_BETA (2024.3 IPU).</li> <li>Applied AMI SA50232 to address below security vulnerability.</li> <li>Applied SA50218 supplement (Vulnerability in EDK2 NetworkPkg).</li> <li>For SA50243 (CVSS3.1 (7.5, High)), fixed that UsbRtSmm module has a TOCTOU vulnerability.</li> </ol>
New features	None
Fixes	<ol> <li>Resolved IPMI PXE boot fails after installing OS.</li> <li>Resolved boot order cannot be adjusted by AMI SCE.</li> </ol>

#### 1.6 SPS: 4.4.4.500 8/11/2023

- 1. Updated BIOS revision to 1.6.
- 2. Updated 5.22\_WhitleyCrb\_OACMS\_ICX\_076\_BETA (IPU-PV 2023.3).
- 3. Changed the strings from "Lock mode" to "Lockdown mode".
- Fixed ENERGY\_PERF\_BIAS\_CFG Mode item change to default after SUM update BIOS with -preserve\_setting command.

# 1.4b SPS: 4.4.4.301 (1/24/2023)

- 1. Updated 5.22\_WhitleyCrb\_OACMS\_ICX\_74 Intel BKCWW46 IPU2023.1, please check header for firmware revisions.
- 2. Updated Intel Server Platform Services for Whitley Server Platforms IPU2023.1 4.4.4.301
- 3. Updated VROC SATA/sSATA EFI driver to VROC PreOS v7.8.0.1012
- 4. Updated M.2 slot BDF for OPROM control and SMBIOS BDF by following AOM silkscreen.
- 1. Fixed an issue where memory errors were not being reported in the SMBIOS event log.

# 1.4 (7/12/2022)

- 1. Updated SmcOOB to the version "\_SMCOOBV1.01.25\_" to fix the unexpected system-reseting when loading NVRAM defaults.
- 2. Updated 5.22\_WhitleyCrb\_OACMS\_ICX\_72.1 Intel BKCWW23 PLR3.
- 3. Updated VROC SATA/sSATA EFI driver to VROC PreOS v7.7.6.1004 to adress INTEL-TA-00692.
- 4. Added support for IPMI PXE boot to all LAN ports feature for both Legacy and UEFI PXE.
- 5. Added Link Retrain per port item in BIOS.

# 1.2 (3/5/2022)

- 1. Updated AMI 5.22 WhitleyCrb OACMS ICX 070 RC27P56 to BKC 2021 WW52 (PLR1 HF).
- 2. Added flag [Preserve BIOS Boot Options Configuration] controlled by BMC/SUM.
- 3. Changed string "VMX" to "Intel Virtualization Technology".
- 4. Change BIOS revision to 1.2.
- 5. Removed 1G option from MMCFG base to avoid system hang.
- 6. Fixed the SMBIOS event log ERROR CODE which is not displaying correctly under BIOS menu issue (EFI error type).
- 7. Fixed SpeedStep (P-States) setting, changed when its default setting is set to Disable, and load BIOS defaults in Setup.
- 8. Fixed COM port resource that can't be changed and item's behavior failure.

# 1.1a (7/21/2021)

- 1. Updated 5.22\_WhitleyCrb\_OACMS\_ICX\_066\_BETA Intel BKCWW24 2021 PV MR4.
- 2. Updated Dx/Mx microcode from Intel-Generic-Microcode-20210701\_NDA.

3.	Updated SmcOOB module to 1.01.24 to support SUM clean SMBIOS Event log through BiosCfg header flag.	
4.	Corrected firmware version and vendor on Trusted Computing page.	
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Product Manager Date		