

BIOS Release Notes Form

Supermicro disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade. All products, computer systems, dates, and figures specified are preliminary based on current expectations, and are subject to change without notice. Supermicro and the Supermicro logo are trademarks of Super Micro Computer, Inc. in the U.S. and/or other countries. Copyright © 2018 Super Micro Computer, Inc. All rights reserved.

Product Name	H12DGQ-NT6
Release Version	3.0
Release Date	10/01/2024
Build Date	10/01/2024
Previous Version	2.8
Update Category	Recommended
Dependencies	IPMI 01.04.02
Important Notes	ECO#30693 BIOS image: BIOS_H12DGQ-NT6-1C5D_20241001_3.0_STDsp.bin BIOS Update package: BIOS_H12DGQ-NT6-1C5D_20241001_3.0_STDsp.zip
Enhancements	1. Update MilanPI to 1.0.0.D based on 5.22_MilanCrb_0ACOU027. 2. Update RomePI to 1.0.0.J based on 5.14_RomeCrb_0ACMK032.
New features	N/A
Fixes	1. Fixed issue where cannot boot to remote iso file. 2. Fixed issue where BIOS setup cannot be displayed in SOL.

Release Notes from Previous Release(s)

2.8 (03/06/2024)

1. Updated MilanPI to 1.0.0.C based on 5.22_MilanCrb_0ACOU024.
2. Updated RomePI to 1.0.0.H based on 5.14_RomeCrb_0ACMK0278.
3. Fixed issue where early video logo failed to display.
4. Fixed issue where Tab, F11 and F12 information was not displayed on POST screen.

2.7a (12/18/2023)

1. Updated MilanPI to 1.0.0.B based on 5.22_MilanCrb_0ACOU022.
2. Updated RomePI to 1.0.0.G based on 5.14_RomeCrb_0ACMK027.
3. Updated SEV firmware to version 1.55.16 (Hex 1.37.10 SPL = 0x14).

2.6b (11/02/2023)

1. Restored SMCI secure boot keys and Update Secure Boot DB variable.
2. Fixed issue where the HPET setting could not be kept after BIOS was upgraded via SUM.
3. Fixed issue where only the first AOC NIC card setting could be modified.
4. Fixed issue where IOMMU still showed up in the PCIe list on the first reboot after BIOS flash.

2.6a (08/10/2023)

1. Modified the default BIOS settings to fine-tune performance, including:
 - SMT Control->Disable,
 - Determinism Control->Manual,
 - Determinism Slider->Power,
 - IOMMU->Disable,
 - APBDIS->1,
 - Fixed SOC Pstate->P0,
 - NUMA nodes per socket->NPS1,
 - TSME->Disable,
 - DDR Power Down Enable->Disable,
 - PCIe Ten Bit Tag Support->Enable,
 - All Root Complex LCLK, Frequency->593MHz,
 - xGMI Link Width Control->Manual,
 - xGMI Force Link Width Control->Force,
 - xGMI Link Max Speed->18GBPS,
 - SR-IOV->Enable.
2. Added CVE-2023-20593.
3. Added AMD Erratum 1468.
4. Updated MilanPI to 1.0.0.A based on 5.22_MilanCrb_0ACOU021.
5. Updated RomePI to 1.0.0.F based on 5.14_RomeCrb_0ACMK026.
6. Updated Milan B0/B1/B2 stepping CPU microcode and Milan-X B2 stepping CPU microcode for AMD-SB-7005 Security Bulletin to address the CVE-2023-20569 issue.
7. Added the LEGACY option to the Boot Mode Select list.
8. Fixed the issue where HPET and Power Loss Option names were not consistent with the setup template.

9. Fixed the issue where the default values of SMBIOS Type1 version and serial number were not consistent with the SMBIOS template.
10. Fixed the incorrect display of the LCLK item missing while no storage drives were connected.

2.5 (03/07/2023)

1. Updated MilanPI to 1.0.0.9 based on 5.22_MilanCrb_0ACOU020.
2. Updated RomePI to 1.0.0.E based on 5.14_RomeCrb_0ACMK025.
3. Followed the SMBIOS template to be in sync with the chassis type from FRU0 to SMBIOS Type 03.
4. Removed the feature of syncing time zone with BMC.
5. Removed the function of VRM modifying cmd in BIOS.
6. Modified the GPU slot ID in type 9 to start from 0x01.
7. Fixed issue with KMS settings could not be kept when SUM tool was used to load default BIOS settings.
8. Added the Factory Mode item to BIOS.
9. Fixed the issue where Samsung memories could not show full serial number in SMBIOS type 17.
10. Fixed the issue with some systems hung at 0x92.

2.4 (08/23/2022)

1. Added LCLK item in BIOS Setup.
2. Modified TPM item to set 'Enabled' as default.
3. Updated MilanPI to 1.0.0.8 based on 5.22_MilanCrb_0ACOU019.
4. Updated RomePI to 1.0.0.D based on 5.14_RomeCrb_0ACMK023.
5. Updated for the UECC/CECC event log failed and did not display DIMM's location.
6. Fixed issue where IRQ number is showing -2147483648.
7. Added slot ID report on SMBIOS type 40 VPD.
8. Modified the AIOM2 slot OPROM showing slot9 in BIOS setup.
9. Fixed BIOS boot order priorities failure by IPMI tool.
10. Fixed UUID which shows AIOM2 mac while installing AIOM1 and AIOM2 at the same time.
11. Fixed system hang up when first booting after upgrading the BIOS while plugging memory on P1-DIMMD2.
12. Fixed HDD LED random blinking.

2.3 (04/25/2022)

- 1.Changed BIOS revision to 2.3.
- 2.Added AIOM card detection function.
- 3.Display "Correctable Error Threshold" while using sum tool to get BIOS config.
- 4.Added BBS item while use sum tool to get BIOS config.
- 5.Fixed issue where BIOS is showing an incorrect memory location in BIOS event log while CECC is occurring.
- 6.Added KMS module.
- 7.Sync Product serial number with FRU data.
- 8.Fixed issue where the UUID is not showing the BMC MAC address while system has no AIOM card.
- 9.Fixed issue where Type39 will show default value only sometime.
- 10.Fixed issue where NVME device Secure Erase fails.
- 11.Fixed issue where there is no BIOS recovery showing in the BMC log.
- 12.Fixed issue where the BMC update UI shows an incorrect BIOS Version.
- 13.Fixed issue where the HDD LED will not blink.
- 14.Fixed KMS test failure.
- 15.Fixed USB Exposed Port System Test failure.
- 16.Temporarily disabled eSATA to fix HDD LED random blinking.
- 17.Fixed issue where some AIOM LAN MAC address cannot update to UUID.
- 18.Fixed issue where the GPU slot always shows in-use.
- 19.Fixed UECC error that is not showing error location in BMC Web UI.

2.0 (02/16/2022)

First release.