



AMD Instinct™ MI300X Firmware Changes

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Chapter 1: Introduction

This document provides release notes for the AMD Instinct™ MI300X accelerator firmware, including enhancements and key fixes.

★ **Important:** The MI300X firmware bundle 24.13 requires an upgrade to [ROCm 6.2.2](#). Bundles 24.11 and 24.12 require an upgrade to ROCm 6.2 or later builds. For assistance with the update process, contact AMD Support.

Chapter 2: Release Contents

The following table details the versions of the constituent firmware components in this release. Highlighted versions have changed since the previous release. Certain versions may remain unchanged across different releases.

Table 2.1: Firmware Bundle Component Versions

| Location | Component | Component Details | Previous Component Details | Version Number |
|----------|-------------------|---------------------------|----------------------------|----------------|
| Bundle | bundle_active | 01.24.13.12 (Production) | 01.24.13.09 (Production) | 0x01180D0C |
| Bundle | ROT | 00730000 (Production) | 00730000 (Production) | 0x00730000 |
| Bundle | IFWI | 01.05.123529 | 01.05.120810 | 0x1051E289 |
| Bundle | SMC | 2.11.0.68.5F (Production) | 2.11.0.66.4F (Production) | 0x0100445F |
| Bundle | SMC FPGA | 00.26.10 | 00.26.10 | 0x00026010 |
| Bundle | UBB FPGA | 00.26.13 | 00.26.13 | 0x00026013 |
| | | 00.26.24 | 00.26.24 | 0x00026024 |
| Bundle | Retimer | 2.11.062 | 2.11.062 | 0x0200B03E |
| | | 2.11.076 | 2.11.076 | 0x0200B04C |
| | | 2.11.077 | 2.11.077 | 0x0200B04D |
| | | 2.11.078 | 2.11.078 | 0x0200B04E |
| Bundle | Remote Management | 4.0.12 | 4.0.12 | 0x0400000C |
| Bundle | VR | 01.009 | 01.009 | 01.009 |
| | | 02.0BE | 02.0BE | 02.0BE |

Chapter 3: Enhancements

The following table lists the enhancements in this release sorted by component.

Table 3.1: Enhancements

| AMD Ref ID | Component | Description |
|------------|-----------|--|
| U0659 | SMC | Persistent SMC journalctl log |
| U0752 | SMC | <p>New Redfish events indicating mode-1 reset occurred and GPU operation restored</p> <p>mode-1 reset occurred event definition:</p> <p>Category of Event: GPU Error Events</p> <p>Sub categories: GPUReset</p> <p>Message Registry:ResourceEvent.1.2.1</p> <p>SecureFWVerifySuccess:InternalResetOccurredOEM</p> <p>OriginOfCondition:/redfish/v1/Chassis/OAM_X where X is 0,1...,7</p> <p>Message: The resource property <Arg1> has detected an internal reset of type '<Arg2>:',</p> <p>Arg1: "OAM_X" where X is 0,1...,7</p> <p>Arg2: "modeX" where X is 1,2</p> <p>Severity: OK</p> <p>RF URI:/Systems/UBB/LogServices/EventLogs/</p> <p>GPU operation restored event definition:</p> <p>Category of Event: GPU Error Events</p> <p>Sub categories:GPUBoot</p> <p>Message Registry: ResourceEvent.1.2.1</p> <p>SecureFWVerifySuccess: GpuOperationRestoredOEM</p> <p>OriginOfCondition: /redfish/v1/Chassis/OAM_X where X is 0,1...,7</p> <p>Message: "The resource property <Arg1> operation restored."</p> <p>Arg1:"OAM_X" where X is 0,1...,7</p> <p>Severity: OK</p> <p>RF URI:/Systems/UBB/LogServices/EventLogs/</p> |

Chapter 4: Key Fixes

The following tables list the key fixes in this release sorted by component.

The Fixed Known Issues table lists known issues that were previously documented and have been resolved in this release. The Fixed New Issues table lists issues that were not previously documented but have been resolved in this release.

Table 4.1: Fixed Known Issues

| AMD Ref ID | Component | Issue | Fix |
|------------|-----------|---|--|
| B1379 | SMC | IFWI update can fail in rare cases. Separate symptom: Rare critical self-test failure on WAFL link training or unhandled boot error following firmware update | Ensure IFWI, VR, and RM programming are complete before voltage rail is cycled |
| B1405 | SMC | Log retrieval may fail if system has unreachable OAM or re-timer | Add additional error checking in the logic that collects logs |
| U0258 | SMC | ForceRestart and ResetAll Redfish actions not working | Ensure files required for booting are always restored after reset to defaults |
| U1078 | SMC | Slow Redfish response times when there is a flood of CPERs | Improve DBUS and I2C utilization |
| P164171 | IFWI | Address ODECC HBM CEs | HBM stability improvements |

Table 4.2: Fixed New Issues

| AMD Ref ID | Component | Issue | Fix |
|------------|-----------|---|--|
| B1319 | SMC | UBB FPGA or SMC FPGA may report health status as warning | Corrected logic to report health status appropriately |
| B1459 | SMC | RM version number in Redfish event during firmware update is always 0 | RM version number in Redfish event is reported correctly, matching the version of the RM in the bundle |

Table 4.2: Fixed New Issues (continued)

| | | | |
|-------|-----|---|---|
| U1017 | SMC | VR configuration not being updated on some part numbers | Added OAM and UBB part numbers to SMC FW lookup tables |
| U1060 | SMC | SplitCPER=false setting is not properly supported | Remove support for SplitCPER=false |
| U1064 | SMC | Unable to determine when SMC firmware is resetting due to mount issue | SMC firmware will delay sending the pre-existing Redfish event, BootSuccess secure, until successful access is verified to the flash partition. |

Appendix A: Notices

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