

# BIOS Release Notes Form

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<b>Product Name</b>	<b>H13SSF</b>
<b>Release Version</b>	<b>3.0a</b>
<b>Release Date</b>	<b>01/21/2025</b>
<b>Build Date</b>	<b>12/06/2025</b>
<b>Previous Version</b>	<b>3.0</b>
<b>Update Category</b>	<b>Recommended</b>
<b>Dependencies</b>	<b>BMC: 01.03.39</b> (BMC_H13AST2600-ROT20-E401MS_20241208_01.03.39_STDsp)
<b>Important Notes</b>	<b>BIOS_H13SSF-1D16_20241206_3.0a_STDsp.bin - BIOS image</b> <b>BIOS_H13SSF-1D16_20241206_3.0a_STDsp.zip - BIOS image and flash utility package</b>
<b>Enhancements</b>	NA
<b>New Features</b>	NA
<b>Fixes</b>	<ol style="list-style-type: none"><li>1. Fixed the issue that the system's Slot1/Slot2 NAOC did not display on the BMC Network AOC page.</li><li>2. Fixed the SATC automation test failure by enabling HTTP boot certificate function for BMC.</li><li>3. Fixed the failure while checking HDD information in BMC due to the "Controller Name" &amp; "Configuration Type" Error.</li><li>4. Fixed the failure in Compare Event Log when downgrading to a previously released BIOS.</li><li>5. Fixed the BIOS ECO automation test failure.</li></ol>

## Release Notes from Previous Release(s)

<b>1.0 (2022/07/19)</b> First release.
<b>1.6 (10/31/2023)</b> Enhancements: 1.Modified the riser card setup string to align other Genoa projects.

- 2.Integrated ProjectSmbios.c to let code dynamically generate SXB-related items.
- 3.Added riser card RSC-X2R-68G5 SMBIOS Type 9 & 40 support.
- 4.Enabled M.2 slot to report SMBIOS Type 9 & 40.
- 5.Changed BIOS version to 1.6 for AGESA1009.
- 6.Added Secure Boot Data Based(Digital Ocean) feature.
- 7.Added Amd CbsConfig load default process before NVRAM reset.
- 8.Added GenoaPI-SP5\_1009\_Patch\_Rev2.
- 9.Added the SuperMicro secure boot key.
- 10.Added embedded uefi() shell. Added GotoSetup command.
- 11.Added ASM1061 workaround to disable ASM1061 64-bit memory address capability.

Fixes:

- 1.Fixed some problems for Riser card RSC-X2R-68G5.
- 2.Fixed an issue with NVQual utility not working properly.
- 3.Fixed an issue where SUM cannot execute ChangeFixedBootCfg successfully.
- 4.Fixed ASG-1115S-NE316R AC stress failed and hung on POST code "A2".
- 5.Fixed the 2U Type2 system U.2 NVMe HDD detect function fail issue.
- 6.Fixed H13SSF 2U Type1 SKU system may hang at 91 when 1st boot up after removing the CPUEVAC heat sink.
- 7.Fixed ASG-2115S-NE332R memory information under BIOS setup is incorrect.

**1.6b (01/25/2024)**

Enhancements:

- 1.Added a HW workaround to modify the EQ parameters of re-driver on RSC-X2R-68G5 to fix the H13SSF 2U system experiencing the CX6 and CX7 Lan card Gen drop issue during DC On/Off testing.
- 2.Filled in the SSID for AMD Host Bridge Bus:0, Dev:0, Fun:0.
- 3.Updated SmcKMS feature. Corrected SUM setup questions & forms containing SMCI strings.
- 4.Preserved BIOS Config function. Fixed BIOS flash failed to preserve original settings issue when update BIOS between different AGESA revision and set preserve option.
- 5.Added "SEV Control" and "ACPI CST C2 Latency" items under the BIOS setup page. Update for AMD H13-Genoa\_Siena BIOS Setup Template v0.9\_20231020.
- 6.Updated secure boot KEK/DB to add new Microsoft certificate.
- 7.Updated the P0 and P1 NVMe hot plug table and CPM table according to the Board ID of the new MB R1.02. Workaround for EE update new MB R1.02 to fix the PCIe signal bad issue. Fixed the NVMe ordering issue for P0 and P1. Fixed issue with PB 160092 Device (slot to device ID mapping) being out of order.
- 8.Added workaround to fix 2U SKU 2 G1 U.2 NVMe hot plug failed issue.
- 9.Updated BIOS auto-detection for 2U Type2 SKU to read the middle BPN ID(BPN-NVMe5-HS219N-S8). (If the middle BPN ID is "0x1304", it is a 2U Type2 SKU.)

Fixes:

1. Fixed some issues with RSC-X2R-68G5.
- 2.Fixed an issue where the BMC UI network AOC would not detect the PCIe network adapter when the riser (RSC-X2R-68G5) jumper is set to 1 and 2.
- 3.Fixed multiple failures when running ATT SAAECO.
- 4.Fixed an issue where the AOC card sensor reading would not work when installed on RSC-X2R-68G5 SLOT2.
- 5.Fixed an issue with the AOC-S100G-b2C sensor and health status is abnormal on BMC Web GUI.
- 6.Fixed an issue where the system kept rebooting with a "Hardware Error" message during OS boot-up.

**1.9 (07/05/2024)**

#### Enhancements:

- 1.Updated the BPN SKU auto-detect function for new MB 1.02a (Board ID=11).
- 2.Updated the BIOS codebase to CRB25/AGESA 1.0.0.C and Synced up SmcPkg/ProjectPkg override files.
- 3.Corrected the key string typo under SupportedSystems.
- 4.Supported BMC (IPMI 30\_68\_F9 bit4) to control QID (serial number) existence for BIOS Attribute.
- 5.Added support for new Type5 1U 20-bay SKU.

#### Fixes:

- 1.Fixed IPv6 string incomplete display issue (missing the last group of numbers) on PEI early video.
- 2.Fixed the issue where the default boot order is restored incorrectly after using SMCIPMITool to set boot device.
- 3.Fixed the G1 SXB2 and G3 SXB1 manual bifurcation setting failure issue.
- 4.Fixed SPPT-related issues.
5. Fixed the issue that "Dmesg" shows some fail & error logs after installing Linux Redhat 9.3.
- 6.Fixed some automation test failures.
- 7.Fixed the issue that PCIe devices 14aa and ASM1062 dropped out during Linux reboot tests.
- 8.Fixed power-on stuck at POST code A2 after reboot by SUM SecureBootManage --action ResetAllKeysToDefault.
- 9.Fixed disqualified Intel MLC Benchmark performance for CXL slots 0-2.
- 10.Fixed the unexpected hot plug detection failure issue.
- 11.Fixed some SATC Redfish Automation test failures.
- 12.Fixed the issue that BMC seems not to control FAN when AOC Temperature exceeds threshold.
- 13.Fixed the issue where the system sticks at POST 92 during AC cycle tests.

### **3.0 (11/05/2024)**

#### Enhancements:

- 1.Set the CXL Gen5 preset to 0x0A as per EE request to work around the RX margin signal issue.
- 2.Enlarged the pFabricResource->PrefetchableMmioSizeAbove4G to 4T for compatibility with new CXL devices.
- 3.Set Motherboard\_TDP\_Protection\_Support =1 to limit CPU power over 400 W. The BIOS will post a message and shut down the system if a 500 W CPU is installed.
- 4.Added support for HTTP TLS CERT and changed HTTP TLS confirmation to JSON from HII.

#### New Feature:

- 1.Dual BIOS supported both AMD EPYC™ 9004/9005 Series Processors (Note: AMD EPYC™ 9005 series drop-in support required motherboard revision 2.0 or above).

#### Fixes:

- 1.Fixed an issue where the MB ID auto-detection failed to set correct CPLD hotplug tables according to the MB ID.
- 2.Fixed the issue with Kioxia CD8-P E3.S failed hot-plug in Windows OS.
- 3.Fixed the failed hot-plug test in Linux OS with BIOS Version 1.9.
- 4.Fixed the incorrect SATA controller name in OOB data for the ASMedia SATA controller.
- 5.Fixed the issue that the system kept rebooting after changing the BIOS settings.
- 6.Addressed the failure to check HDD information in BMC due to "Controller Name" & "Configuration Type" errors.
- 7.Fixed the SOL function failure.
- 8.Fixed the issue that IPMI list AIOM NIC card sensor readings and information.
- 9.Fixed the issue causing SATC automation tests to fail.

10. Corrected the problem preventing the System Diagnostics test from running, where the system got stuck at POST code A2.

11. Fixed the issue where "Secure Boot Support" settings were not preserved.