

IPMI Firmware / BIOS Release Notes Form

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Product Name	H12SSW-AN6
Release Version	3.2
Release Date	01/24/2025
Build Date	01/24/2025
Previous Version	3.0
Update Category	Recommended
Dependencies	N/A
Important Notes	ECO #31187 BIOS Image: BIOS_H12SSWAN6-1C1F_20250124_3.2_STDsp.bin - BIOS image BIOS Update Package: BIOS_H12SSWAN6-1C1F_20250124_3.2_STDsp.zip Please update BIOS with attached Flash Utility in package.
Enhancements	1. [Rome] Update AMI Modules based on 5.14_RomeCrb_0ACMK032 ; Security Update for SA50158 and Update AGESA RomePI to 1.0.0.L. 2. Update AMI Modules based on (BETA)5.22_MilanCrb_0ACOU029 Part II ; Update AGESA RomePI to 1.0.0.F.
New features	N/A
Fixes	N/A

Release Notes from Previous Release(s)

Revision 3.0 (2024/07/20)

1. Update AMI Modules based on 5.14_RomeCrb_0ACMK032 ; Security Update for SA50158 and Update AGESA RomePI to 1.0.0.J.
2. Update AMI Modules based on 5.14_RomeCrb_0ACMK031 ; Security Update for SA50258.
3. Fill DUID with UUID to avoid all system's DUID in IPv6 DHCP is the same ; The DUID in IPv6 DHCP should be unique.
4. Update AGESA MilanPI to 1.0.0.D based on 5.22_MilanCrb_0ACOU027.
5. Update AMI Modules based on 5.14_RomeCrb_0ACMK029
6. Update AMI Modules based on 5.14_RomeCrb_0ACMK030 ; Security Update for SA50221,SA50229,SA50232,SA50236 and SA50243.
7. Add AMI Resizable BAR support; AMI added this function in the PCI BUS module label PciBus_25. The Rome code base currently uses PciBus_19, and Milan code base uses PciBus_24. It needs to integrate it into the Rome code base additionally.
8. Update AMI Modules based on (BETA)5.22_MilanCrb_0ACOU025 ; Security Update for SA50229,SA50232,SA50236 and SA50243.
9. Add SMC_OS_PERFORMANCE_ENHANCE item ; Toggle CPU freq to the highest
10. Set Chassis type in smbios to default (0x11) when Chassis type from fru is 0x00 or 0xFF or NULL.

Revision 2.8 (2024/01/15)

1. Update AGESA RomePI to 1.0.0.H based on 5.14_RomeCrb_0ACMK028.
2. Update AGESA RomePI to 1.0.0.C based on 5.22_MilanCrb_0ACOU024.
3. Update Milan SEV FW version from 1.37.7 to 1.37.10 for AMD-SN-3005: AMD INVD Instruction Security Notice.

Revision 2.7 (2023/10/31)

1. [Milan]Update MilanPI to 1.0.0.B based on 5.22_MilanCrb_0ACOU022.
2. [Rome]Update AGESA RomePI to 1.0.0.G based on 5.14_RomeCrb_0ACMK027.
3. Upgrade Milan Agesa code base to 1.0.0.B and Rome Agesa code base to 1.0.0.G for AMD Security Notices CVE 2023-SN-3002.
4. Fixed issue for Default Bios Config does NOT match with Current BIOS Config.

Revision 2.6a(2023/08/08)

1. Update Milan B0/B1/B2 stepping CPU microcode and Milan-X B2 stepping CPU microcode for AMD-SB-7005 Security Bulletin to address CVE-2023-20569 issue.
2. Update Rome B0 stepping CPU microcode 0x0830107A for AMD-SB-7008 Security Bulletin to address CVE-2023-20593 issue.

Revision 2.6(2023/04/18)

1. Update Milan AGESA to 1.0.0.A
2. Update Rome AGESA to 1.0.0.F

Revision 2.5(2022/10/06)

1. Update Milan AGESA to 1.0.0.9
2. Update Rome AGESA to 1.0.0.E
3. Follow the SMBIOS template and sync the chassis type from FRU0 to SMBIOS Type 03.
4. Fixed the system can't boot to UEFI USB KEY with command 'ipmi power bootoption 11'.
5. Fixed Update SMBIOS type 41 data. Change "NEC USB" string to "Renesas USB"

Revision 2.4(2022/02/23)

1. Update Milan AGESA to 1.0.0.8
2. Update Rome AGESA to 1.0.0.D
3. Fixed the system randomly hanged postcode "0x00"/"0xCd"/"0x4b" when the system runs AC On/Off burning test.
4. Fixed the system hanged postcode "AC" when TPM 2.0 module is installed.
5. Fixed the MAC address of AOC-A25G-i2SM that can't show in BMC Web UI.
6. Fixed Milan and Milan-X CPU can't detect AMD GPU MI210 populated on slot-1 but Rome can.
7. Change each root bridge MMIO size from 16M to 64M to fix out of resource issue.

Revision 2.3 (2021/10/01)

1. Update Milan AGESA to 1.0.0.6
2. Update Rome AGESA to 1.0.0.C

Revision 2.2 (2021/08/09)

1. Followed the new cable plan to change the G1 & G2 PCIe config.
2. New AIOM card (AOC-A100G-m2CM) is Support; BIOS supports this AIOM card for BMC sensor reading.
3. When the AIOM1 changed device, the BMC gets a reset. However, it causes the BIOS cannot update the OOB file successfully.
4. Update BIOS version from 2.1 to 2.2
5. USB controller PCIe gen. speed fix from PCIe Gen. 1 to PCIe Gen. 2

Revision 2.1 (2021/07/12)

fixes the BIOS version shows incorrectly

Revision 2.0 (2021/05/25)

First release.