

BIOS Release Notes Form

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|------------------|---|
| Product Name | H13SSW |
| Release Version | 3.4b |
| Release Date | 03/13/2025 |
| Previous Version | 3.4a |
| Update Category | Recommended |
| Dependencies | IPMI 01.03.11 |
| Important Notes | <p>BIOS_H13SSW-1C85_20250313_3.4b_STDsp.bin - BIOS image BIOS_H13SSW-1C85_20250313_3.4b_STDsp.zip - BIOS image and flash utility package.</p> <p>For AMD EPYC™ 9005 Series Processor support, please ensure your motherboard revision is R2.0 or higher. The BIOS and BMC package versions should be BIOS (V3.0 or higher) and BMC (V01.03.03 or higher).</p> <p>If you need to roll back the BIOS from V3.x (AMD EPYC™ 9004/9005 Series processors) dual BIOS to V1.x (AMD EPYC™ 9004 Series processor) single BIOS, make sure to install an AMD EPYC™ 9004 Series processor before updating the BIOS.</p> |
| Enhancements | N/A |
| New Features | N/A |
| Fixes | 1. Patched PCIe switch card no detect issue on 2U Slot2. |

Release Notes from Previous Release(s)

3.4a (2025/03/07)

[Fixes]

1. Fixed the issue that System hangs when enter into the setup page of BROADCOM <SAS 3916> Configuration Utility.
2. Fixed the issue that disabled LongSerialNumber support.

3.4 (2025/02/13)

[Enhancements]

1. Updated to 5.35_OACST_PI1.0.0.3_017 for Turin AGESA 1.0.0.3.
2. Added TurinPI_SP5_1003_Patch_1.
3. Updated to 5.27_GenoaCrb_OACQT027 for Genoa AGESA 1.0.0.E.
4. Added support for early video in Graphics mode.
5. Updated to 5.35_OACST_PI1.0.0.1_015 for Turin AGESA 1.0.0.1.
6. Added support for HTTP TLS certificates.
7. Updated to 5.27_GenoaCrb_OACQT026 for Genoa AGESA 1.0.0.D.
8. Updated SEL support for MBIST.
9. Modified ROT signature region to include FV_MAIN and FV_BB in both the top and bottom binaries.

[Fixes]

1. Fixed issue where legacy mode would get stuck at B2.
2. Fixed issue where the second LAN port of two controllers in AIOM2 was not displayed.
3. Fixed issue where the "Secure Boot Mode" BIOS attribute setting could not be obtained using the "GetCurrentBiosCfg" option.
4. Fixed issue where IPv6 configuration was inconsistent between the BIOS and Web UI.
5. Fixed issue where the DNS IP was displayed incorrectly when set in the Web UI.
6. Fixed issue where AIOM2 SMBIOS Type 9 information was missing.

3.0 (2024/09/23)

[Enhancements]

1. Added AMD EPYCTM 9005 Series Processor PI-SP5_1.0.0.0_Patch_3.
2. Added AMD Memory patch for BIOS ROM image of AMD EPYCTM 9004 Series Processor.

[New feature]

1. The dual image of BIOS 3.0 supports both AMD EPYCTM 9004/9005 Series Processor.

1.9 (2024/05/28)

[Fixes]

1. Fixed SOL not working.
2. Fixed IPv6 string incomplete display issue on PEI early video.
3. Updated SmcCorrMemErrEn related code to optimize CECC control during DXE phase.
4. Disabled "Correctable/Non-Fatal error reporting enable" bits when BIOS item "PCI AER support" set Disabled.

1.6b (2024/03/21)

[Fixes]

1. No detection on 1G ports(Intel X550) of AOC-AH25G-M2S2TM-O
2. Supported 3rdParty AOC card Mac report to Type40 with SuperMicro format.
3. Fixed Redfish SATC boot option test for FixedBootOrder.
4. Fixed IPv6 string incomplete display issue(miss the last group of numbers) on PEI early video.
5. Fixed default boot order was restored incorrectly after using SMCIPMITool for boot device setting

1.6a (2024/01/24)

[Enhancements]

1. Generate the duplicate setting 'ASPM Support' with same order via sub command

GetCurrentBiosCfg

2. Force Attribute include serial number.
3. Preserve BIOS Config function.
4. Support 16 bit Post code.
5. 3rd party card may stuck at FRU reading in PEI phase.
6. System hang up at 0xA2 when NVMe namespace over 100

[Fixes]

1. Fixed LAN port lost issue when installed two controllers of AIOM
2. Fixed load default function can not work via Oob.
3. Fixed Redfish SATC boot option test for FixedBootOrder.

1.6 (2023/10/05)

[Enhancements]

1. Upgraded BIOS version to 1.6 for AGESA1009.
2. Added GenoaPI-SP5_1009_Patch_Rev2.
3. Added SuperMicro secure boot key.
4. Disabled PMIC error message reporting.
5. Supported Build-in SuperDiag.
6. Supported SPD DATA to ACPI.
7. Enabled AMD ADDC feature.
8. Added Amd CbsConfig load default process before NVRAM reset.
9. Patched AOC-S25GC-i2S/i4S sensor.

[Fixes]

1. Fixed memory reporting log when memory is not installed.
2. Fixed BIOS setting display issue with sum tool when setting Boot URI.
3. Fixed ChangeFixedBootCfg command via SUM.
4. Fixed string overwrite issue after CMOS reset.

1.5 (2023/07/25)

[Enhancements]

1. Upgraded BIOS version to 1.5 for AGESA1007.
2. Added support for redfish API Oem/Supermicro/FixedBootOrder.
3. Added an option to hide the Local APIC Mode.
4. Corrected the "xGMI Force Link Width Control" & "xGMI Force Link Width" settings.
5. Fixed the problem no event logs generated in IPMI Web after "DRAM UECC Retry Error Injection" was run.
6. Changed the ASPM setting to not sync to the PCIe port.
7. Changed the "Power Off" string to "Stay off."
8. Synced up Core control option items
9. Disabled the ASPM root port to fix the problem of single disk IO in which SAS card was installed in Slot 2.

[Fixes]

1. Fixed the issue in which the AMD cbs_config item did not align with APCB and caused systems hang in the early video phase.
2. Fixed the issue with AIOM2 detection as P3 BDF reversal.

3. Fixed the issue with BBS item modification the SUM tool.
4. Fixed the issue with uncorrectable non-fatal reporting SERR.
5. Fixed the issue with setting up BIOS without using ACS control.
6. Fixed CPU related event logs.

1.4 (2023/04/17)

[Enhancements]

1. Changed BIOS version to 1.4 for AGESA1006.
2. Followed the spec to update from MHz to MT/s in POST messages.
3. Changed PSP Logo to add a warning message at POST time.
4. Added support for the Xilinx U30 FPGA card.

[Fixes]

1. Fixed the error of MCA not being triggering when MCEON was disabled.
2. Fixed non-fatal triggering the SERR report.
3. Fixed issue with UUID having no MAC address in SMBIOS type 1 when AIOM was installed.
4. Fixed issue with DMI data not being restored after the default settings were loaded.
5. Fixed the OPROM FW display issue on the Setup Advanced page when AIOM was installed with two controllers.
6. Corrected CCD/ core count control setting on the BIOS CPU information page.

1.1 (2023/01/17)

[Enhancements]

1. Changed BIOS version to 1.1.
2. Updated AMD patch amd-dxio-floyd-1.13.04-20230111212355 for the NVMe hot plug issue.
3. Updated AMD patch -GenoAPI-SP5_1.0.0.3_Patch_1-for GPU (NVqual Test).
4. Upgraded code base to CRB16_Beta/AGESA 1.0.0.3.
5. [OOB] Updated SmcSecureBoot module to support the KEK, DB, DBX, DBT, and DBR key delete functions.
6. Fixed USB KB no function under Legacy mode.
7. [OOB] Upgraded Platform_ID to 0x0D for SUM Feature Flag.
8. [OOB] Added "Supermicro Security Erase Configuration item to the SUM Tool.
9. [OOB] Fixed the issue with the "User Password" item not displaying when SUM was used. 10. Added the Enhanced PPR item.

[Fixes]

1. Fixed the issue by automatically disabling the Above 4G Decode option when Intel LAN chip PCIe/AIOM card was installed under Legacy mode.
2. Fixed the issue with Slot 1 A2 GPU being shown lost on BMC Web when six A2 GPUs were installed.
3. Fixed the issue with AIOM1 failing to load Oeprom when AIOM1, AIOM2 and RSC-D2-666G5&RSC-D2R-666G5 were installed.
4. Fixed the issue with [SuperDiag_1.7.0b220810] that SuperDiag could not be run on H13SSW remotely or via USB.
5. Fixed the issue in which the riser card failed to auto-detect.
6. Fixed the issue in which the "AOC-AG-i2M AIOM2 OPROM" option in PCIe/PCI/Pnp Configuration was not available after "Boot Mode" was set to "Legacy".
7. Fixed the issue with AOC-AG-i2M: The legacy PXE banner failed to show up during system boot up.
8. Fixed the issue of system hanging (0xb0) after the SUM Inband update BIOS command was executed and system was reboot.
9. Fixed the incorrect display of Memory SN on BMC Web.

1.0 (2022/11/15)

First release.