

# BIOS Release Notes Form

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| <b>Product Name</b>     | <b>H11DSi/H11DSi-NT</b>   |
| <b>Release Version</b>  | <b>3.3</b>  |
| <b>Release Date</b>     | <b>03/28/2025</b>   |
| <b>Previous Version</b> | <b>3.0</b>  |
| <b>Update Category</b>  | <b>Recommended</b>  |
| <b>Dependencies</b>     | <b>MB Rev 2.00</b>  |
| <b>Important Notes</b>  | <b>BIOS Image:<br/>BIOS_H11DSI-0964_20250328_3.3_STDsp.bin<br/>BIOS Update Package:<br/>BIOS_H11DSI-0964_20250328_3.3_STDsp.zip<br/>Note:<br/>BIOS R 2.x only supports MB Rev. 2.00, with 32MB SPI flash ROM to support both AMD 7001/7002 series processors.</b>   |
| <b>Enhancements</b>     | <ol style="list-style-type: none"><li>1. [Naples][Enhancements] Update SA50299 to address CVE-2025-22830(High, 7.3)</li><li>2. [Naples][Enhancements] Update AGESA NaplesPI to 1.0.0.Q based on (BETA)5.013_NaplesCrb_0ACIJ035.</li><li>3. [Rome][Enhancements] Update AMI Modules based on (BETA)5.14_RomeCrb_0ACMK035.</li><li>4. [Rome][Enhancements] Update Legacy Serial Redirection module from MAINT_LegacySreDir_13.01 to MAINT_LegacySreDir_14.01</li><li>5. [Naples][Rome][Enhancements] Update Secure Boot DBX to address AMI SA50300 (CVE-2024-7344\CVE-2023-24932 (8.2 High/6.7 Middle)) security issue</li><li>6. [Naples][Rome][Enhancements] Update SA50289(TianoCompress Privilege Escalation Vulnerability) to address CVE-119</li><li>7. [Naples][Enhancements] Update AGESA NaplesPI to 1.0.0.P based on 5.013_NaplesCrb_0ACIJ034.</li><li>8. [Rome][Enhancements] Update AMI Modules based on 5.14_RomeCrb_0ACMK034.</li></ol> |

9. [Naples][Enhancements] Update AGESA NaplesPI to 1.0.0.N based on 5.013\_NaplesCrb\_0ACIJ033.
10. [Naples][Enhancements] Merge SA50191 code change
11. [Naples][Enhancements] Update SA50273 (OpenSSL Vulnerabilities) to address CVE-2006-7250(Medium, 5.0), CVE-2009-1377(Medium, 5.0), CVE-2009-1378(Medium, 5.0), CVE-2009-1387(Medium, 5.0), CVE-2009-3245(High, 10.0), CVE-2009-4355(Medium, 5.0), CVE-2010-0433(Medium, 4.3), CVE-2010-0740(Medium, 5.0), CVE-2010-0742(High, 7.5), CVE-2010-3864(High, 7.6), CVE-2010-4180(Medium, 4.3), CVE-2011-0014(Medium, 5.0), CVE-2011-3210(Medium, 5.0), CVE-2011-4108(Medium, 4.3), CVE-2011-4109(High, 9.3), CVE-2012-0884(Medium, 5.0), CVE-2012-1165(Medium, 5.0), CVE-2012-2110(High, 7.5), CVE-2012-2333(Medium, 5.0), CVE-2013-0166(Medium, 5.0), CVE-2013-0169(Low, 2.6), CVE-2014-0195(Medium, 6.8), CVE-2014-0221(Medium, 4.3), CVE-2014-0224(High, 7.4), CVE-2014-3470(Medium, 4.3), CVE-2014-8176(High, 7.5), CVE-2015-0292(High, 7.5), CVE-2014-3505(Medium, 5.0), CVE-2014-3506(Medium, 5.0), CVE-2014-3507(Medium, 5.0), CVE-2014-3508(Medium, 4.3), CVE-2014-3510(Medium, 4.3), CVE-2014-3568(Medium, 4.3), CVE-2014-3567(High, 7.1), CVE-2014-3570(Medium, 5.0), CVE-2014-3571(Medium, 5.0), CVE-2014-3572(Medium, 5.0), CVE-2014-8275(Medium, 5.0), CVE-2015-0204(Medium, 4.3), CVE-2015-0209(Medium, 6.8), CVE-2015-0286(Medium, 5.0), CVE-2015-0287(Medium, 5.0), CVE-2015-0288(Medium, 5.0), CVE-2015-0289(Medium, 5.0), CVE-2015-0293(Medium, 5.0), CVE-2016-0703(Medium, 5.9), CVE-2016-0704(Medium, 5.9), CVE-2011-1945(Low, 2.6), CVE-2015-1789(High, 7.5), CVE-2015-1790(Medium, 5.0), CVE-2015-1791(Medium, 6.8), CVE-2015-1792(Medium, 5.0), CVE-2015-1794(Medium, 5.0), CVE-2015-3193(High, 7.5), CVE-2015-3194(High, 7.5), CVE-2015-3195(Medium, 5.3), CVE-2015-3197(Medium, 5.9), CVE-2016-0701(Low, 3.7), CVE-2016-0702(Medium, 5.1), CVE-2016-0705(High, 9.8), CVE-2016-0797(High, 7.5), CVE-2016-0798(High, 7.5), CVE-2016-0799(High, 9.8), CVE-2016-0800(Medium, 5.9), CVE-2016-

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|   | <p>2842(High, 9.8),<br/> CVE-2016-2105(High, 7.5), CVE-2016-2106(High, 7.5), CVE-2016-2107(Medium, 5.9), CVE-2016-2109(High, 7.5), CVE-2016-2176(High, 8.2),<br/> CVE-2016-2182(High, 9.8), CVE-2016-6302(High, 7.5), CVE-2016-2177(High, 9.8), CVE-2016-2178(Medium, 5.5), CVE-2016-2179(High, 7.5),<br/> CVE-2016-2180(High, 7.5), CVE-2016-2181(High, 7.5), CVE-2016-2183(High, 7.5), CVE-2016-6306(Medium, 5.9), CVE-2020-1968(Low, 3.7),<br/> CVE-2017-3737(Medium, 5.9), CVE-2017-3731(High, 7.5), CVE-2017-3735(Medium, 5.3), CVE-2018-0739(Medium, 6.5), CVE-2018-0732(High, 7.5),<br/> CVE-2018-5407(Medium, 4.7), CVE-2018-0734(Medium, 5.9),<br/> CVE-2019-1543(High, 7.4), CVE-2019-1547(Medium, 4.7), CVE-2019-1563(Low, 3.7),<br/> CVE-2020-1967(High, 7.5), CVE-2020-1971(Medium, 5.9), CVE-2021-23840(High, 7.5), CVE-2021-23841(Medium, 5.9), CVE-2021-3449(Medium, 5.9),<br/> CVE-2021-3450(High, 7.4), CVE-2021-3711(High, 9.8), CVE-2021-3712(High, 7.4), CVE-2022-0778(High, 7.5), CVE-2022-4450(High, 7.5),<br/> CVE-2023-0215(High, 7.5), CVE-2023-0286(High, 7.4), CVE-2023-0464(High, 7.5), CVE-2023-0465(Medium, 5.3) security issue.</p> <p>12. [Rome][Enhancements] Update AMI Modules based on 5.14_RomeCrb_0ACMK033.</p> |
| <b>New features</b>   | N/A  |
| <b>Fixes</b>  | N/A  |
| <p><b>2.8 (12/14/2023)</b></p> <ol style="list-style-type: none"> <li>1. Updated AGESA RomePI to 1.0.0.H based on 5.14_RomeCrb_0ACMK028.</li> <li>2. Updated AGESA NaplesPI to 1.0.0.L based on 5.013_NaplesCrb_0ACIJ031.</li> <li>3. Updated SA50216_Supplement(LogoFAIL Vulnerability).<br/> Disabled "Correctable/Non-Fatal error reporting enable" bits when BIOS item "PCI AER support"</li> <li>4. Fixed MP1(SMU) and CPU WDT uncorrectable error, "no event log" issue.</li> </ol> <p><b>2.7 (10/25/2023)</b></p> <ol style="list-style-type: none"> <li>1. Updated AGESA RomePI to 1.0.0.F based on 5.14_RomeCrb_0ACMK026 for AMD-SB-1032.</li> <li>2. Added Rocky Linux Boot Option Name.</li> </ol> |  |

3. Updated Rome B0 stepping CPU microcode 0x0830107A from AMD-SB-7008 Security Bulletin to address CVE-2023-20593 issue.
4. Restored SMCI secure boot keys and Updated Secure Boot DB variable. (Unknown certificate "Addtrust External CA Root" expired on May 30th 2020).
5. Updated AGESA RomePI to 1.0.0.G based on 5.14\_RomeCrb\_0ACMK027.
6. Changed Pxe from Uefi(U)/Legacy(L) to L/U through Redfish.
7. Added InBand OemFID support.
8. Added OutBand OemFID support.
9. Updated OEM FID information for PRICESO\_0\_DESC and PRICESO\_1\_DESC.
10. Set RDIMM\LRDIMM\3DSDIMM memory throttling trip-point @85C.

### **2.5 (10/27/2022)**

1. Updated the DBX file to fix Secure Boot Bypass issue.
2. Updated AGESA RomePI to 1.0.0.E based on 5.14\_RomeCrb\_0ACMK025.
3. Updated Rome B0 stepping CPU microcode 0x08301055 to patch Errata 1161, 1176, 1179, 1183, 1214, 1268, 1386, 1417.
4. Changed setup string title from "SMCI" to "Supermicro".
5. Removed "Vendor Keys" in security page.

### **2.4 (12/28/2021)**

1. Changed BIOS revision to 2.4
2. Updated AGESA NaplesPI to 1.0.0.H.
3. Updated the 8012 ZP-B2 microcode to 0800126E.
4. Updated the AGESA RomePI to 1.0.0.D.
5. Updated the 8310 SSP-B0 microcode to 8301052.
6. Set Relaxed Ordering default to "Enabled".
7. Added setup item: "Enhanced Preferred IO Mode".

### **2.3 (8/2/2021)**

1. Updated AGESA NaplesPI to 1.0.0.D.
2. Updated 8012 ZP-B2 microcode 0800126C.
3. Fixed problem of system hanging on POSTCODE 0xB2 when JPG1 is set to disabled and the VGA card is plugged in.
4. Added SMCI HDD Security feature.
5. Updated help string of item "Input the description".
6. Changed BIOS revision to 2.3.
7. Updated AGESA RomePI to 1.0.0.9.
8. Updated 8310 SSP-B0 microcode 830104D.
9. Updated Byte6 setting and Byte12 setting of slave address C0h and C2h SATA redriver from 0xE1 to 0xC1.

10. Fixed failures of FRU0 - Manufacturer Name (PM) to sync to SMBIOS Type 3 - Manufacturer (CM) and FRU0 - Product Part/Model Number (PPM) to sync to SMBIOS Type 1 - ProductName (PN).
11. Removed "\$SMCUNHIDE\$" string from "PCI AER support" setup item help string.
12. Added AMD IOMMU patch code to fix NVMe Devices drop and Hardware error in RH 7.x.
13. Fixed problem of TCG admin password reverting.
14. Corrected CPU speed information in BIOS setup.
15. Fixed inability to detect SATA device (CPU1 SATA0-3) during Ubuntu 20.10 installation when networking AOC (AOC-STGS-i2T) is plugged into Slot 5.
16. Rolled back SmcHttpBoot module to fix problem of BIOS flash from 2.1 to 2.3 hanging up and erasing all blocks in Non-Boot with Rome CPU.

### **2.1 (2/21/2020)**

1. Changed BIOS revision to 2.1.
2. Updated AGESA RomePI to 1.0.0.5 based on 5.14\_RomeCrb\_0ACMK013.
3. Displayed "PCI AER Support" setup item on ACPI page.
4. Added SMC HDD Security feature.
5. Fixed issue of system hanging at post code A7h.
6. Fixed inability of SUM to change the function of NUMA Node Per Socket.
7. Fixed problem of system sometimes rebooting during legacy Windows 2019 OS installation when using Rome CPU 7502.
8. Removed requirement to use Admin password for erasing TCG device.

### **2.0b (11/19/2019)**

1. Changed BIOS revision to 2.0b.
2. Added "DRAM Scrub Time" to Memory Configuration.
3. Updated AGESA RomePI to 1.0.0.4 based on 5.14\_RomeCrb\_0ACMK012.
4. Set AMD CBS "PCIe ARI Support" item to be used instead of "ARI Forwarding".
5. Updated item string "Input the description" and "HTTP Boot One Time" to adhere to Rome BIOS Setup Template v0.7\_20190705.
6. Displayed 3rd IPMI version in BIOS setup.
7. Updated SSID of AMD Host Bridge according to each project's board ID.
8. Forced all PCIe to Gen3 only for H11 drop-in projects.
9. Displayed the "4-link xGMI max speed" setup item and set to 10.667Gbps by default on H11 DP drop-in projects.
10. Set IOMMU default to Auto (Enabled)
11. Displayed "Preferred IO" item.
12. Prevented display of any AMD memory error messages during the POST phase.
13. Fixed malfunction of recovery.
14. Added support for OOB SATA HDD information and asset information of 2 SATA controllers.
15. Fixed missing screen output when Boot Mode is changed to EFI.

16. Fixed problem of system hanging when installing NVidia RTX 2080/5000/6000.
17. Fixed the issue of "SMCI POST Screen Message" appearing on BIOS setup menu.
18. Fixed the issue of "SMCI POST Screen Message" appearing on POST screen when executing EFI Shell application.
19. Corrected the CPM table setting.
20. Fixed failure of iSCSI function when LAN option is in UEFI mode.
21. Fixed problem of the xGMI speed reaching 16G after pressing "load default."
22. Fixed problem of the default xGMI speed not being 10.6G.
23. Fixed problem of yellow exclamation mark appearing in Windows 2019 Device Manager when running AST2500 VGA UEFI OPROM on NBIO1.
24. Fixed inability to detect NVMe device on P2\_NVME0 and P2\_NVME1 ports.

## **2.0 (9/25/2019)**

1. Initial Release