BIOS Release Notes Form

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Product Name	H13DSG-O-CPU
Release Version	3.5
Release Date	4/19/2025
Build Date	4/19/2025
Previous Version	3.4
Update Category	Recommended
Dependencies	IPMI firmware R 01.03.11 or above
Important Notes	BIOS Image:
	BIOS_H13DSGO-1C7E_20250419_3.5_STDsp.bin
	BIOS Update Package:
	BIOS_H13DSGO-1C7E_20250419_3.5_STDsp.zip
	Note:
	To support the AMD EPYC Bergamo and Genoa X processors,
	please update to BIOS version 1.4 or later.
Enhancements	1. Updated to 5.35_0ACST_PI1.0.0.4_018 for AGESA 1.0.0.4.
	2. Updated Secure Boot DBX to address the AMI SA50300 security
	issues: CVE-2024-7344 (8.2, High) and CVE-2023-24932 (6.7,
	Medium).
	3. Updated TurinPI_SP5_1004_Patch_1.
	4. Updated the reporting of V2-V9 tags in the SMCVPD based on
	LAN port number.
	5. Implemented the TPM EK public certificate feature.
	6. Updated to 5.27_GenoaCrb_0ACQT028 for AGESA 1.0.0.F.
	7. Enabled PCIe board GPU slot hot plug function.
New features	N/A
Fixes	Adjusted CPU0 P1 memory resource allocation to support 513GB
	MMIO 64 per H200 GPU card for PLX firmware 4.16, specifically for
	the S1C0 SKU.

3.4 (2/11/2025)

- 1. Disabled all hotkeys before booting into SuperDiag.
- 2. Exposed the "Secure Boot Mode" setup item to SAA.
- 3. Added support for Rocky and Alma Linux.
- 4. Enhanced the Workload Profile Function P-state processing.
- 5. Updated the built-in SuperDiag for the BirchStream platform, based on GIT_dccd148ce312dc6d96903b0fa73b4b8fc8443b8c. (BMC support required)
- 6. Revised the system diagnostics hot key flow.
- 7. Updated to version 5.35_0ACST_PI1.0.0.3_017 for AGESA 1.0.0.3.
- 8. Updated to version 5.27_GenoaCrb_0ACQT027 for AGESA 1.0.0.E.
- 9. Updated SA50292 (OpenSSL 3.1.4 Vulnerabilities Fixes) to address security issues: CVE-2023-6237(Medium, 5.9), CVE-2024-5535(High, 9.1), and CVE-2024-6119(High, 7.5).
- 10. Updated to TurinPI_SP5_1003_Patch_1.

3.2 (11/21/2024)

- 1. Updated the BIOS to version 3.2.
- 2. Provided initial support for the Turin CPU platform.
- 3. Updated to 5.35_0ACST_PI1.0.0.2_016 for Turin with AGESA 1.0.0.2 Patch 1.

1.9c (9/19/2024)

1. Added a BIOS workaround for verifying POST completion.

1.9b (9/7/2024)

- 1. Updated PLX FW v4.14 slot porting for S1 and S2 SKUs.
- 2. Updated SEL support for MBIST.
- 3. Changed dev_type for P1 and P2 to Pci2PciBridge.
- 4. Fixed IRQ4 being disabled in COM1.
- 5. Updated NBIO resource allocation for the MI210 hang-up issue.

1.9a (5/24/2024)

- 1. Added a workaround to fix PLX board drop when DC on/off in S1 configuration.
- 2. Fixed the problem of SOL function failure in Red Hat OS.

1.9 (5/10/2024)

- 1. Updated to 5.27_GenoaCrb_0ACQT025 for AGESA 1.0.0.C.
- Updated SA50218 (Vulnerability in EDK2 NetworkPkg/PixieFail VU#132380) to address CVE-2023-45229/CWE-125 (Medium, 6.5), CVE-2023-45230/CWE-119 (High, 8.3), CVE-2023-45231/CWE-125 (Medium, 6.5), CVE-2023-45232/CWE-835 (Medium, 6.5), CVE-2023-45233/CWE-835 (Medium, 6.5), CVE-2023-45234/CWE-119 (High, 8.3), and CVE-2023-45235/CWE-119 (High, 8.3) issues.
- 3. Updated AMITSE module for AMI SA50216 and SA50230 Security Advisories (LogoFAIL Vulnerability) to address CVE-2023-39538 (7.5, High) and CVE-2023-39539 (7.5, High) security issues.
- Updated SA50235 (Extended Image Parser Corruption Correction) to address BRLY-LOGOFAIL-2023-013 (Medium, 5.1), BRLY-LOGOFAIL-2023-014 (Medium, 4.4), BRLY-LOGOFAIL-2023-015 (Medium, 4.4), BRLY-LOGOFAIL-2023-016 (High, 7.5), BRLY-LOGOFAIL-2023-017 (High, 7.5), BRLY-LOGOFAIL-2023-018 (High, 7.5), BRLY-LOGOFAIL-2023-019 (High, 7.5), BRLY-LOGOFAIL-2023-020 (High, 7.5), BRLY-LOGOFAIL-2023-022 (High, 7.5), BRLY-LOGOFAIL-2023-023 (High, 7.5), BRLY-LOGOFAIL-2023-024 (High, 7.5) issues.
- 5. Updated the Secure Boot KEK/DB to add new Microsoft certificate.
- 6. Disabled ASPM in ACPI FACP when PCIe ASPM is disabled.

- 7. Followed SW-PM's command to change the BIOS default setting to enable AMD ADDC by default.
- 8. Report GPU information to type 40 if the card is a GPU hybrid card. (GPU system only)
- 9. Removed the LAN condition when getting VPD data.
- 10. Masked Advisory Non-Fatal Error for X710 upstream link.
- 11. Updated the SUM OOB module to SMCOOBV2.00.22 to support Redfish replacing the display name feature.
- 12. Fixed the issue where efi OS is not the first in the boot order. If BMC is issued to change the boot order (once), then efi OS is installed via PXE.
- 13. Fixed the issue of abnormal startup order of IPMI function change.
- 14. Fixed SUM GetSataInfo failure.
- 15. Fixed the issue that the "CCD control" and "Core Control" items cannot function.
- 16. Added FBO group to BIOS cfg file dump from SUM.
- 17. Fixed the issued of incorrect CPU freq. setting.
- 18. Removed SRMS PXE boot Workaround because it caused the Getlp6Gateway command to fail.
- 19. Fixed ACPI BIOS error (CSMI) in dmesg.

1.6b (2/29/2024)

- 1. Added auto-detection for supporting ASM106x when only one CPU.
- 2. Added a workaround to fix the SRMS PXE boot issue when AC is on/off.

1.6a (11/18/2023)

- 1. Updated to 5.27_GenoaCrb_0ACQT022 for AGESA 1.0.0.9 with patch.
- 2. Enabled token ASM1061_Workaround to disable ASM1061 64-bit memory address capability.
- 3. Set the default settings "SR-IOV" to "Auto", it can switch the default settings of "SRIOV" according to different configurations by auto-detection.
- 4. Updated SecureBoot and AmiSecureBootPkg to correct secure boot system mode.
- 5. Fixed duplicate Lan string in boot order.
- 6. Fixed an issue that "Present but not trained" is displayed in the memory information when no DIMM was installed.
- 7. Fixed Memory Pmic event log DIMM location being incorrect.
- 8. Updated W/a for onboard X710 and H100 link width drop.
- 9. Added SBR for PLX root port to patch PLX device drop issue.

1.6 (10/17/2023)

- 1. Updated to 5.27_GenoaCrb_0ACQT022 for AGESA 1.0.0.9 with patch.
- 2. Enabled token ASM1061_Workaround to disable ASM1061 64-bit memory address capability.
- 3. Workaround for onboard X710 link width drop.
- 4. Enabled the default setting "SR-IOV" to avoid VGA output disappearing when installing Ubuntu OS.
- 5. Updated SecureBoot and AmiSecureBootPkg to correct secure boot system mode.
- 6. Fixed issue of incorrect DIMM location in the Memory PMIC event log.
- 7. Fixed duplicate LAN string in the boot order.
- 8. Fixed issue that "Present but not trained" is displayed in the memory information when no DIMM was installed.

1.5 (8/8/2023)

- 1. Updated AMI label to 5.27_GenoaCrb_0ACQT020 for AGESA 1.0.0.7
- 2. Added reset delay function for some GPU missing issues, such as the A100.
- 3. Added S2-C0/C1 AIOM Pci location to PcdSlot define to fix AIOM NIC disconnect issue.
- 4. Updated Pcode for erratum#1484/1485

- 5. Added AMD AGESA 1007 patch.
- 6. Added support for BIOS getting into a reset loop after IOMMU is disabled or SRIOV is enabled.
- 7. Fixed problem of SMT control cannot be kept after updating BIOS.
- 8. Fixed problem of memory temp showing N/A in BMC web after updating AGESA 1007.
- 9. Updated MPIO binary patch from AMD.

1.4a (5/4/2023)

- 1. Updated AMI label to 5.27_GenoaCrb_0ACQT018 for AGESA 1.0.0.6
- 2. Added support for B2 recovery hanging.
- 3. Added AMD AGESA 1006 patch.
- 4. Removed AOM-PTG-I2T onboard device define from SMBIOS type 41.
- 5. Fixed system hanging issues while MB CPLD is updated by SUM.

1.3a (4/25/2023)

1. Fixed the GPU drop issue.

1.3 (4/1/2023)

- 1. Updated AMI label to 5.27_GenoaCrb_0ACQT018 for AGESA 1.0.0.5.
- 1. Supported Mellanox and Broadcom 3rd Party LAN Card VPD Data.
- 2. Supported S2-C0/C1 cabling configuration.
- 3. Supported S1-C0/C1 cabling configuration.
- 4. Supported PEI Early Video.
- 5. GPU Link Width Drop Recovery.

1.0 (1/11/2023)

Initial Release