BIOS Release Notes Form

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| Product Name | H12DSG-Q-CPU6 | | | | |
|-------------------------|--|--|--|--|--|
| Release Version | 3.2a | | | | |
| Build Date | 04/10/2025 | | | | |
| Previous Version | 3.2 | | | | |
| Update Category | Recommended | | | | |
| Dependencies | IPMI firmware 01.06.01 | | | | |
| | BIOS image: BIOS_H12DSGQ-1C11_20250410_3.2a_STDsp.bin | | | | |
| Important Notes | BIOS Update package: BIOS_H12DSGQ- | | | | |
| | 1C11_20250410_3.2a_STDsp.zip | | | | |
| | Notes: | | | | |
| | 1) BIOS R2.x supports 7002 and 7003 processors. | | | | |
| | 2) The Flash Utility in the package only supports updating BIOS R1.x | | | | |
| | to R2.x. Rolling back is not allowed. | | | | |
| | 3) The default boot mode for BIOS R2.x has been changed to EFI. If | | | | |
| | your OS is legacy, press the key during POST to enter the | | | | |
| | BIOS setup and change boot mode after upgrading from R1.x to | | | | |
| | R2.x. | | | | |
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| Enhancements | 1. Updated Secure Boot DBX to address AMI SA50300 (CVE-2024-7344\CVE-2023-24932) security issue. | | | | |
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| New features | N/A | | | | |
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| Fixes | 1. Fixed booting issue with Rome processors on BIOS 3.2. | | | | |
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3.2 (2025/02/14)

- 1. Updated BIOS revision to 3.2.
- 2. Updated RomePI to 1.0.0.L.
- 3. Updated MilanPI to 1.0.0.F.

3.0 (2024/10/10)

- 1. Updated BIOS revision to 3.0.
- 2. Updated RomePI to 1.0.0.J.
- 3. Updated MilanPI to 1.0.0.E.
- 4. Updated Microcodes for Milan and Rome.
- 5. Fixed issue where the SMT option could not be changed.
- 6. Fixed issue where the NPS option could not be changed.
- 7. Fixed issue where DMI information was not preserved after loading LoadDefaultBiosCfg.
- 8. Fixed issue where the IPMI boot option could not be set to "OS image mount."

2.8 (2024/02/08)

- 1. Updated BIOS revision to 2.8.
- 2. Updated RomePI to 1.0.0.H.
- 3. Updated MilanPI to 1.0.0.C.
- 4. Updated Microcodes for Milan and Rome.
- 5. Updated SA50216_Supplement (LogoFAIL Vulnerability).

2.7a (2023/12/18)

- 1. Updated BIOS revision to 2.7a.
- 2. Updated Rome AGESA to 1.0.0.G.
- 3. Updated Milan AGESA to 1.0.0.B.
- 4. Updated SEV firmware to version 1.55.16 (Hex 1.37.10 SPL = 0x14).
- 5. Updated Redfish module.
- 6. Fixed issue where ECC error displayed wrong DIMM location in BIOS log.
- 7. Fixed issue where system hung on POST code 0x92 in cold boot.
- 8. Fixed issue where SMT Control didn't work.

2.6a (2023/10/05)

- 1. Changed BIOS revision to 2.6a.
- 2. Updated Milan B0and B1 stepping CPU microcodes (0x0A001079 and 0x0A0011D1) and Milan-X B2 stepping CPU microcode (0x0A001234) for AMD-SB-7005 Security Bulletin to address the CVE-2023-20569 issue. .
- Updated Rome AGESA to 1.0.0.F.
- 4. Updated ROME microcode for AMD-SB-7008.
- 5. Changed OPROM setting name to match cable mapping.
- 6. Updated Milan AGESA to 1.0.0.A.
- 7. Added "Onboard Video Option ROM" setting.
- 8. Fixed issue where PSU information could not be read in SMBIOS type 39 after a BMC reset.
- 9. Fixed issue where LoadDefaultBiosCfg failed with no hard drives were installed.
- 10. Fixed issue where HttpBoot did not reset after reboot.
- 11. Fixed issue where AOC RAID legacy OPROM could not load.

2.5b (2023/04/21)

- 1. Changed BIOS revision to 2.5b.
- 2. Added AOM to type9 info.

- 3. Fixed issue where AOM-PTG-i2T LAN would drop after the system was turned on and off.
- 4. Fixed issue where the system could not boot with legacy PXE when AOM-PTG-i2T was installed.
- 5. Fixed issue where MAC address of OBL did not show in Web GUI when AOM-PTG-i2T was installed.

2.5 (2022/11/25)

- 1. Changed BIOS revision to 2.5.
- 2. Updated Milan AGESA to 1.0.0.9.
- 3. Updated Rome AGESA to 1.0.0.E.
- 4. Changed "SMCI" strings to "Supermicro.".
- 5. Fixed issue where BIOS could not be recovered by using ROT.
- 6. Fixed issue where IPMICFG couldn't change boot options.
- 7. Fixed issue where type 1 and type 3 information could not be restored from FRU.
- 8. Fixed issue where NUMA setting could not be changed by SUM.
- 9. Fixed issue of settings could not be dumped with no hard drives in the system.
- 10. Corrected DIMM mappings for UECC/CECC.

2.4 (2022/07/29)

- 1. Changed BIOS revision to 2.4.
- 2. Updated Milan AGESA to 1.0.0.8.
- 3. Updated microcode for Errata #1415.
- 4. Added SMC KMS feature.
- 5. Updated Rome AGESA to 1.0.0.D.
- 6. Fixed issue where BMC static IP could not be set by BIOS.
- 7. Fixed issue of BIOS recovery.

2.3a (2022/05/10)

- 1. Changed BIOS revision to 2.3a.
- 2. Added bifurcation settings for Rome.
- 3. Added EDC settings for Milan-X CPU validation.
- 4. Updated Milan AGESA to 1.0.0.7.
- 5. Fixed issue where fixed boot order would reset to default after installing OS.
- 6. Enhanced KCS algorithm.

2.3 (2021/10/18)

- 1. Changed BIOS revision to 2.3.
- 2. Supported secure boot redfish interface.
- 3. Supported SUM TLS certificate upload.
- 4. Supported SUM SetHttpBoot.
- 5. Updated AGESA MilanPI to 1.0.0.6.
- 6. Supported Milan-X CPU.
- 7. Updated CPU microcode.
- 8. Enhanced OEMFID information.
- 9. Updated AGESA RomePI to 1.0.0.C.
- 10. Updated microcode for Milan erratum 1381.
- 11. Fixed issue of TPM was disabled by default.
- 12. Fixed issue of system would boot loop if a TPM is disabled.
- 13. Fixed issue of DMI type 39 info was not filled in after cold booting.

2.1 (2021/07/07)

- 1. Added more OOB settings.
- 2. Added support for Redfish boot option management API.
- 3. Fixed issue of the system UUID generating incorrectly if multiple LAN cards are installed.

2.0c (2021/06/14)

- 1. Added "Correctable Error Threshold" and "PCI AER" setting.
- 2. Added support for SUM 2.6 SetHttpBoot.
- 3. Removed UEFI NVMe boot option group.

2.0b (2021/05/20)

- 1. Added "Memory Corrected Error Enabling" setting.
- 2. Corrected boot order.
- 3. Corrected setting of serial over LAN module.

2.0a (2021/05/06)

- 1. Added timestamp data to SMBIOS type1 data.
- 2. Fixed in-band update.
- 3. Fixed type9 slot names.
- 4. Reported number of NVMe to CPLD.
- 5. Fixed version number reading in \$FID.
- 6. Removed AOM info from type9.
- 7. Updated Product Name in DMI table type 2.
- 8. Fixed USB device detection for JUSB1.
- 9. Rebuilt BIOS for Rome to have the same build date as Milan.

2.0 (2021/04/07)

- 1. Changed BIOS revision to 2.0.
- 2. Updated AGESA MilanPI to 1.0.0.1.
- 3. Updated AGESA RomePI to 1.0.0.A.
- 4. Adjusted SMBIOS type9/40 information.
- 5. Adjusted hotplug function for NVMe/SATA backplane.
- 6. Added bifurcation setting for slots 1, 3, 4, and 5.
- 7. Fixed SMBIOS Type 39 PSU reading procedure.
- 8. Fixed issue of BIOS hanging at postcode 94 when plugging in AOC-STG-I2T while GPU board is connected to system.
- 9. Fixed issue with BIOS ECO TC221.
- 10. Fixed inability to change CPU settings.
- 11. Fixed issue of SMSTC reporting "filesize out of range" in testing.
- 12. Fixed issue of BIOS boot order showing duplicate entries and no additional information.
- 13. Fixed issue of no sensors showing with dual image BIOS.
- 14. Fixed failure of AOM to appear in network devices section of WebGUI.
- 15. Fixed duplicate setting on PCIe/PCI/PNP configuration menu.
- 16. Fixed failure of MAC address on board LAN1/2 to appear on WebGUI.

1.0a (2020/12/06)

- 1. Changed BIOS revision to 1.0a.
- 2. Enabled LPC TPM.
- 3. Fixed preservation of DMI when updating BIOS.
- 4. Fixed SUM test case 220.

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| 1.0 (2020/10/28) | | | |
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