

# IPMI Firmware / BIOS Release Notes Form

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|-------------------------|---|
| <b>Product Name</b>     | H13SSL-N/NT   |
| <b>Release Version</b>  | 3.5   |
| <b>Release Date</b>     | 04/17/2025  |
| <b>Build Date</b>       | 04/17/2025  |
| <b>Previous Version</b> | 3.4   |
| <b>Update Category</b>  | Recommended   |
| <b>Dependencies</b>     | N/A   |
| <b>Important Notes</b>  | <p>ECO #31581</p> <p>BIOS Image: BIOS_H13SSL-1C7D_20250417_3.5_STDsp.bin</p> <p>BIOS Update Package: BIOS_H13SSL-1C7D_20250417_3.5_STDsp.zip</p> <p>Please update BIOS with attached Flash Utility in package.</p> <p><b>For AMD EPYC 9005 Series Processor support, please ensure your motherboard revision is R2.0 or higher. The BIOS and BMC package versions should be BIOS V3.0 or higher and BMC V01.03.03 or higher.</b></p> <p><b>Please note that if you would like to roll back the BIOS from V3.x (AMD EPYC 9004/9005 Series Processor) dual BIOS to V1.x (AMD EPYC 9004 Series Processor) single BIOS, make sure to install AMD EPYCTM 9004 Series Processor before updating the BIOS.</b></p> <p><b>To support AMD EPYC Bergamo/Genoa-X processor, please update to BIOS v1.4 or higher</b></p> |
| <b>Enhancements</b>     | <ol style="list-style-type: none"><li>1. [Turin]Update Agesa code to Turin PI 1.0.0.4</li><li>2. [Turin]Update TurinPI_1004_Patch_1</li><li>3. [Genoa][Turin] Add add the Security Advisories (SA50300) for DBX upgradation.</li><li>4. [Turin] Support the DDR6400 dynamic hidden option function.</li><li>5. [Genoa][Turin] Update PN get method for third-party Intel E810 series.</li></ol>   |
| <b>New features</b>     | N/A   |
| <b>Fixes</b>            | <ol style="list-style-type: none"><li>1. The system hangs when enter into the setup page of BROADCOM &lt;SAS 3916&gt; Configuration Utility.</li><li>2. Disable LongSerialNumber support; Correct memory serial number in type 17.</li></ol>  |

## ***Release Notes from Previous Release(s)***

### **Revision 3.4 (02/13/2025)**

1. ID192406 - ECO -- 25452 -- 01.00.08.00(2024/12/18) -- Web Function -- Inconsistency in IPv6 Configuration Between BIOS and Web UI.
2. IDIPv6 address and mode are inconsistent between BIOS and web.
3. Fix when disabling Onboard LAN 1 OROM, Onboard LAN still can work during POST.
4. Fixed Legacy mode will stuck B2 issue.
5. Fix when executed KMS Key-Rotation with Storage by BIOS Setup, Key ID number wasn't changed.
6. Resolved an issue where the system would hang when restoring the Secure Boot keys.
7. Fixed Slot Usage in SMBIOS Type 9 fail due to the usage status of "CPU SLOT2 PCIe5.0 X8" and "CPU SLOT4 PCIe5.0 X8" are always "Available".

### **Revision 3.0 (09/23/2024)**

1. First release for dual BIOS image Rev. 3.0.
2. Add TurinPI-SP5\_1.0.0.0\_Patch\_3
3. Update Genoa BIOS ROM image to Genoa 0903 3.0

### **Revision 1.9 (05/28/2024)**

1. Update to 5.27\_GenoaCrb\_0ACQT025 for AGESA 1.0.0.C
2. Add SMC Debug log support.
3. Correct the key string typo under SupportedSystems.
4. Removed BIOS attribute serial number(IPMI 30\_68\_F9 bit4).
5. Update SMC Debug log support to avoid CPU exception after inject PCIe error.
6. Update SmcCorrMemErrEn related code to optimize CECC control during DXE phase.
7. Add CPU core (FP/IF/L3/L2/DE/LS) error detection.
8. Disable "Correctable/Non-Fatal error reporting enable" bits when BIOS item "PCI AER support" set Disabled.

### **Revision 1.6b (03/08/2024)**

1. 3rdParty AOC card Mac report to Type40 with SuperMicro format
2. Force Attribute include serial number.
3. Preserve BIOS Config function.
4. Disable ASPM in ACPI FACP when pcie ASPM is disabled.
5. SuperEdit/BCP can't modify build date success
6. Fixed IPv6 string incomplete display issue(miss the last group of numbers) on PEI early video.
7. Fixes After using SMCIPMITool to set boot device and then default boot order is restored incorrectly.
8. [PBID#169388]fixes Generate the duplicate setting 'ASPM Support' with same order via sub command GetCurrentBiosCfg
9. Fixed TC3111 failed due to boot option missing after reset default secure key.
10. Fixes SAAECO test case 3004 failed on the DefaultString of Device Reset returning spaces.

### **Revision 1.6 (11/03/2023)**

1. [H13xxx][Genoa] Add Secure Boot Data Base feature.
2. Support Built-in SuperDiag.
3. Support SPD DATA to ACPI.
4. Enabled AMD ADDC(Autonomous Debug Data Collection) feature.(Crash dump)
5. Add Amd CbsConfig load default process before NVRAM reset.
6. Fixed memory information report wrong message when not install memory.
7. Add GenoaPI-SP5\_1009\_Patch\_Rev2.
8. Change BIOS version to 1.6 for AGESA1009.
9. Use IPMI 30\_68\_F9 BIT4 to control hidden/display the serial number of Attribute name.
10. Support 16 bit Post code.
11. Disable PMIC error message reporting
11. Fixes Sum tool cannot get current BIOS setting after using sum tool to set the Boot URI.
12. Fixes ID160518 [SUM] Can't execute ChangeFixedBootCfg succeed via SUM.
13. Fixes only show one LAN port when installed AOC card.
14. Fixed load default function can not work via Oob.
15. Fixed Redfish SATC boot option test for FixedBootOrder.
16. Fixed string overwrite issue after CMOS reset.

### **Revision 1.5 (07/25/2023)**

1. Change BIOS version to 1.5 for AGESA1007.
2. AMD ucode patch 2 to fix for erratum #1483/1485
3. Support redfish API Oem/Supermicro/FixedBootOrder.
4. Hidden Local APIC Mode due to PM's decision, will update H13 setup template later.
5. Setup ASPM not sync to PCIe port.
6. Change "Power Off" string to "Stay off" to match AMD H13-Genoa BIOS Setup Template.
7. Sync up Core control option items with AMD H13-Genoa BIOS Setup Template v0.8\_20230112.xlsx
8. Fix AMD cbs\_config item does not align APCB cause system hang at early video phase.
9. Fix BBS item cannot be changed problem via SUM tool.
10. Fix uncorrectable non-fatal will report SERR issue.
11. Fix BIOS setup doesn't have ACS control item.

12. Force BMC (AST2600) link speed to Gen1 to avoid link speed drop issue on DC on-off test

### **Revision 1.4(04/17/2023)**

1. Change BIOS version to 1.4 for AGESA1006.
2. Upgrade code base to CRB19/AGESA 1.0.0.6
3. Using CBS\_CONFIG variable directly in SmcSetup.sd to prevent sync setting fail in SmcAmdCbsDxeDriver.c
4. Follow the spec to change unit of memory translate from MHz to MT/s in POST message.
5. Change PSP Logo to include add warming message for post time.
6. Enable display of Day in Date Format.
7. Add SuperGuardians support.
8. Add "AmdSetup" variable group support for OOB used.
9. Increase OEM\_STRING\_1/OEM\_STRING\_2 in SMBIOS type11 string length to at least 50 characters to meet SMBIOS spec.
10. Fixed MCA correctable error cannot trigger when MCEON disabled.
11. Fixed uncorrectable non-fatal will report SERR issue.

### **Revision 1.1a(02/14/2023)**

Update AMD patch amd-dxio-floyd-1.13.04-20230111212355 to fix NVMe hotplug issue.

### **Revision 1.1(12/23/2022)**

1. Upgrade code base to AGESA 1.0.0.3
2. Onboard M.2 Support PCIe speed for M.2-H1/H2.

### **Revision 1.0 (2022/11/22)**

First release.