

BIOS Release Notes Form

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Product Name	X13DEG-OAD
Release Version	2.5a SPS: 6.1.4.75
Build Date	02/26/2025
Previous Version	2.4 SPS: 6.1.4.047
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	<ol style="list-style-type: none">1. Updated AMI label to 5.32_EagleStreamCrb_OACOR_107_BETA for uPLR3.2. Updated m_87_c06f2_21000291 for EMR A stepping CPU.3. Updated m_87_806f8_2b000620 for SPR E stepping CPU.4. Updated m_10_806f8_2c0003e0 for SPR HBM B stepping CPU.5. Updated Intel Server Platform Services SPS_E5_06.01.04.075.6. Updated Seamless MCU capsule revision to 2.4.7. Changed the setting of GPIO pin GPP_E7.8. Updated the SmcOob module to SMCOOBV2.01.06 to enhance the hidden items when it is not exposed in the page.9. Moved to hide the SmcSecureBoot page policy.10. Fixed how the DNS IP showed up incorrectly when setting the DNS IP in Web.11. Modified the "MMIO High Granularity Size" default value to "1024G."12. Updated the Intel-Restricted-2025-2-IPU-20250205_20252IPU Production candidate.

	<p>13. Disabled SMC_CORRECTED_MCA_THRESHOLD and filter corrected the error of the processor.</p> <p>14. Corrected the processor event information.</p>
New features	None
Fixes	<ol style="list-style-type: none"> 1. Enabled SMC USB support. 2. Applied the candidate solution for the issue "DMI/FRU mismatch issue." 3. Canceled to force the BIOS to assign the address resource under 32 bit for X550. 4. Patched some issues. (Based on GIT_48999ff3500beef7e9f90d21f66d15fd540215d8.) 5. Fixed the HttpBootCheckSpaceToDeleteAllHttp problem. 6. Fixed the issue where enable debug mode would cause system free pool assert.

Release Notes from Previous Release(s)

2.4 SPS: 6.1.4.047 (08/09/2024)

1. Updated AMI label to 5.32_EagleStreamCrb_0ACOR_105_BETA for uPLR2.
2. Updated m_87_c06f2_21000283 for EMR A stepping CPU.
3. Updated m_87_806f8_2B000603 for SPR E stepping CPU.
4. Updated m_10_806f8_2C0003D3 for SPR HBM B stepping CPU.
5. Updated Intel Server Platform Services SPS_E5_06.01.04.047.
6. Improved the SmcVPD third-party LAN card's function.
7. Updated Seamless MCU capsule revision to 2.4.
8. Updated VROC SATA/sSATA/tSATA/VMD EFI driver to VROC PreOS 8.6.0.1136.
9. Updated AMI label to 5.32_EagleStreamCrb_0ACOR_102_BETA for BKC uPLR1 OOB2.
10. Improved the SmcVPD third-party LAN card's function.
11. Added a function to support HTTPS boot TLS certificate upload via BMC Redfish API.
12. Updated TLS Certification setup page.
13. Updated AMI label to 5.32_EagleStreamCrb_0ACOR_101 for BKC UMR1 2024_WW05.
14. Updated the SUM OOB module to SMCOOBV2.00.22 to support Redfish replacing the display name feature.
15. Exposed "Directory Mode Enable" and added X-AMI.
16. Updated EDK2 NetworkPkg for AMI SA50218.
17. Updated AMITSE module for AMI SA50216.
18. Updated AMITSE module for AMI SA50230.
19. Removed the limit of PN and SN in VPD data.
20. Enhanced Function Policy for the ACS CTL register.

2.1 SPS: 6.1.4.005 (11/29/2023)

1. Upgraded the codebase to Intel BKC 2023 WW47.
2. Fixed the boot option information for NVMe devices.

1.4 SPS: 6.0.5.046 (08/04/2023)

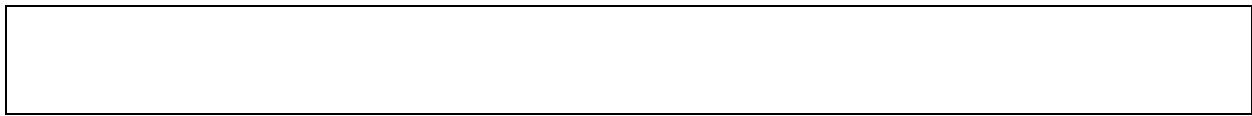
1. Applied the patch to fix the MCC timing issue.
2. Upgraded the codebase to Intel BKC 2023 WW26 PLR2/3.

1.3 SPS: 6.0.4.070 (05/04/2023)

1. Upgraded the codebase to Intel BKC 2023 WW13 PLR1

1.2 SPS: 6.0.4.033 (04/20/2023)

1. Got related information from BF2 cards and reported to BMC through SMBIOS records for enhancement
2. Updated codebase to Intel BKC 2023 WW09 MR2 for enhancement



Product Manager

Date