BIOS Release Notes Form

Product Name	**X14DBHM**
Release Version	**1.3**
Release Date	**04/25/2025**
Previous Version	**1.2**
Update Category	**Recommended**
Dependencies	**None**
Important Notes	**None**
Enhancements	1. **Synced CoreMod to the current status and correct SWAP config in the setup for the SSG_222B_NE3X24R_2U system.** 2. **To fix how VPP was not setting when BPN was installed on MXIO4/5.** 3. **Changed the I2C detect address from portA to portB, fit R1.01/1.02 design.** 4. **Added a SWAP config for the SSG_222B_NE3X24R_2U system.** 5. **Added MB R1.01 and R1.02 conditions to write BPN CPLD.** 6. **Updated the Seamless MCU version to 1.1 for GNR MR1 OOB.** 7. **Fixed how the system was getting stuck at 0x92 due to a returned error status from Tpm2SubmitCommand.** 8. **Updated code base to 5.35_0ACQZ_3544P48_GNRSP_075_BETA.** 9. **Rolled back the hang 0x92 checkpoint enhancements.** 10. **Added more checkpoints for PCI enumeration.** 11. **Corrected the COM port number under UEFI SHELL (with dh command).**

	12. Added a control option for switching the modes between FBO and STD.
	13. Added support for Intel IPU Adapter E2100-CCQDA2HL.
	14. Updated the code base to 5.35_0ACQZ_3544P36_GNRAP_GNRSP_SRFSP_073_BETA.
	15. Updated Birch Stream Granite Rapids SP - MR1 OOB Candidate BKC.
	16. Added Birch Stream Clear Water Forest Pre-Alpha Candidate BKC 2025 WW13 binaries.
	17. Updated Secure Boot DBX to address the AMI SA50300 (CVE-2024-7344\CVE-2023-24932 (8.2 High/6.7 Middle)) security issue.
	18. Revised TOKEN "MICROCODE4_FILES" to include PRQ MCU by checking TOKEN "PRQ_BINARIES_INCLUSION."
	19. Updated Birch Stream Granite Rapids AP - MR1 OOB Candidate BKC - 2025 WW16 binaries.
	20. Removed the warning message, "Crystal Ridge is not support."
	21. Updated Birch Stream Granite Rapids SP R1S - MR1 BKC - 2025 WW12 binaries.
	22. Hid the "Enable SBFT" item.
	23. Supported PCT GNR-SP.
	24. Fixed the issue where the UEFI network setting of boot order by SuperEdit could not change.
	25. Fixed the CPU one core drop issue.
	26. Exposed the "Enable SAF" item.
	27. Updated OEMFID information.
	28. Fixed the build error when Enable SMC_STYLE_CPU_CORES_ENABLE.

	29. Updated the built-in SuperDiag to ECO candidate 1.10.1 version, based on GIT_2bc33501019e721aa4722f4269f0e2bfbcf5c34c.
New features	**None**
Fixes	1. Fixed the NVME count error defined on SSG_222B_NE3X24R_2U. 2. Resolve a DP project build error.

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1. Set AUTO_ACCEPT_PPI to avoid waiting for user acceptance to continue when enabling block SID.
2. Implemented SmcTpmInfo module for TPM Redfish MeasurementSet feature.
3. Updated code base to 5.35_0ACQZ_3544P36_GNRSP_072_8S_BETA.
4. Updated built-in SuperDiag to ECO candidate 1.10.1 version based on GIT_2cf0c012654e12838a24777224d86a6a3811b034.
5. Updated Birch Stream Sierra Forest SP PLR2 Candidate - 2025 binaries and Birch Stream Granite Rapids AP - MR1 Candidate BKC - 2025 WW12 binaries.
6. Enhanced SMC_STYLE_CPU_CORES_ENABLE function by die count.
7. Added VROC OOB related FDF, Token, and GUID; the default was disabled.
8. Supported the SMC_STYLE_CPU_CORES_ENABLE function.
9. Disabled SMC_CORRECTED_MCA_THRESHOLD and filter corrected the error of the processor.
10. Corrected processor event information.
11. Updated code base to 5.35_0ACQZ_3544P34_GNRSP_071.
12. Updated the PN and MAC access method for third-party Intel E810 series.
13. The attribute 'MaxTDPWatts' could not appear at URI '/redfish/v1/Systems/1/Processors/[number].'
14. Fixed the system hang after executing PPR.
15. Fixed how there was no PPR event log.
16. Fixed how some BIOS items' default value could not be changed.

1.0

1. Beta Release.