

IPMI Firmware / BIOS Release Notes Form

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Product Name	X10DRX
Release Version	R2.0b
Release Date	08/01/2017
Previous Version	R2.0a
Update Category	Critical
Dependencies	None
Important Notes	None
Enhancements	<ul style="list-style-type: none"> 1.[Enhancements] Updated Broadwell-EP RC 4.3.0 PLR10 release. 2.[Enhancements] Updated Intel Server Platform Services 3.1.3.38 PLR7 for Grantley Refresh Platforms 3 [Enhancements] Updated CPU microcode SRV_P_192 for Broadwell-EP B0/M0/R0. 4.[Enhancements] Updated CPU microcode SRV_P_191 for Haswell-EP C0,1/M0,1/R2. 5.[Enhancements] Added VB tag to VPD data if not already present. 6.[Enhancements] Set graphic mode as 1024x768 resolution and SMC Logo as 800x600 resolution. 7.[Enhancements] Fixed problem of IPMI boot command being cleared even when persistence boot has been set. 8.[Enhancements] Set SMART to change the charging timeout of supercap to 6m because of coin-shaped supercap. 9.[Enhancements] Set non-Supermicro format VPD data to be skipped and fill VB tag when SMC card has VPD but no VB tag. 10.[Enhancements] Updated SATA RAID OPRM/EFI driver to RSTe PreOS v4.6.0.1018 (RSTe package 4.6.0.1085). 11.[Enhancements] Added tCCD_L Relaxation item under Memory Configuration menu. 12.[Enhancements] Enabled use of Ctrl+R hotkey from remote console for LSI RAID card. 13.[Enhancements] Added "OPROM" string in slot option rom for conforming to Grantley setup template. 14.[Enhancements] Added BBS reset function to SmcOobLoadBiosDefault(). 15.[Enhancements] Changed tCCD_L Relaxation default to 1 and change string from Enabled to Auto. 16.[Enhancements] Appended VPD - VB tag in VPD-W Tag (0x91) area. 17.[Enhancement] Added SumBbsSupportFlag into DAT file. 18.[Enhancements] Updated BIOS ACM 3.1.1 PW, and SINIT ACM 3.1.1 PW.

New features	<p>1.[Features] Added display of Monitor/Mwait setup item in CPU Configuration.</p> <p>2.[Features] Added ability of MECI (Multiple Event Count Increment) and METW (Multiple Event Time Window) to work with memory correct error and PCIe error reports.</p> <p>3.[Features] Implemented Dynamic MMCFG BASE feature.</p>
Fixes	<p>1.[Fixes] Applied AMI EIP EIP274689 to fix some BIOS setup items not working properly with default values loaded on the CPU page.</p> <p>2.[Fixes] Fixed issue where changes in METW or MECI values do not take effect in current boot to OS.</p> <p>3.[Fixes] Fixed problem of Reset Trigger ADR not working.</p> <p>4.[Fixes] Corrected MECI help string typo.</p> <p>5.[Fixes] Fixed system hanging at 0xA9 when the GPNV event log is full.</p> <p>6.[Fixes] Fixed SUM error when running "GetCurrentBiosCfgTextFile" after BIOS setup modified by AMIBCP.</p> <p>7.[Fixes] Fixed inability to change "Processor Frequency" when setting "Boot performance mode" to "Max Efficient" by AMIBCP.</p> <p>8.[Fixes] Fixed system hanging when plugging in an add-on card that contains non-SMC format VPD data.</p> <p>9.[Fixes] Fixed multiple event time count not following the specifications.</p> <p>10.[Fixes] Fixed inability to get HDD information by SUM.</p> <p>11.[Fixes] Fixed issue of power button not being reported in ACPI table when set to "4 Seconds Override".</p> <p>12.[Fixes] Fixed issue of SUM TC306 and TC317 failing in certain configuration cases.</p> <p>13.[Fixes] Fixed problem of system hanging when Watchdog function is enabled before BIOS update.</p> <p>14.[Fixes] Fixed failure of BIOS Auto Recovery.</p> <p>15.[Fixes] Fixed issue with Grantley where Super.rom does not work and SUT hangs on POST 79.</p> <p>16.[Fixed] Fixed problem of NVDIMM setup items appearing when optimized defaults load even without NVDIMM being installed.</p> <p>17.[Fixes] Fixed problem of SMBIOS Type4 CPU2 current speed not showing as zero when CPU2 is not installed.</p> <p>18.[Fixes] Fixed system reset or hanging after Watchdog function is enabled during BIOS update.</p> <p>19.[Fixes] Fixed inability to find correct Memory CECC DIMM location through SD5.</p>

Notes

- All fields must be completed; please indicate as "N/A" if not applicable.
- Release Notes should document functional changes, dependencies, bug fixes and added features.
- Release Notes must not mention the methods of implementation and/or internal processes.
- Release Notes must not mention of silicon errata and/or other external causations.
- Release Notes must not mention confidential/proprietary information and/or trade secrets of Super Micro or any third party.

Release Notes from Previous Release(s) – these will be appended at the bottom of the current release notes in italic and in smaller print

Product Manager

Date

Project Team Leader

Date

Department Head

Date