

IPMI Firmware / BIOS Release Notes Form

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| Product Name | X11DPU-Z |
| Release Version | 2.0 |
| Release Date | 11/30/2017 |
| Previous Version | 1.0 |
| Update Category | Critical |
| Dependencies | None |
| Important Notes | N/A |
| Enhancements | <ol style="list-style-type: none">1. Updated 5.12_PurleyCrb_0ACFD084_BETA for Purley Skylake platform PLR 3.1.2. Updated CPU microcode SRV_P_214 for Skylake-EP H0/M0/U0 stepping CPUs.3. Updated SPS 4.00.04.294 PLR 3.1 PV version.4. Updated BIOS ACM 1.3.4.5. Updated SATA RAID OPROM/EFI driver to RSTe PreOS v5.3.0.1052.6. Added support for 16GB x 4 density DRAM Chip (64GB in total for 2rank x 4 RDIMM).7. Enabled CPU core by Core Count.8. Showed number of active UPI links.9. Removed PCI PERR/SERR Support item and enabled reporting PCIe error by default.10. Implemented SDDC/ADDDC event logging to IPMI. |

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| | <p>11. Added "SMBIOS Preservation" Enabled/Disabled item for flash recovery.</p> |
| New features | <p>1. Set flashing BIOS/ME to be skipped when trying to downgrade the SPS ME firmware from 4.0.4.288 to previous version.</p> |
| Fixes | <p>1. Fixed problem of system hanging at LSI OPROM with two LSI 3108 cards installed.</p> <p>2. Fixed problem of TPM 1.2 PS index not being Write-Protected so that the content of TPM 1.2 PS index still can be modified after TPM 1.2 is nvLocked.</p> <p>3. Fixed problem of serial console output showing SMC logo when EarlyVideo logo item is disabled.</p> |