

IPMI Firmware / BIOS Release Notes Form

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| Product Name | X11SPL-F |
| Release Version | 2.0a |
| Release Date | 12/5/2017 |
| Previous Version | 1.0 |
| Update Category | Critical |
| Dependencies | None |
| Important Notes | N/A |
| Enhancements | <ol style="list-style-type: none">1. Changed BIOS revision to 2.0a.2. Updated 5.12_PurleyCrb_0ACFD084_BETA for Purley Skylake platform PLR 3.1.3. Updated CPU microcode SRV_P_217 for Skylake-EP H0/M0/U0 stepping CPUs.4. Updated SPS XML setting for SPS 4.0.4.294.5. Displayed "PCIe PLL SSC" setup item for clock spectrum function.6. Enabled display of EarlyVideo message by onboard video when VGA Priority is set to Offboard.7. Removed the "System Firmware Error (POST ERROR)" error log from BMC and "EFI 01030006" in BIOS event log.8. Updated BMC LAN configuration for saving settings of BIOS setup menu.9. Updated help string for tCCD relax setup option. |

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| | <p>10. Corrected help string of setup item "Enforce POR".</p> <p>11. Updated new MRC error log definition.</p> <p>12. Allowed runtime memory UCE mapout message to be displayed one time during BIOS POST.</p> <p>13. Masked off PCIe correctable and non-fatal errors.</p> <p>14. Moved Run Sure item to Memory RAS Configuration setup page.</p> <p>15. Updated SATA RAID OPRON/EFI driver to RSTe PreOS v5.3.0.1052.</p> <p>16. Added support for AOC-SLG3-2M2 card.</p> <p>17. Hid memory frequency 2200 and 2600.</p> |
| New features | <p>1. Removed support for Ctrl+home triggering recovery.</p> <p>2. Set display of setup option for always turbo mode function as default.</p> <p>3. Added the "PEI--IPMI Initialization" Post-Help message.</p> <p>4. Added Run Sure setup item.</p> <p>5. Set message "BIOS cannot support downgrade to previous version or ROMID mismatch" to show when trying to downgrade BIOS or flash other model of BIOS.</p> |
| Fixes | <p>1. Fixed failure of Password Preservation Test due to password not being preserved.</p> <p>2. Fixed problem of "Correctable, Non-Fatal and Fatal" error reporting flags being disabled on "Infiniband controller: Mellanox Technologies MT27700 Family [ConnectX-4]" when "PCI PERR/SERR Support" is enabled.</p> <p>3. Fixed failure of setup item "Install Windows 7 USB support".</p> <p>4. Fixed problem of the string of "Error DIMM information" on screen being corrupted when equipping failed DIMM.</p> <p>5. Fixed problem of system sometimes hanging at post code B2 when running Cburn ONOFF.</p> <p>6. Fixed inability to enter setup menu when pressing "DEL" key if there is no boot device.</p> |

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| | <p>7. Fixed problem of SUM GetDmiInfo command error "Invalid DMI information from BIOS" occurring.</p> <p>8. Fixed failure of SMBIOS Type 20 to fill Interleave Position and Interleaved Data Depth correctly.</p> <p>9. Corrected Hynix DIMM info as "SK Hynix".</p> <p>10. Fixed SUM Test Case #216.</p> <p>11. Fixed problem of IPMI SEL logging "Memory training failure." and "No memory DIMM detected, install memory DIMMs." twice per reboot.</p> <p>12. Fixed problem of TPM 1.2 PS index not being Write-Protected so that the content of TPM 1.2 PS index still can be modified after TPM 1.2 is nvLocked.</p> <p>13. Corrected the "Save Changes" setup option string in "Save & Exit" menu.</p> <p>14. Fixed problem of system generating abnormal strings under DOS after triggering SERR or PERR error event in PCH slot.</p> <p>15. Fixed problem of system hanging when using AFUWIN/AFULNX to flash BIOS under OS.</p> <p>16. Fixed problem of DMI clearing when running SUM LoadDefaultBiosCfg.</p> |
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