# IPMI Firmware / BIOS Release Notes Form

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| **Product Name** | **X11DPU-Z+** |
| **Release Version** | **2.0a** |
| **Release Date** | **12/8/2017** |
| **Previous Version** | **2.0** |
| **Update Category** | **Critical** |
| **Dependencies** | **None** |
| **Important Notes** | **N/A** |
| **Enhancements** | **1. Updated CPU microcode SRV_P_217 for Skylake-EP H0/M0/U0 stepping CPUs.**<br>**2. Added Setup Item "SmcBusMasterEn" to enable Parent bridge of the devices in the gSmcPciForceEnableBusMasterList.**<br>**3. Added new IPMI Standard Event feature.** |
| **New features** | **N/A** |
| **Fixes** | **N/A** |