## **IPMI Firmware / BIOS Release Notes Form**

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Product Name	X11SPG-TF
Release Version	2.0a
Release Date	12/6/2017
Previous Version	1.0a
Update Category	Critical
Dependencies	None
Important Notes	N/A
Enhancements	1. Updated CPU microcode SRV_P_217 for Skylake-EP H0/M0/U0
	stepping CPUs.
	2. Enabled IERR crash dump function.
	3. Updated CPU microcode SRV_P_214 for Skylake-EP H0/M0/U0
	stepping CPUs.
	4. Updated 5.12_PurleyCrb_0ACFD084_BETA for Purley Skylake
	platform PLR 3.1.
	5. Updated SPS 4.00.04.294 PLR 3.1 PV version.
New features	1. Added UIO LAN control function.
	2. Implemented SMC OOB TPM Provisioning via IPMI Feature for
	customized provisioning table.
Fixes	1. Fixed problem of some platforms hanging up at POST code 0xB2
	when equipping dTPM module.

- 2. Fixed problem of DMI being cleared when SUM LoadDefaultBiosCfg is run.
- 3. Fixed problem of system rebooting endlessly when equipping dTPM module.
- 4. Fixed problem of TPM 2.0 PS NV Index not being write-protected even if customized provisioning table indicates that it must be write-protected when using "SMC OOB TPM Provisioning via IPMI feature".
- 5. Fixed problem of system hanging during BIOS flashing if Watch Dog function is enabled.
- 6. Corrected the "Save Changes" setup option string in "Save & Exit" menu.
- 7. Disabled SNC once NVDIMM is present in system.
- 8. Fixed problem of changes to CPU core Enable/Disable in setup menu sometimes not taking effect on Windows OS.
- 9. Fixed problem of system generating abnormal strings under DOS after triggering SERR or PERR error event in PCH slot.