

IPMI Firmware / BIOS Release Notes Form

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Product Name	X11SPM-F/TF/TPF
Release Version	2.0
Release Date	11/29/2017
Previous Version	1.0
Update Category	Critical
Dependencies	None
Important Notes	N/A
Enhancements	<ol style="list-style-type: none">1. Enabled display of EarlyVideo message by onboard video when VGA Priority is set to Offboard.2. Removed the "System Firmware Error (POST ERROR)" error log from BMC and "EFI 01030006" in BIOS event log.3. Added support for M.2 slot (PCIe M.2).4. Updated 5.12_PurleyCrb_0ACFD084_BETA for Purley Skylake platform PLR 3.1.5. Updated SPS_E5_04.00.03.235.0.6. Updated BMC LAN configuration for saving settings of BIOS setup menu.7. Set to avoid TCG protocol "PassThroughToTpm" failure under EFI shell when user enables "TXT Support" with disabled "TPM state" in TPM 1.2 BIOS setup menu.8. Changed BIOS revision to 2.0.

	<p>9. Updated help string for tCCD relax setup option.</p> <p>10. Updated CPU microcode SRV_P_209 for Skylake-EP H0 stepping CPUs.</p> <p>11. Corrected help string of setup item "Enforce POR".</p> <p>12. Updated new MRC error log definition.</p> <p>13. Allowed runtime memory UCE mapout message to be displayed one time during BIOS POST.</p> <p>14. Increased SataRaidOromDelay & SataRaidOromDelay to 6 seconds.</p> <p>15. Updated SPS 4.00.04.294 PLR 3.1 PV version.</p> <p>16. Updated CPU microcode SRV_P_214 for Skylake-EP H0/M0/U0 stepping CPUs.</p> <p>17. Updated BIOS ACM 1.3.4.</p> <p>18. Updated SATA RAID OPROM/EFI driver to RSTe PreOS v5.3.0.1052.</p> <p>19. Set message "BIOS cannot support downgrade to previous version or ROMID mismatch" to show when trying to downgrade BIOS or flash other model of BIOS.</p> <p>20. Updated NVM to LBGHAD2A.bin.</p> <p>21. Hid "IPMI" and "Secure Boot" forms for SUM.</p>
New features	<p>1. Removed support for Ctrl+home triggering recovery.</p> <p>2. Set display of setup option for always turbo mode function as default.</p> <p>3. Exported "IMC Interleaving" item for performance tuning.</p> <p>4. Displayed "PCIe PLL SSC" setup item for clock spectrum function.</p> <p>5. Added the "PEI--IPMI Initialization" Post-Help message.</p> <p>6. Added setup item for Samsung tRWSR workaround.</p> <p>7. Displayed setup item "Core Disable Bitmap(Hex)" in BIOS CPU Configuration page.</p>

Fixes	<ol style="list-style-type: none"> 1. Fixed problem of "Correctable, Non-Fatal and Fatal" error reporting flags being disabled on "Infiniband controller: Mellanox Technologies MT27700 Family [ConnectX-4]" when "PCI PERR/SERR Support" is enabled. 2. Fixed inability of the system to enter TXT Environment when using TPM 1.2 on production platform. 3. Fixed problem of TPM 2.0 BIOS-Based Provisioning table not setting PW flag to PS LCP Data for production platform. 4. Fixed failure of setup item "Install Windows 7 USB support". 5. Fixed failure of Password Preservation Test due to password not being preserved. 6. Fixed problem of the string of "Error DIMM information" on screen being corrupted when equipping failed DIMM. 7. Fixed inability of Intel P3100 M.2 to boot to OS. 8. Fixed inability to enter setup menu when pressing "DEL" key if there is no boot device. 9. Fixed problem of SUM GetDmiInfo command error "Invalid DMI information from BIOS" occurring. 10. Fixed failure of SMBIOS Type 20 to fill Interleave Position and Interleaved Data Depth correctly. 11. Fixed problem of system hanging when set to NVMe VMD mode for CPU PCIe Root Port. 12. Fixed BMC PCIe root port being forced to Gen1. 13. Fixed problem of Redfish attribute "SDDC" display name not matching the BIOS page. 14. Corrected Hynix DIMM info as "SK Hynix". 15. Fixed problem of system hanging when using AFUWIN/AFULNX to flash BIOS under OS. 16. Fixed problem of changes to CPU core Enable/Disable in setup menu sometimes not taking effect on Windows OS.
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	<p>17. Corrected the "Save Changes" setup option string in "Save & Exit" menu.</p> <p>18. Fixed problem of system hanging during BIOS flashing if Watch Dog function is enabled.</p> <p>19. Fixed problem of DMI clearing when running SUM LoadDefaultBiosCfg.</p>
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