## **IPMI Firmware / BIOS Release Notes Form**

Supermicro disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade. All products, computer systems, dates, and figures specified are preliminary based on current expectations, and are subject to change without notice. Supermicro and the Supermicro logo are trademarks of Super Micro Computer, Inc. in the U.S. and/or other countries. Copyright © 2018 Super Micro Computer, Inc. All rights reserved.

o 32MB.	
or Skylake-FP	
2. Rolled back CPU microcode to MB750654_02000035 for Skylake-EP H0/M0/U0 stepping CPUs.	
. Fixed problem of PCIe slot 7 strings name in BIOS setup not natching motherboard's silkscreen. . Fixed failure to check IOBP settings.	

Release Notes from Previous Release(s) - these will be appended at the bottom of the current release notes in italic and in smaller print