

## IPMI Firmware / BIOS Release Notes Form

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<b>Product Name</b>	<b>X10DRT-P/PT/PIBF/PIBQ</b>
<b>Release Version</b>	<b>3.0a</b>
<b>Release Date</b>	<b>2/9/2018</b>
<b>Previous Version</b>	<b>2.0b</b>
<b>Update Category</b>	<b>Critical</b>
<b>Dependencies</b>	<b>None</b>
<b>Important Notes</b>	<b>N/A</b>
<b>Enhancements</b>	<b>1. Implemented enhancement to address ‘Spectre’ variant 2 (CVE 2017-5715) security patch issue.</b> <b>2. Changed BIOS revision to 3.0a.</b> <b>3. Updated Broadwell-EP RC 4.3.0 PLR11 release.</b> <b>4. Updated BIOS ACM 3.1.1 PW and SINIT ACM 3.1.1 PW.</b> <b>5. Added tCCD_L Relaxation item under Memory Configuration menu.</b> <b>6. Added SumBbsSupportFlag into DAT file.</b> <b>7. Changed Memory correctable threshold to 100 and enabled Cloaking for Broadwell CPU E5-26xx SKU.</b>
<b>New features</b>	<b>1. Added support for JEDEC NVDIMM.</b> <b>2. Implemented SMC Recovery Flash Boot Block feature.</b> <b>3. Implemented dynamic update SATA re-driver settings for 1U and 2U backplane.</b>

<b>Fixes</b>	<ol style="list-style-type: none"><li data-bbox="467 239 1214 275">1. Added Ramaxel JEDEC ID to support Ramaxel memory.</li><li data-bbox="467 297 1366 387">2. Fixed problem of NVDIMM setup items appearing when optimized defaults load even without NVDIMM being installed.</li><li data-bbox="467 409 1353 499">3. Fixed problem of SMBIOS Type4 CPU2 current speed not showing as zero when CPU2 is not installed.</li><li data-bbox="467 521 1310 611">4. Fixed problem of system resetting or hanging after Watch Dog function is enabled during BIOS update.</li><li data-bbox="467 633 1190 669">5. Fixed problem of MWAIT being disabled on BSP only.</li><li data-bbox="467 692 1278 728">6. Fixed inability of SUM to get COM2/SOL settings from BIOS.</li><li data-bbox="467 750 1366 786">7. Fixed issue of BIOS setup menu having "PCI Latency Timer" option.</li></ol>
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