

IPMI Firmware / BIOS Release Notes Form

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| Product Name | X11DPU |
| Release Version | 2.0b |
| Release Date | 02/24/2018 |
| Previous Version | 2.0a |
| Update Category | Critical |
| Dependencies | None |
| Important Notes | None |
| Enhancements | 1. Updated CPU microcode SRV_B_216 for Skylake-SP H0/M0/U0 stepping CPUs – Security Patch. 2. Added setup item "SmcBusMasterEn" for enabling Parent bridge of the devices in the gSmcPciForceEnableBusMasterList. 3. Updated Purley RC 151.R03. 4. Displayed setup item "ACS Control" to Enable/Disable PCIe Access Control Services Extended Capability, with Enabled as default. |
| New features | 1. Changed RC default setting to upgrade POST Memory correctable error to uncorrectable error and map out the channel. |
| Fixes | 1. Fixed problem of DMI being cleared when running SUM LoadDefaultBiosCfg. 2. Disabled CPU2 IIO PCIe root port ACPI hot plug function. 3. Fixed issue with IPMI force boot. |

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| | <p>4. Fixed problem of SMC skipping first boot option in UEFI mode when retrying boot.</p> <p>5. Fixed inability to reset the system under DOS by pressing "Ctrl-Alt-Del" on USB keyboard when "Port 60/64 Emulation" is disabled.</p> <p>6. Fixed inability of some CPUs to display correct microcode revision under BIOS setup menu.</p> |
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