IPMI Firmware / BIOS Release Notes Form

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Product Name	X10DRD-L/-iNT/-LT
Release Version	3.0a
Release Date	2/8/2018
Previous Version	2.0b
Update Category	Critical
Dependencies	None
Important Notes	None
Enhancements	1. Changed BIOS revision to 3.0a.
	2. Updated SATA RAID OPROM/EFI driver to RSTe PreOS v4.7.0.1014.
	3. Updated Intel Server Platform Services 3.1.3.50 for Grantley
	Refresh Platforms.
	4. Updated Haswell-EP C0,1/M0,1/R2 CPU microcode
	M6F306F2_0000003C and Broadwell-EP B0/M0/R0 CPU microcode
	MEF406F1_0B00002A from SRV_B_204 to address CVE-2017-5715.
	5. Removed support for using Ctrl+home to trigger recovery.
	6. Changed Memory correctable threshold to 100 and enabled
	Cloaking for Broadwell CPU E5-26xx SKU.
	7. Updated BIOS ACM 3.1.1 PW and SINIT ACM 3.1.1 PW.
	8. Added BBS reset function to SmcOobLoadBiosDefault().
	9. Changed tCCD_L Relaxation default to 1 and changed string from
	Enabled to Auto.

	10. Appended VPD - VB tag in VPD-W Tag(0x91) area.
	11. Updated Broadwell-EP RC 4.3.0 PLR11 release.
	12. Added SumBbsSupportFlag into DAT file.
	13. Added support for LSI 3008/3016 HBA sensor.
	14. Updated new MRC error log definition.
	15. Added support for JEDEC NVDIMM.
	16. Enabled OA1_SUPPORT to fix inability to modify OEM Data by
	AMIBCP.exe.
	17. Added Intel SPI vulnerability patch for Grantley.
	18. Updated Broadwell-EP B0/M0/R0 CPU uCode
	MEF406F1_0B000022.
	19. Added "SMBIOS Preservation" Enabled/Disabled item for flash
	recovery.
New features	1. Displayed setup item "ACS Control" to Enable/Disable PCIe Access
	Control Services Extended Capability.
	2. Implemented SMC Recovery Flash Boot Block feature.
Fixes	1. Fixed missing Manufacturer string in SMBIOS type 17 when
	Ramaxel DIMM is plugged in.
	2. Fixed problem of NVDIMM setup items appearing when optimized
	defaults load even without NVDIMM being installed.
	3. Fixed problem of SMBIOS Type4 CPU2 current speed not showing
	as zero when CPU2 is not installed.
	4. Fixed problem of system resetting or hanging after Watch Dog
	function is enabled and then BIOS is updated.
	5. Fixed inability to get correct Memory CECC DIMM location via SD5.
	6. Fixed problem of system hanging at 79 with some NVDIMMs.
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Release Notes from Previous Release(s) – these will be appended at the bottom of the current release notes in italic and in smaller print	
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