IPMI Firmware / BIOS Release Notes Form

Product Name	**X10DRL-i (All)**
Release Version	**3.0a**
Release Date	**2/9/2018**
Previous Version	**2.0b**
Update Category	**Critical**
Dependencies	**N/A**
Important Notes	**ECO #20440** **BIOS Image X10DRL8.209** **Please update BIOS with attached Flash Utility in package.**
Enhancements	1. **Updated CPU microcode to address CVE-2017-5715 security issue.** 2. **Changed BIOS revision to 3.0a.** 3. **Updated Broadwell-EP RC 4.3.0 PLR11 release.** 4. **Updated BIOS ACM 3.1.1 PW and SINIT ACM 3.1.1 PW.** 5. **Added tCCD_L Relaxation item under Memory Configuration menu.** 6. **Added SumBbsSupportFlag into DAT file.** 7. **Changed Memory correctable threshold to 100 and enabled Cloaking for Broadwell CPU E5-26xx SKU.**
New features	1. **Added support for JEDEC NVDIMM.** 2. **Implemented SMC Recovery Flash Boot Block feature.**
Fixes	1. **Added Ramaxel JEDEC ID to support Ramaxel memory.** 2. **Fixed problem of NVDIMM setup items appearing when optimized defaults load even without NVDIMM being installed.** 3. **Fixed problem of SMBIOS Type4 CPU2 current speed not showing as zero when CPU2 is not installed.** 4. **Fixed problem of system resetting or hanging after Watch Dog function is enabled during BIOS update.** 5. **Fixed problem of MWAIT being disabled on BSP only.**