IPMI Firmware / BIOS Release Notes Form

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Product Name	X11SPW-(C)TF
Release Version	2.0b
Release Date	2/26/2018
Previous Version	2.0a
Update Category	Critical
Dependencies	None
Important Notes	None
Enhancements	1. Updated CPU microcode to address 'Spectre' variant 2 (CVE 2017-
	5715) security issue.
	2. Updated 5.12_PurleyCrb_0ACFD085Beta for Purley Skylake
	platform PLR 4, BKC WW02.
	3. Enabled IERR crash dump function.
	4. Added setup item "SmcBusMasterEn" for enabling Parent bridge of
	the devices in the gSmcPciForceEnableBusMasterList.
	5. Displayed setup item "ACS Control" to Enable/Disable PCIe Access
	Control Services Extended Capability, with Enabled as default.
	6. Changed BIOS revision to 2.0b.
New features	1. Implemented SMC OOB TPM Provisioning via IPMI Feature for
	customized provisioning table.
	2. Changed RC default setting to upgrade POST Memory correctable
	error to uncorrectable error and map out the channel.

	1. Changed the disabling of CPU Core by core number.
	2. Corrected the "Save Changes" setup option string in "Save & Exit"
	menu.
	3. Fixed problem of system hanging during BIOS flashing if Watch Dog
	function is enabled.
	4. Fixed problem of TPM 2.0 PS NV Index not being write-protected
	even if customized provisioning table indicates that it must be write-
	protected when using "SMC OOB TPM Provisioning via IPMI feature".
	5. Fixed problem of DMI being cleared when SUM LoadDefaultBiosCfg
	is run.
	6. Fixed issue with IPMI force boot.
Fixes	7. Fixed issue of all commands requesting to be persistent.
	8. Fixed malfunction of "SMBIOS Preservation" Disabled.
	9. Fixed problem of SMC skipping first boot option in UEFI mode
	when retrying boot.
	10. Fixed problem of the endpoint PCIe device having error bits in PCI
	Status or Device Status register.
	11. Fixed inability to set memory policy.
	12. Fixed inability to reset the system under DOS by pressing "Ctrl-Alt-
	Del" on USB keyboard when "Port 60/64 Emulation" is disabled.
	13. Fixed failure of BIOS ECO ATT test case 306.
	14. Fixed inability of some CPUs to display correct microcode revision
	under BIOS setup menu.