

IPMI Firmware / BIOS Release Notes Form

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Product Name	X10DRT-B+
Release Version	3.0a
Release Date	2/13/2018
Previous Version	2.0
Update Category	Critical
Dependencies	None
Important Notes	None
Enhancements	<ol style="list-style-type: none">1. Implemented enhancement to address 'Spectre' variant 2 (CVE 2017-5715) security patch issue.2. Updated Broadwell-EP RC 4.3.0 PLR11 release.3. Updated Intel Server Platform Services 3.1.3.50 for Grantley Refresh Platforms.4. Set graphic mode as 1024x768 resolution and SMC Logo as 800x600 resolution.5. Updated SATA RAID OROM/EFI driver to RSTe PreOS v4.7.0.1014 (RSTe SATA 4.7.0.1069 and NVMe 4.7.0.2063).6. Enabled use of Ctrl+R hotkey from remote console for LSI RAID card.7. Added tCCD_L Relaxation item under Memory Configuration menu.8. Displayed setup item "ACS Control" to Enable/Disable PCIe Access Control Services Extended Capability.

	<p>9. Updated BIOS ACM 3.1.1 PW and SINIT ACM 3.1.1 PW.</p> <p>10. Added SumBbsSupportFlag into DAT file.</p> <p>11. Removed SIOM report for SMBIOS type 41 so that SIOM only reports on SMBIOS type 9.</p> <p>12. Implemented dynamic update of SMBIOS type 9 for different backplane and riser.</p> <p>13. Added "SMBIOS Preservation" Enabled/Disabled item for flash recovery.</p> <p>14. Enhanced SMC Recovery Flash Boot Block feature.</p> <p>15. Fixed inability of SUM to get COM2/SOL settings from BIOS.</p> <p>16. Fixed inability of SUM utility to get "Setup Prompt Timeout" setup item.</p>
New features	<p>1. Implemented Dynamic MMCFG BASE feature.</p> <p>2. Added new support for backplane adapter cards.</p> <p>3. Added support for JEDEC NVDIMM.</p> <p>4. Added support for AOC-SLG3-2M2 auto bifurcation on CPU1 SXB1 SLOT.</p> <p>5. Implemented SMC Recovery Flash Boot Block Feature, adding message to inform user with the POST screen message, "The System Is Going To Reset And Then Entering To Recovery Mode Again..."</p> <p>6. Added support for System Firmware Progress feature.</p>
Fixes	<p>1. Fixed multiple event time count not following the specifications.</p> <p>2. Fixed problem of IPMI boot command being cleared even when persistence boot has been set.</p> <p>3. Fixed issue of power button not being reported in ACPI table when set to "4 Seconds Override".</p> <p>4. Fixed inability of the system to boot at debug card showing 00h after over 50% of boot block is erased and JBR1 is set to pins 2-3 (BIOS RECOVERY).</p> <p>5. Fixed failure of BIOS Auto Recovery.</p> <p>6. Fixed problem of system resetting or hanging after Watch Dog</p>

	<p>function is enabled during BIOS update.</p> <p>7. Fixed issue of Super.rom not working and SUT hanging on POST 79.</p> <p>8. Fixed problem of NVDIMM setup items appearing when optimized defaults load even without NVDIMM being installed.</p> <p>9. Fixed problem of MWAIT being disabled on BSP only.</p> <p>10. Fixed inability to get correct Memory CECC DIMM location via SD5.</p> <p>11. Fixed problem of SMBIOS Type4 CPU2 current speed not showing as zero when CPU2 is not installed.</p> <p>12. Fixed problem of system hanging at POST 0xA9 when entering setup menu while Intel P4800X uEFI driver is loaded.</p> <p>13. Updated FlashDriver module to Label 5 in order to fix inability of system to enter recovery mode when MAIN block is updated 45% and then system powers off.</p>
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