

IPMI Firmware / BIOS Release Notes Form

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Product Name	X11DSC+
Release Version	2.0b
Release Date	2/26/2018
Previous Version	2.0a
Update Category	Critical
Dependencies	None
Important Notes	None
Enhancements	<ol style="list-style-type: none">1. Updated 5.12_PurleyCrb_0ACFD085Beta for Purley Skylake platform PLR 4, BKC WW02.2. Updated SPS 4.00.04.294 PLR 3.1 PV version.3. Updated DMI CTLE value.4. Updated re-driver for AOC-SLG3-2E4R.5. Added setup item "SmcBusMasterEn" for enabling Parent bridge of the devices in the gSmcPciForceEnableBusMasterList.6. Displayed setup item "ACS Control" to Enable/Disable PCIe Access Control Services Extended Capability, with Enabled as default.7. Updated CPU microcode SRV_B_216 for Skylake-SP H0/M0/U0 stepping CPUs.8. Updated BIOS version to 2.0b.9. Updated BIOS ACM 1.3.5 and SINIT ACM 1.3.3.10. Changed maximum speed in SMBIOS type 4 to 4500Mhz.

	<p>11. Added one event log to record that the event log is full.</p>
New features	<p>1. Changed RC default setting to upgrade POST Memory correctable error to uncorrectable error and map out the channel.</p>
Fixes	<p>1. Fixed inability of BIOS to boot into OS with Intel P3608 PCIe NVMe drive installed.</p> <p>2. Fixed issue with IPMI force boot.</p> <p>3. Fixed issue of all commands requesting to be persistent.</p> <p>4. Fixed malfunction of "SMBIOS Preservation" Disabled.</p> <p>5. Fixed problem of SMC skipping first boot option in UEFI mode when retrying boot.</p> <p>6. Fixed problem of the endpoint PCIe device having error bits in PCI Status or Device Status register.</p> <p>7. Fixed inability to set memory policy.</p> <p>8. Fixed inability to reset the system under DOS by pressing "Ctrl-Alt-Del" on USB keyboard when "Port 60/64 Emulation" is disabled.</p> <p>9. Fixed failure of BIOS ECO ATT test case 306.</p> <p>10. Disabled CPU2 IIO PCIe root port ACPI hot plug function.</p> <p>11. Fixed inability of some CPUs to display correct microcode revision under BIOS setup menu.</p>