

IPMI Firmware / BIOS Release Notes Form

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Product Name	X10DRX
Release Version	3.0a
Release Date	2/8/2018
Previous Version	2.0b
Update Category	Critical
Dependencies	None
Important Notes	None
Enhancements	<ol style="list-style-type: none">1. Updated SATA RAID OPRM/EFI driver to RSTe PreOS v4.7.0.1014.2. Updated Intel Server Platform Services 3.1.3.50 for Grantley Refresh Platforms.3. Updated CPU microcode SRV_B_204 for Haswell-EP C0,1/M0,1/R2 & Broadwell-EP B0/M0/R0.4. Removed support for using Ctrl+home to trigger recovery.5. Changed BIOS revision to 3.0a.6. Changed Memory correctable threshold to 100 and enabled Cloaking for Broadwell CPU E5-26xx SKU.7. Updated Broadwell-EP RC 4.3.0 PLR10 release.8. Removed the "P1" string when Memory error occurs on UP server.9. Corrected POST diagnostic signOn string.10. Patched Micron Z11C DIMM to improve stability.

	<p>11. Changed tCCD_L Relaxation default to 1 and changed string from Enabled to Auto.</p> <p>12. Appended VPD - VB tag in VPD-W Tag(0x91) area.</p> <p>13. Updated CPU microcode SRV_P_192 for Broadwell-EP B0/M0/R0.</p> <p>14. Updated new MRC error log definition.</p> <p>15. Updated Intel Server Platform Services 3.1.3.38 PLR7 for Grantley Refresh Platforms.</p> <p>16. Updated CPU microcode SRV_P_181 for Broadwell-EP B0/M0/R0.</p> <p>17. Updated CPU microcode SRV_P_182 for Haswell-EP C0,1/M0,1/R2.</p> <p>18. Added Intel SPI vulnerability patch for Grantley.</p> <p>19. Updated Broadwell-EP B0/M0/R0 CPU uCode MEF406F1_0B000022.</p> <p>20. Added "SMBIOS Preservation" Enabled/Disabled item for flash recovery.</p> <p>21. Enhanced SMC Recovery Flash Boot Block feature.</p>
New features	<p>1. Displayed setup item "ACS Control" to Enable/Disable PCIe Access Control Services Extended Capability.</p> <p>2. Added BBS reset function to SmcOobLoadBiosDefault().</p> <p>3. Added SumBbsSupportFlag into DAT file.</p> <p>4. Added support for JEDEC NVDIMM.</p> <p>5. Added support for LSI 3008/3016 HBA sensor.</p> <p>6. Displayed Monitor/Mwait setup item.</p> <p>7. Added ability of MECI (Multiple Event Count Increment) and METW (Multiple Event Time Window) to work with memory correct error and PCIe error reports.</p> <p>8. Added VB tag to VPD data if not already present.</p> <p>9. Implemented SMC Recovery Flash Boot Block feature.</p>
Fixes	<p>1. Fixed issue of SUM TC306 and TC317 failing in certain configuration cases.</p>

	<p>2. Fixed problem of system hanging after Watch Dog function is enabled and then BIOS is updated.</p> <p>3. Fixed incorrect memory error location on UP server.</p> <p>4. Fixed missing Manufacturer string in SMBIOS type 17 when Ramaxel DIMM is plugged in.</p> <p>5. Fixed problem of NVDIMM setup items appearing when optimized defaults load even without NVDIMM being installed.</p> <p>6. Fixed problem of system hanging on POST code 0xB2.</p> <p>7. Fixed problem of system resetting or hanging after Watch Dog function is enabled during BIOS update.</p> <p>8. Fixed inability to get correct Memory CECC DIMM location via SD5.</p> <p>9. Fixed inability to log Patrol Scrub UCE.</p> <p>10. Fixed problem of system hanging at 79 with some NVDIMMs.</p> <p>11. Applied AMI EIP EIP274689 to fix some BIOS setup items not working properly with default values loaded on the CPU page.</p> <p>12. Fixed issue where changes in METW or MECI values do not take effect in current boot to OS.</p> <p>13. Corrected MECI help string typo.</p> <p>14. Fixed system hanging at 0xA9 when the GPNV event log is full.</p> <p>15. Fixed system hanging at POST 0xB2 when installing Mellanox MCX311a and MCX312C-XCCT.</p> <p>16. Fixed SUM error when running "GetCurrentBiosCfgTextFile" after BIOS setup modified by AMIBCP.</p> <p>17. Fixed inability to change "Processor Frequency" when setting "Boot performance mode" to "Max Efficient" by AMIBCP.</p> <p>18. Fixed system hanging when plugging in an add-on card that contains non-SMC format VPD data.</p> <p>19. Fixed multiple event time count not following the specifications.</p> <p>20. Fixed problem of incorrect PCI slot number showing on Legacy/UEFI mode.</p>
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Release Notes from Previous Release(s) – these will be appended at the bottom of the current release notes in italic and in smaller print