

# **Notes to Release Notes Authors**

- All fields must be completed; please indicate as “N/A” if not applicable.
- Release Notes should document functional changes, dependencies, bug fixes and added features.
- Release Notes must not mention the methods of implementation and/or internal processes.
- Release Notes must not mention of silicon errata and/or other external causations.
- Release Notes must not mention confidential/proprietary information and/or trade secrets of Supermicro or any third party.
- Please delete this page and the signature fields before submitting the document in PDF Format to Marketing for publication.

## IPMI Firmware / BIOS Release Notes Form

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<b>Product Name</b>	<b>X10QRH+</b>
<b>Release Version</b>	<b>3.0a</b>
<b>Release Date</b>	<b>3/1/2018</b>
<b>Previous Version</b>	<b>3.0</b>
<b>Update Category</b>	<b>Recommended</b>
<b>Dependencies</b>	<b>None</b>
<b>Important Notes</b>	<b>None</b>
<b>Enhancements</b>	<b>1. Updated CPU microcode of Broadwell B0, M0, and R0 to version 2A and Haswell C0, C1, and M1 to version 3C. 2. Updated CPU4 PCIE RX_CTLE_PEAK_GEN3_0 and RX_CTLE_PEAK_GEN3_1 per SI lab requirement.</b>
<b>New features</b>	<b>N/A</b>
<b>Fixes</b>	<b>N/A</b>

***Release Notes from Previous Release(s)*** – these will be appended at the bottom of the current release notes in italic and in smaller print

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*Product Manager*

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*Date*