IPMI Firmware / BIOS Release Notes Form

Product Name	**X11DPX-T**
Release Version	**2.0c**
Release Date	**3/13/2018**
Previous Version	**2.0b**
Update Category	**Critical**
Dependencies	**None**
Important Notes	**None**
Enhancements	**1. Updated CPU microcode SRV_P_229 for Skylake-SP H0/M0/U0 stepping CPUs to address 'Spectre' variant 2 (CVE 2017-5715) security issue.** **2. Changed BIOS revision to 2.0c.** **3. Displayed setup item "ACS Control" to Enable/Disable PCIe Access Control Services Extended Capability, with Enabled as default.** **4. Added support for VMD settings to be preserved after flashing, with disabled as default.** **5. Changed maximum speed in SMBIOS type 4 to 4500Mhz.**
New features	**1. Changed RC default setting to upgrade POST Memory correctable error to uncorrectable error and map out the channel.**
Fixes	**1. Fixed problem of DMI being cleared when running SUM LoadDefaultBiosCfg.**

	2. Disabled CPU2 IIO PCIe root port ACPI hot plug function.
	3. Fixed issue with IPMI force boot.
	4. Fixed inability to set memory policy.
	5. Fixed inability to reset the system under DOS by pressing "Ctrl-Alt-Del" on USB keyboard when "Port 60/64 Emulation" is disabled.
	6. Fixed problem of SMC skipping first boot option in UEFI mode when retrying boot.
	7. Fixed inability of some CPUs to display correct microcode revision under BIOS setup menu.
	8. Patched a workaround for some motherboards' on board LAN having correctable error or GEN speed drop.