

IPMI Firmware / BIOS Release Notes Form

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Product Name	X10DRL-i
Release Version	3.1
Release Date	6/7/2018
Previous Version	3.0a
Update Category	Critical
Dependencies	N/A
Important Notes	ECO #20966 BIOS Image X10DRL8.607 Please update BIOS with attached Flash Utility in package.
Enhancements	1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue. 2. Changed BIOS version to 3.1. 3. Added support for IPMI IPV6. 4. Removed unsupported memory frequency options from setup menu. 5. Updated Broadwell-EP RC 4.4.0 release.
New features	N/A
Fixes	1. Fixed problem of Afu /O command clearing SMC SMBIOS region (\$SMC). 2. Fixed problem of SUM OOB GetSataInfo always showing "Configuration Type" as "AHCI" when setting "Configure SATA as" to "RAID" or "IDE". 3. Set Descriptor Region of BIOS Region Write Access to "No".

3.0a (2/9/2018)

1. *Updated CPU microcode to address CVE-2017-5715 security issue.*
2. *Changed BIOS revision to 3.0a.*
3. *Updated Broadwell-EP RC 4.3.0 PLR11 release.*
4. *Updated BIOS ACM 3.1.1 PW and SINIT ACM 3.1.1 PW.*
5. *Added tCCD_L Relaxation item under Memory Configuration menu.*
6. *Added SumBbsSupportFlag into DAT file.*
7. *Changed Memory correctable threshold to 100 and enabled Cloaking for Broadwell CPU E5-26xx SKU.*
8. *Added support for JEDEC NVDIMM.*
9. *Implemented SMC Recovery Flash Boot Block feature.*
10. *Added Ramaxel JEDEC ID to support Ramaxel memory.*
11. *Fixed problem of NVDIMM setup items appearing when optimized defaults load even without NVDIMM being installed.*
12. *Fixed problem of SMBIOS Type4 CPU2 current speed not showing as zero when CPU2 is not installed.*
13. *Fixed problem of system resetting or hanging after Watch Dog function is enabled during BIOS update.*
14. *Fixed problem of MWAIT being disabled on BSP only*