

IPMI Firmware / BIOS Release Notes Form

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Product Name	X10DRT-H/HIBF
Release Version	3.1
Release Date	6/9/2018
Previous Version	3.0a
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue. 2. Added support for IPMI IPV6. 3. Removed unsupported memory frequency options from setup menu. 4. Updated Broadwell-EP RC 4.4.0 release.
New features	N/A
Fixes	1. Fixed problem of Afu /O command clearing SMC SMBIOS region (\$SMC). 2. Fixed problem of SUM OOB GetSataInfo always showing "Configuration Type" as "AHCI" when setting "Configure SATA as" to "RAID" or "IDE".

	3. Fixed inability to enter setup menu by pressing "DEL" key if Re-try Boot feature is enabled and there are no boot devices. 4. Set Descriptor Region of BIOS Region Write Access to "No".
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Release Notes from Previous Release(s)

3.0a (02/8/2018)

1. Implemented enhancement to address 'Spectre' variant 2 (CVE 2017-5715) security patch issue.
2. Updated Broadwell-EP RC 4.3.0 PLR11 release.
3. Updated Intel Server Platform Services 3.1.3.50 for Grantley Refresh Platforms.
4. Updated SATA RAID OPROM/EFI driver to RSTe PreOS v4.7.0.1014 (RSTe SATA 4.7.0.1069 and NVMe 4.7.0.2063).
5. Added tCCD_L Relaxation item under Memory Configuration menu.
6. Updated BIOS ACM 3.1.1 PW and SINIT ACM 3.1.1 PW.
7. Added SumBbsSupportFlag into DAT file.
8. Added "SMBIOS Preservation" Enabled/Disabled item for flash recovery.
9. Enhanced SMC Recovery Flash Boot Block feature.
10. Fixed inability of SUM to get COM2/SOL settings from BIOS.
11. Fixed inability of SUM utility to get "Setup Prompt Timeout" setup item.
12. Added support for UEFI mode PXE boot via F12 hot key Net boot.
13. Added ability of MECI (Multiple Event Count Increment) and METW (Multiple Event Time Window) to work with memory correct error and PCIe error reports.
14. Implemented Dynamic MMCFG BASE feature.
15. Added support for JEDEC NVDIMM.
16. Implemented SMC Recovery Flash Boot Block feature.
17. Fixed system hanging when plugging in an add-on card that contains non-SMC format VPD data.
18. Fixed inability to change "Processor Frequency" when setting "Boot performance mode" to "Max Efficient" by AMIBCP.
19. Fixed SUM error when running "GetCurrentBiosCfgTextFile" after BIOS setup modified by AMIBCP.
20. Fixed system hanging at POST 0xB2 when installing Mellanox MCX311a and MCX312C-XCCT.
21. Corrected MECI help string typo.
22. Fixed problem of IPMI boot command being cleared even when persistence boot has been set.
23. Fixed issue of "ChkHddInfoX64.efi -c" command failing.
24. Fixed problem of system resetting or hanging after Watch Dog function is enabled during BIOS update.
25. Fixed issue of SUM TC306 and TC317 failing in certain configuration cases.
26. Fixed issue of power button not being reported in ACPI table when set to "4 Seconds Override".
27. Fixed failure of software automation testing when riser card is plugged in.
28. Fixed issue of Super.rom not working and SUT hanging on POST 79.
29. Fixed failure of BIOS Auto Recovery.
30. Fixed problem of MWAIT being disabled on BSP only.
31. Fixed inability to get correct Memory CECC DIMM location via SD5.
32. Fixed problem of SMBIOS Type4 CPU2 current speed not showing as zero when CPU2 is not installed.
33. Updated FlashDriver module to Label 5 in order to fix inability of system to enter recovery mode when MAIN block is updated 45% and then system powers off.