IPMI Firmware / BIOS Release Notes Form

Product Name	**X10DRW-i(T)**
Release Version	**3.1**
Release Date	**6/7/2018**
Previous Version	**3.0a**
Update Category	**Recommended**
Dependencies	**None**
Important Notes	**None**
Enhancements	**1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.** **2. Changed BIOS version to 3.1.** **3. Updated Broadwell-EP RC 4.4.0 release.** **4. Added support for IPMI IPV6.** **5. Removed unsupported memory frequency options from setup menu.**
New features	**N/A**
Fixes	**1. Patched workaround for QPI error.** **2. Fixed problem of Afu /O command clearing SMC SMBIOS region ($SMC).**

	3. Fixed problem of SUM OOB GetSataInfo always showing "Configuration Type" as "AHCI" when setting "Configure SATA as" to "RAID" or "IDE".
	4. Fixed failure of SMBIOS to change to default even if preserve SMBIOS setup item is set to disabled during recovery.
	5. Set Descriptor Region of BIOS Region Write Access to "No".

3.0a (02/8/2018)

1. Updated SATA RAID OPROM/EFI driver to RSTe PreOS v4.7.0.1014.

2. Updated Intel Server Platform Services 3.1.3.50 for Grantley Refresh Platforms.

3. Updated CPU microcode SRV_B_204 for Haswell-EP C0,1/M0,1/R2 & Broadwell-EP B0/M0/R0.

4. Removed support for using Ctrl+home to trigger recovery.

5. Changed BIOS revision to 3.0a.

6. Changed Memory correctable threshold to 100 and enabled Cloaking for Broadwell CPU E5-26xx SKU.

7. Updated Broadwell-EP RC 4.3.0 PLR10 release.

8. Updated BIOS ACM 3.1.1 PW and SINIT ACM 3.1.1 PW.

9. Added BBS reset function to SmcOobLoadBiosDefault().

10. Changed tCCD_L Relaxation default to 1 and changed string from Enabled to Auto.

11. Appended VPD - VB tag in VPD-W Tag(0x91) area.

12. Added SumBbsSupportFlag into DAT file.

13. Added support for JEDEC NVDIMM.

14. Added support for LSI 3008/3016 HBA sensor.

15. Updated new MRC error log definition.

16. Added Intel SPI vulnerability patch for Grantley.

17. Updated Broadwell-EP B0/M0/R0 CPU uCode MEF406F1_0B000022.

18. Added "SMBIOS Preservation" Enabled/Disabled item for flash recovery.

19. Enhanced SMC Recovery Flash Boot Block feature.

20. Implemented SMC Recovery Flash Boot Block feature.

21. Fixed missing Manufacturer string in SMBIOS type 17 when Ramaxel DIMM is plugged in.

22. Fixed problem of NVDIMM setup items appearing when optimized defaults load even without NVDIMM being installed.

23. Fixed problem of SMBIOS Type4 CPU2 current speed not showing as zero when CPU2 is not installed.

24. Fixed problem of system resetting or hanging after Watch Dog function is enabled during BIOS update.

25. Fixed inability to get correct Memory CECC DIMM location via SD5.