

IPMI Firmware / BIOS Release Notes Form

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| Product Name | X10DRL-LN4 |
| Release Version | 3.1 |
| Release Date | 6/7/2018 |
| Previous Version | 3.0a |
| Update Category | Critical |
| Dependencies | N/A |
| Important Notes | ECO #21051 BIOS Image X10DRLN8.607 Please update BIOS with attached Flash Utility in package. |
| Enhancements | 1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue. 2. Changed BIOS revision to 3.1. 3. Updated Broadwell-EP RC 4.4.0 release. 4. Added support for IPMI IPV6. 5. Removed unsupported memory frequency options from setup menu. |
| New features | N/A |
| Fixes | 1. Fixed problem of Afu /O command clearing SMC SMBIOS region (\$SMC). 2. Fixed problem of SUM OOB GetSataInfo always showing "Configuration Type" as "AHCI" when setting "Configure SATA as" to "RAID" or "IDE". 3. Fixed failure of SMBIOS to change to default even if preserve SMBIOS setup item is set to disabled during recovery. 4. Set Descriptor Region of BIOS Region Write Access to "No". |

BIOS 3.0a (2/8/2018)

1. Updated CPU microcode to address 'Spectre' variant 2 (CVE 2017-5715) security issue.
2. Changed BIOS revision to 3.0a.
3. Corrected POST diagnostic signOn string.
4. Patched for Micron Z11C DIMM to improve the stability.
5. Changed tCCD_L Relaxation default to 1 and changed string from Enabled to Auto.
6. Appended VPD - VB tag in VPD-W Tag(0x91) area.
7. Updated new MRC error log definition.
8. Updated Intel Server Platform Services 3.1.3.38 PLR7 for Grantley Refresh Platforms.
9. Added Intel SPI vulnerability patch for Grantley.
10. Updated Broadwell-EP B0/M0/R0 CPU uCode MEF406F1_0B000022.
11. Added "SMBIOS Preservation" Enabled/Disabled item for flash recovery.
12. Enhanced SMC Recovery Flash Boot Block feature.
13. Updated SATA RAID OPROM/EFI driver to RSTe PreOS v4.7.0.1014.
14. Updated Intel Server Platform Services 3.1.3.50 for Grantley Refresh Platforms.
15. Removed support for using Ctrl+home to trigger recovery.
16. Updated Broadwell-EP RC 4.3.0 PLR10 release.
17. Changed Memory correctable threshold to 100 and enabled Cloaking for Broadwell CPU E5-26xx SKU.
18. Displayed setup item "ACS Control" to Enable/Disable PCIe Access Control Services Extended Capability.
19. Added BBS reset function to SmcOobLoadBiosDefault().
20. Added SumBbsSupportFlag into DAT file.
21. Added support for JEDEC NVDIMM.
22. Added LSI 3008/3016 HBA sensor support.
23. Implemented SMC Recovery Flash Boot Block feature.
24. Fixed issue of SUM TC306 and TC317 failing in certain configuration cases.
25. Fixed problem of system hanging after Watch Dog function is enabled and then BIOS is updated.
26. Fixed missing Manufacturer string in SMBIOS type 17 when Ramaxel DIMM is plugged in.
27. Fixed problem of NVDIMM setup items appearing when optimized defaults load even without NVDIMM being installed.
28. Fixed problem of system hanging on POST code 0xB2.
29. Fixed problem of system resetting or hanging after Watch Dog function is enabled and then BIOS is updated.
30. Fixed inability to get correct Memory CECC DIMM location via SD5.
31. Fixed inability to log Patrol Scrub UCE.
32. Fixed problem of system hanging at 79 with some NVDIMMs.
33. Fixed problem of incorrect PCI slot number showing on Legacy/UEFI mode.