

IPMI Firmware / BIOS Release Notes Form

Supermicro disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade. All products, computer systems, dates, and figures specified are preliminary based on current expectations, and are subject to change without notice. Supermicro and the Supermicro logo are trademarks of Super Micro Computer, Inc. in the U.S. and/or other countries. Copyright © 2018 Super Micro Computer, Inc. All rights reserved.

Product Name	X10DRC, I - LN4+ / T4+
Release Version	3.1
Release Date	6/8/2018
Previous Version	3.0a
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue. 2. Changed BIOS revision to 3.1. 3. Added support for IPMI IPV6. 4. Removed unsupported memory frequency options from setup menu. 5. Updated Broadwell-EP RC 4.4.0 release.
New features	N/A
Fixes	1. Fixed problem of Afu /O command clearing SMC SMBIOS region (\$SMC). 2. Fixed problem of SUM OOB GetSataInfo always showing "Configuration Type" as "AHCI" when setting "Configure SATA as" to

	<p>"RAID" or "IDE".</p> <p>3. Fixed failure of SMBIOS to change to default even if preserve SMBIOS setup item is set to disabled during recovery.</p>
--	---

Release Notes from Previous Release(s)

3.0a (2/9/2018)

1. Implemented enhancement to address 'Spectre' variant 2 (CVE 2017-5715) security patch issue.
2. Changed BIOS revision to 3.0a.
3. Updated Intel Server Platform Services 3.1.3.50 for Grantley Refresh Platforms.
4. Updated SATA RAID OPRM/EFI driver to RSTe PreOS v4.7.0.1014 (RSTe SATA 4.7.0.1069 and NVMe 4.7.0.2063).
5. Removed support for using Ctrl+home to trigger recovery.
6. Added support for UEFI mode PXE boot via F12 hot key Net boot.
7. Fixed inability of SUM utility to get "Setup Prompt Timeout" setup item.
8. Updated Broadwell-EP RC 4.3.0 PLR11 release.
9. Displayed setup item "ACS Control" to Enable/Disable PCIe Access Control Services Extended Capability.
10. Updated BIOS ACM 3.1.1 PW and SINIT ACM 3.1.1 PW.
11. Added tCCD_L Relaxation item under Memory Configuration menu.
12. Added SumBbsSupportFlag into DAT file.
13. Added Intel SPI vulnerability patch for Grantley.
14. Added "SMBIOS Preservation" Enabled/Disabled item for flash recovery.
15. Enhanced SMC Recovery Flash Boot Block feature.
16. Added support for JEDEC NVDIMM.
17. Implemented SMC Recovery Flash Boot Block feature.
18. Updated FlashDriver module to Label 5 in order to fix inability of system to enter recovery mode when MAIN block is updated 45% and then system powers off.
19. Added Ramaxel JEDEC ID to support Ramaxel memory.
20. Fixed problem of NVDIMM setup items appearing when optimized defaults load even without NVDIMM being installed.
21. Corrected POST diagnostic signOn string.
22. Fixed problem of SMBIOS Type4 CPU2 current speed not showing as zero when CPU2 is not installed.
23. Fixed problem of system resetting or hanging after Watch Dog function is enabled during BIOS update.
24. Fixed problem of MWAIT being disabled on BSP only.
25. Fixed inability to get correct Memory CECC DIMM location via SD5.